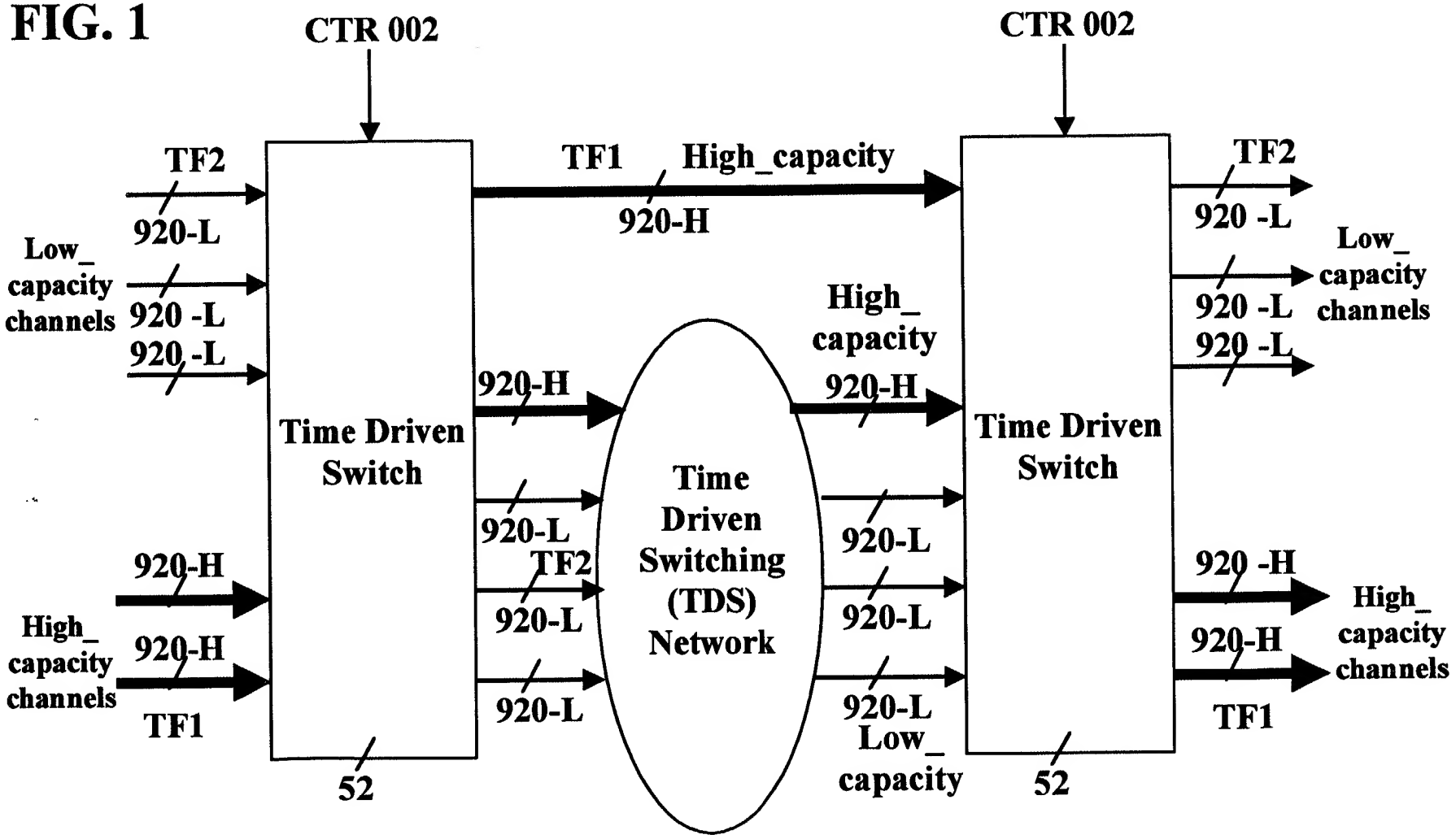
FIG. 1



c = High_capacity/Low_capacity

FIG. 2

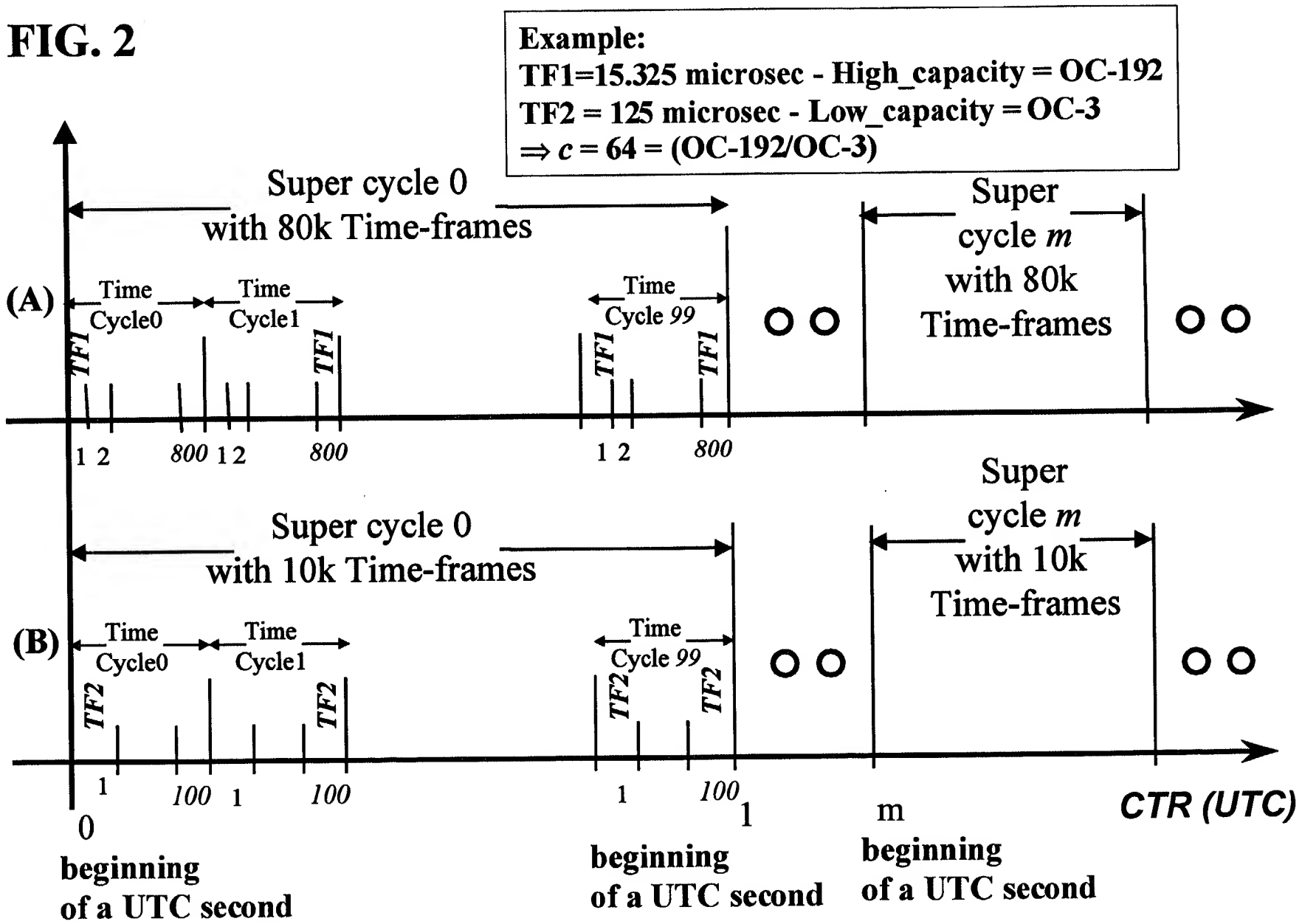

Example:
TF1=15.325 microsec - High_capacity = OC-192
TF2 = 125 microsec - Low_capacity = OC-3
⇒ c = 64 = (OC-192/OC-3)
Super cycle 0
with 80k Time-frames
Super
cycle m
with 80k
Time-frames
(A)
Time
Cycle0
Time
Cycle1
Time
Cycle 99
TF1
1 2
800
Super cycle 0
with 10k Time-frames
Super
cycle m
with 10k
Time-frames
(B)
TF2
1
100
0
1
m
CTR (UTC)
beginning
of a UTC second

FIG. 3

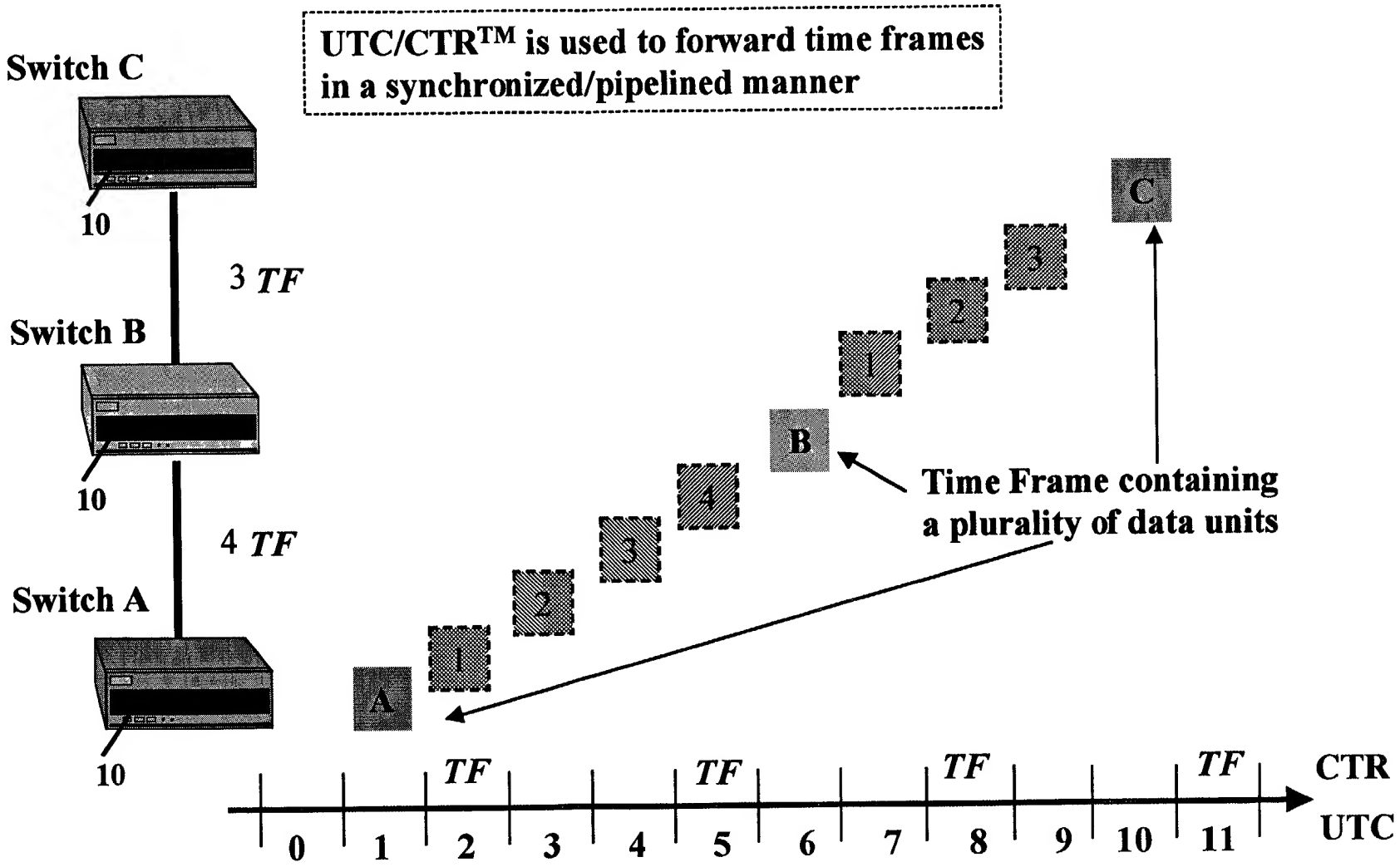


FIG. 4

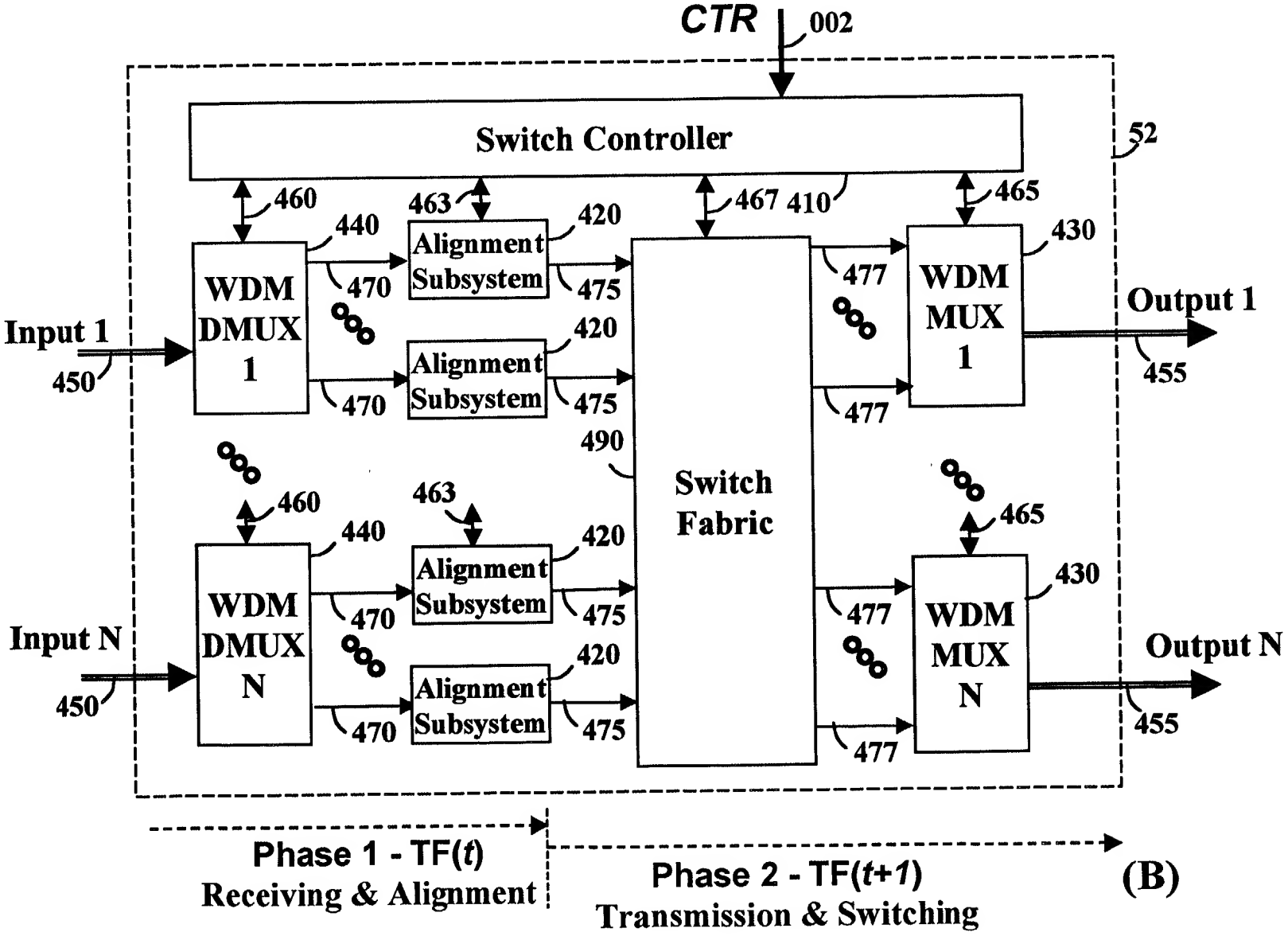


FIG. 5

Two time intervals: SC1_length·TF1 = 1 UTC second

- SC2_length·TF2 = 1 UTC second
- TF2 = (SC1_length / SC2_length) · TF1 = k · TF1, where the time cycles of TF1 and TF2 are aligned with respect to UTC.

For $k = 2$ and $c = 4$ (e.g., High_capacity=OC-192, Low_capacity=OC-48):

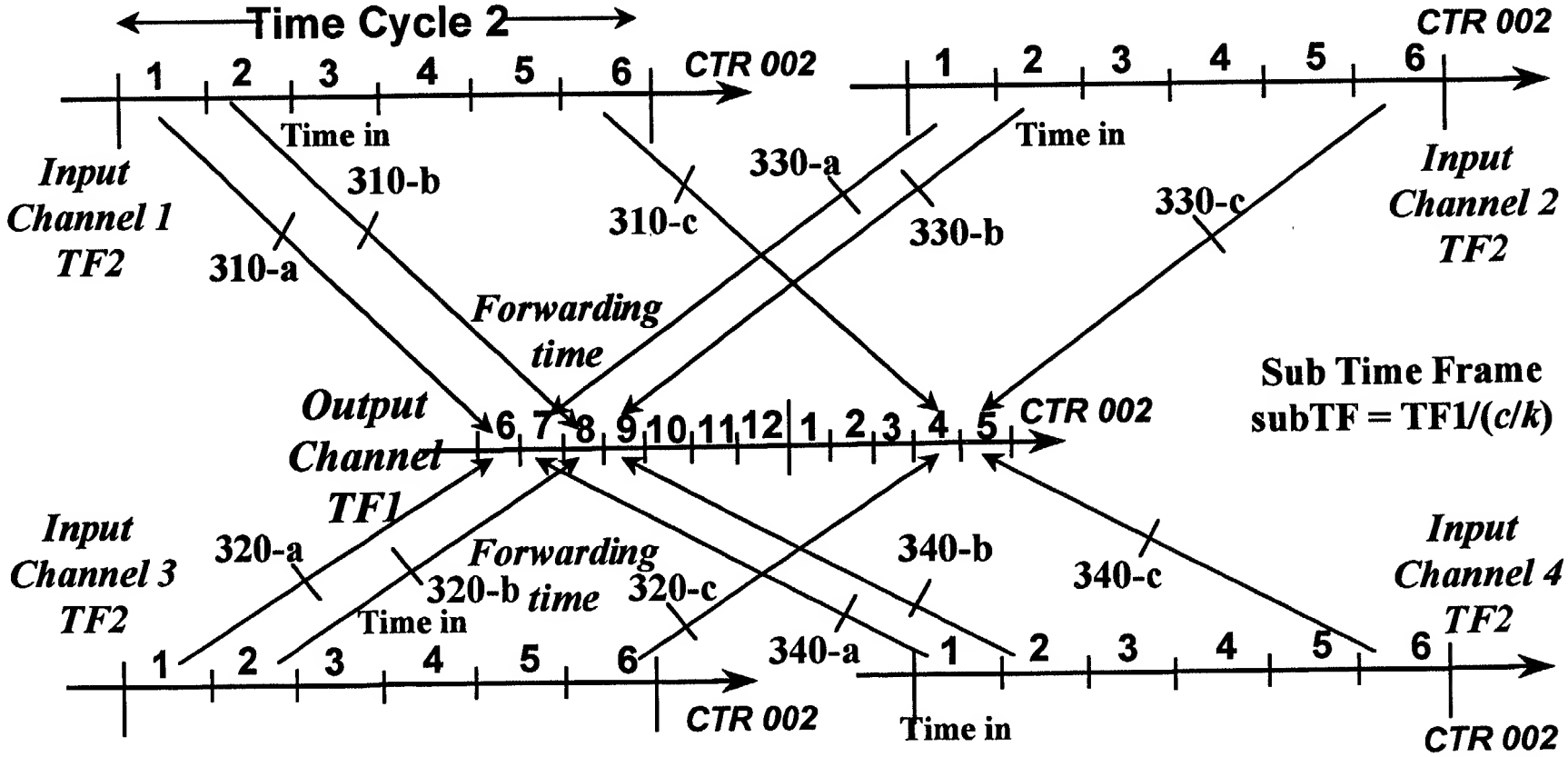


FIG. 6

Two time intervals: ***SC1_length·TF1 = 1*** **UTC second**

- ***SC2_length·TF2 = 1*** **UTC second**
- ***TF2 = (SC1_length / SC2_length) · TF1 = k · TF1***, where the time cycles of ***TF1*** and ***TF2*** are aligned with respect to UTC.

For $k = 2$ and $c = 4$ (e.g., High_capacity=OC-192, Low_capacity=OC-48):

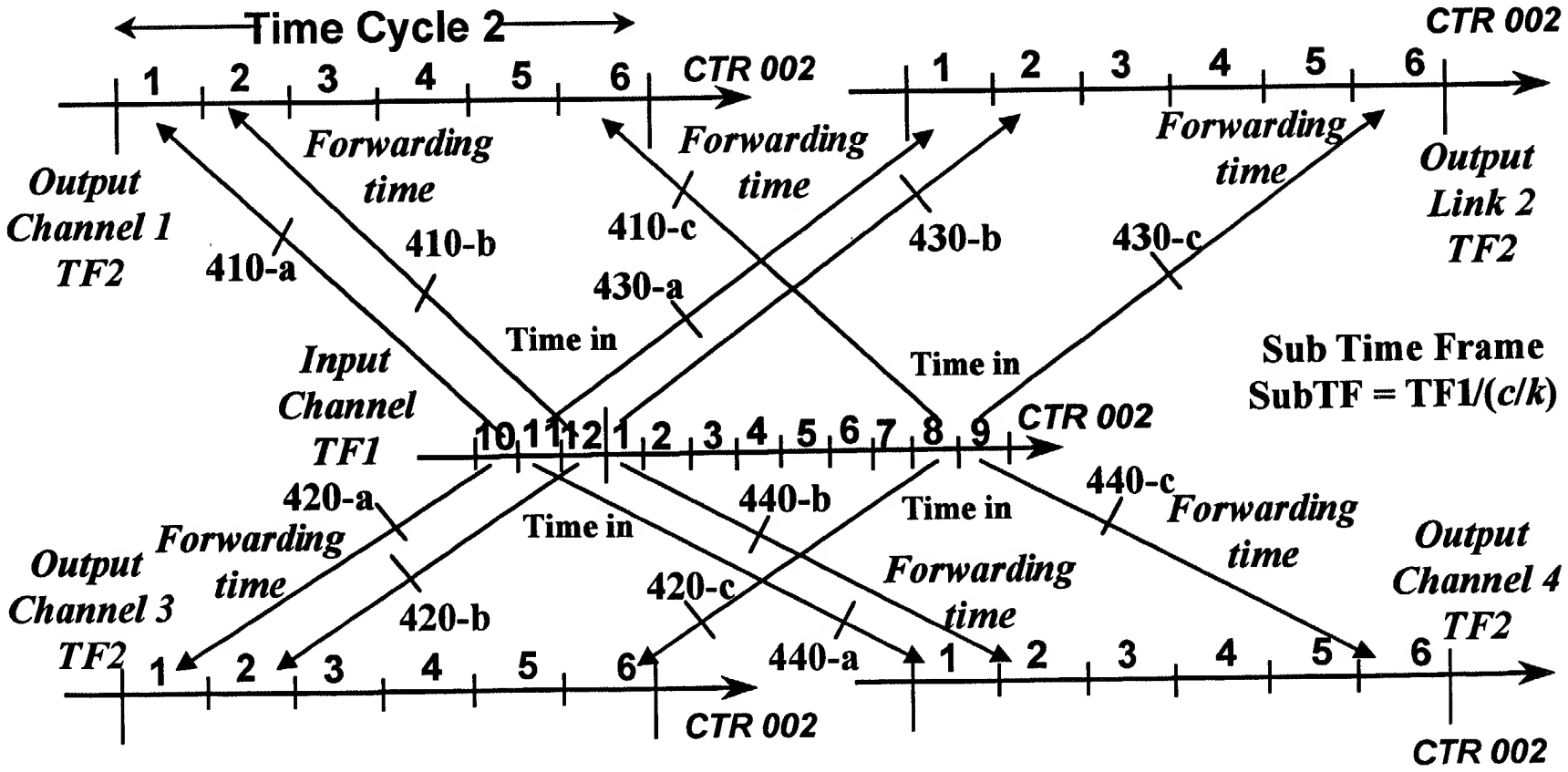


FIG. 7

Two time intervals: *SC1_length·TF1 = 1* UTC second

- ***SC2_length·TF2 = 1*** **UTC second**
- ***TF2 = (SC1_length / SC2_length) · TF1 = k · TF1*****, where the time cycles of *TF1* and *TF2* are aligned with respect to UTC.**
  For *k* = 2 and *c* = 4 (e.g., High_capacity=OC-192, Low_capacity=OC-48):

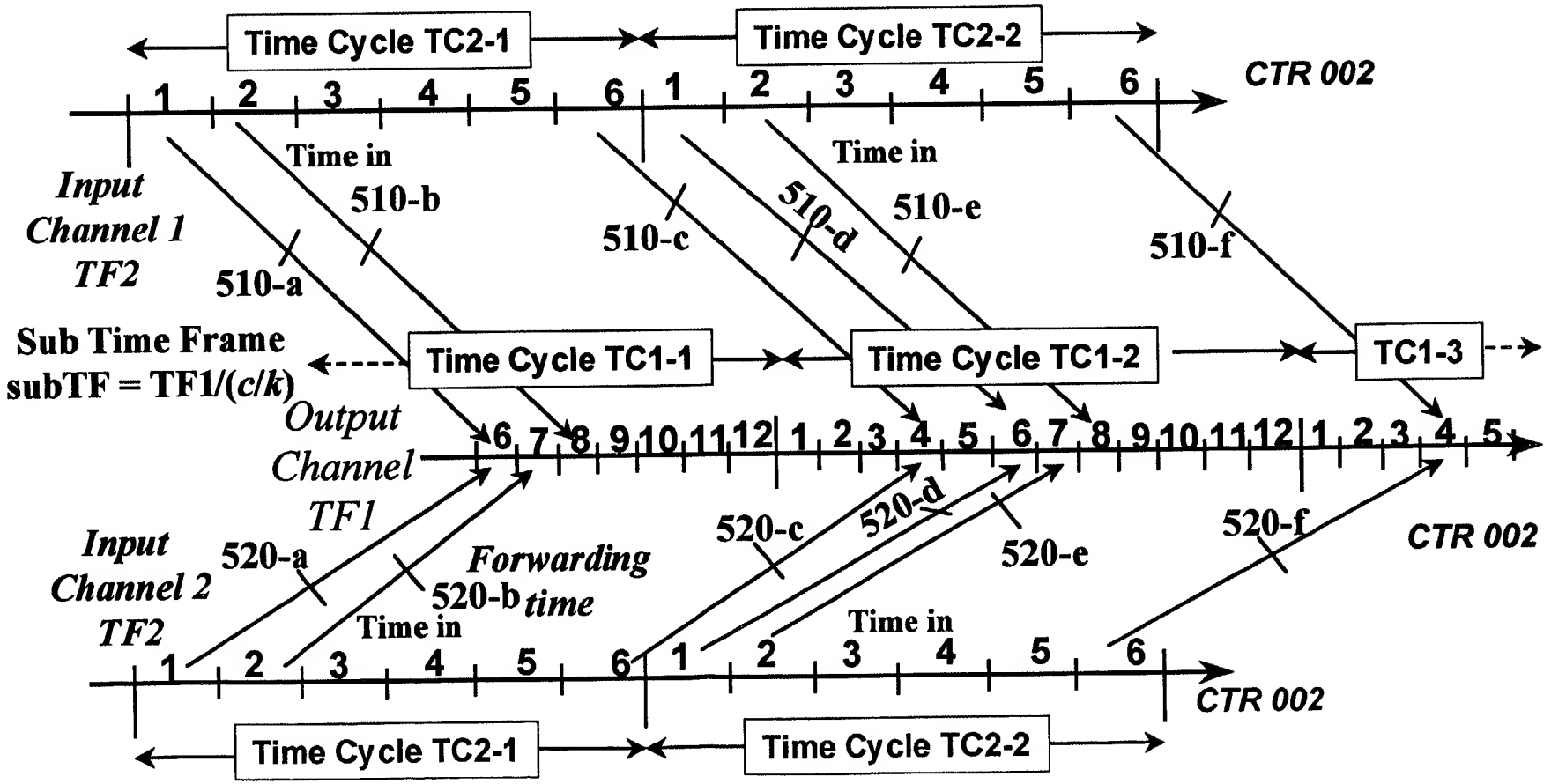


FIG. 8

Two time intervals: *SC1_length·TF1 = 1* UTC second

- *SC2_length·TF2 = 1* UTC second
- *TF2 = (SC1_length / SC2_length) · TF1 = k · TF1*, where the time cycles of *TF1* and *TF2* are aligned with respect to UTC.

For $k = 2$ and $c = 4$ (e.g., High_capacity=OC-192, Low_capacity=OC-48):

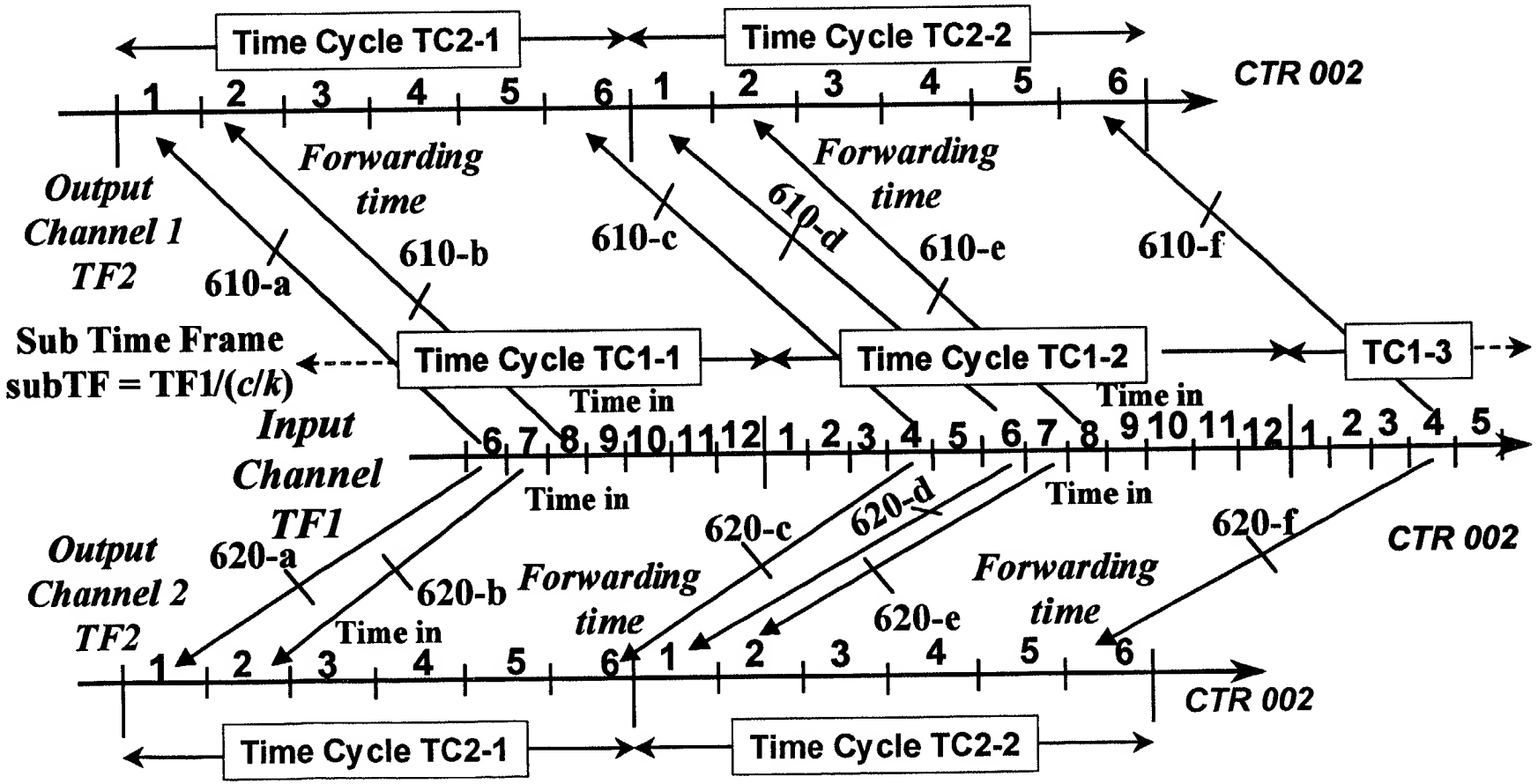


FIG. 9

c=4, e.g., OC-192/OC-48
k=2, e.g., 25 microsec/12.5 microsec

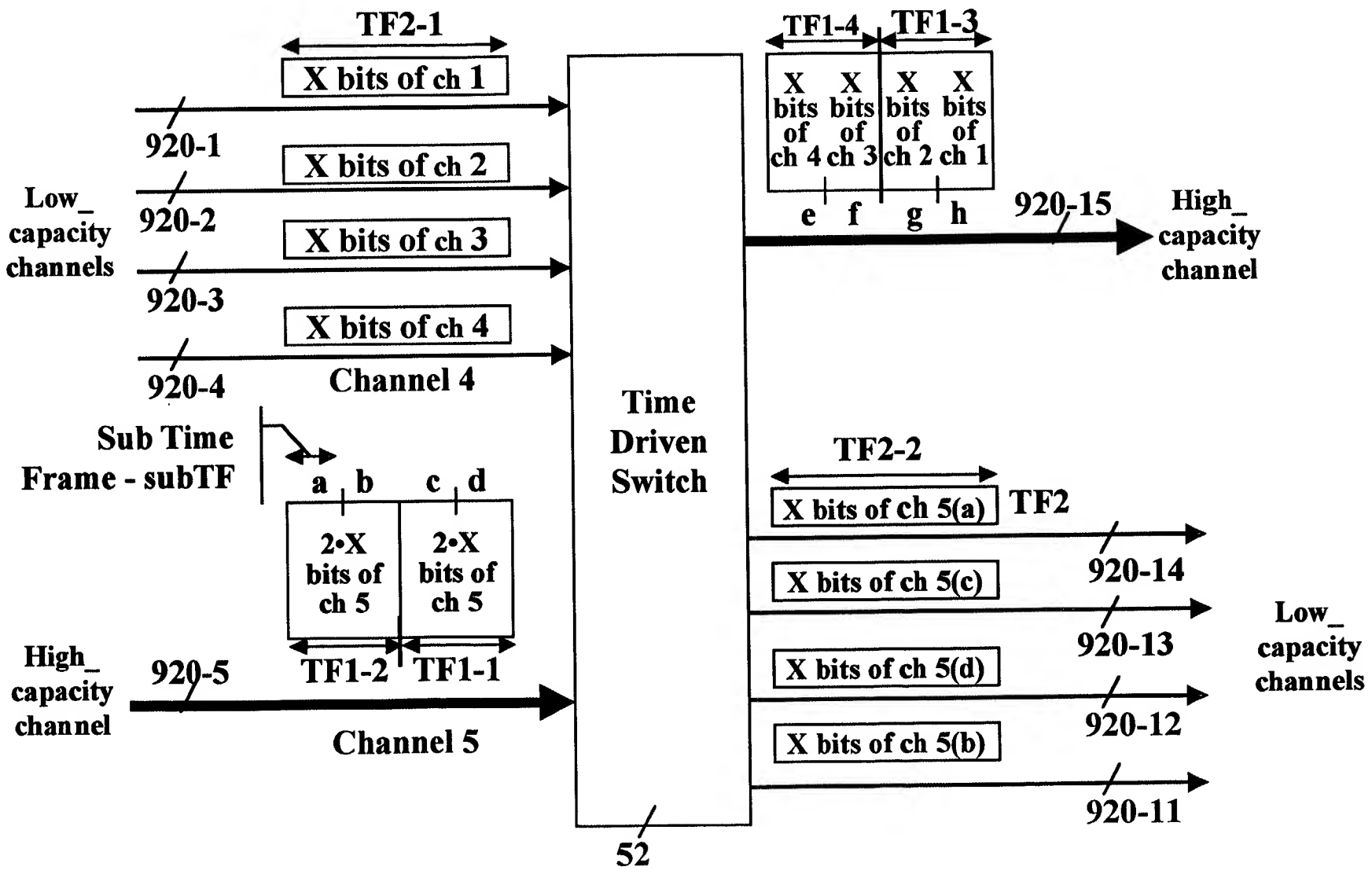


FIG. 10

c=4, e.g., OC-192/OC-48
k=2, e.g., 25 microsec/12.5 microsec

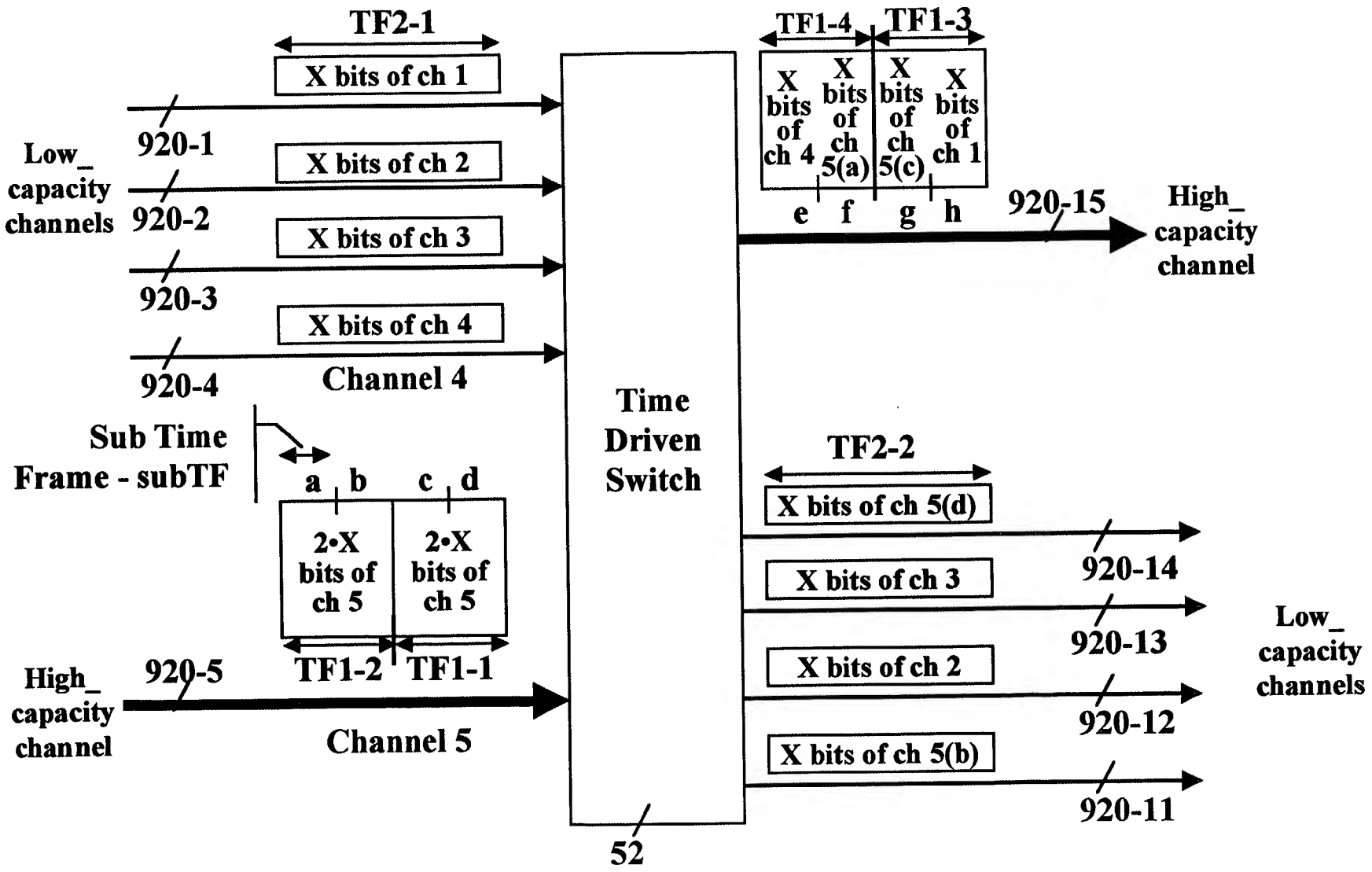


FIG. 11

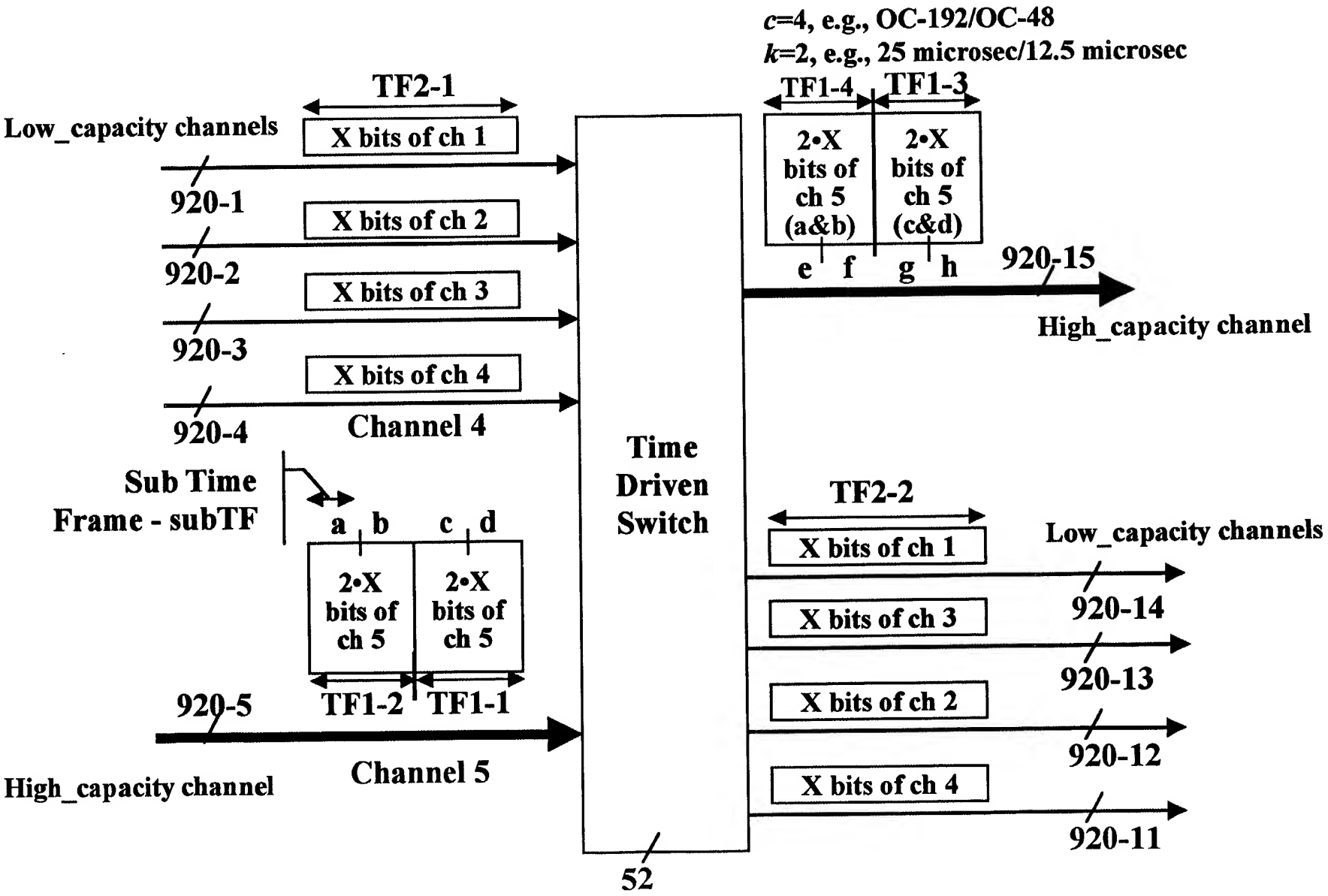


FIG. 12

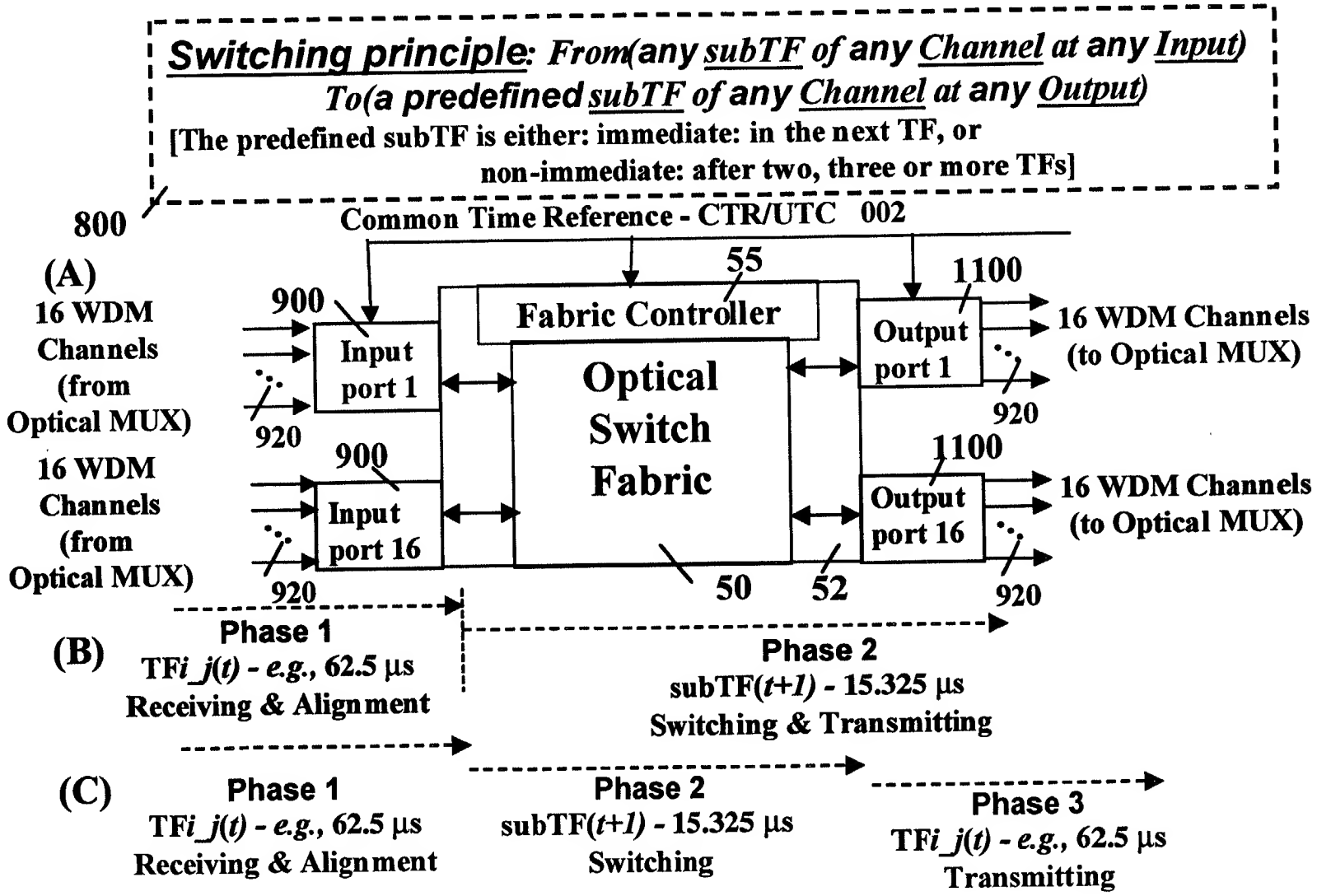

Switching principle: From(any subTF of any Channel at any Input)
To(a predefined subTF of any Channel at any Output)
[The predefined subTF is either: immediate: in the next TF, or
non-immediate: after two, three or more TFs]
800
Common Time Reference - CTR/UTC 002
(A)
16 WDM Channels (from Optical MUX)
16 WDM Channels (from Optical MUX)
900
900
Input port 1
Input port 16
920
920
55
Fabric Controller
Optical Switch Fabric
50
52
1100
1100
Output port 1
Output port 16
920
920
16 WDM Channels (to Optical MUX)
16 WDM Channels (to Optical MUX)
(B)
Phase 1
TFi_j(t) - e.g., 62.5 µs
Receiving & Alignment
Phase 2
subTF(t+1) - 15.325 µs
Switching & Transmitting
(C)
Phase 1
TFi_j(t) - e.g., 62.5 µs
Receiving & Alignment
Phase 2
subTF(t+1) - 15.325 µs
Switching
Phase 3
TFi_j(t) - e.g., 62.5 µs
Transmitting

FIG. 13

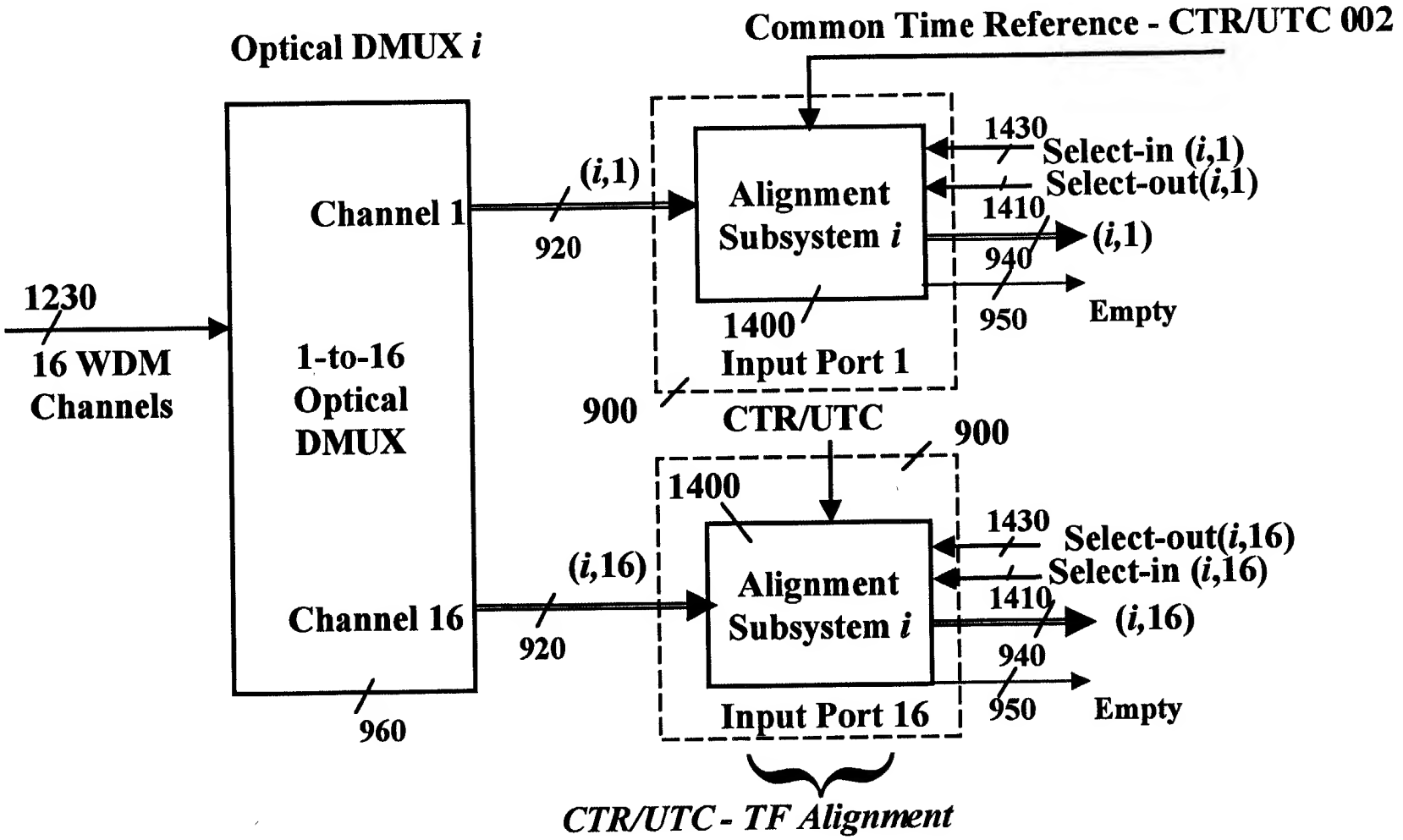


FIG. 14

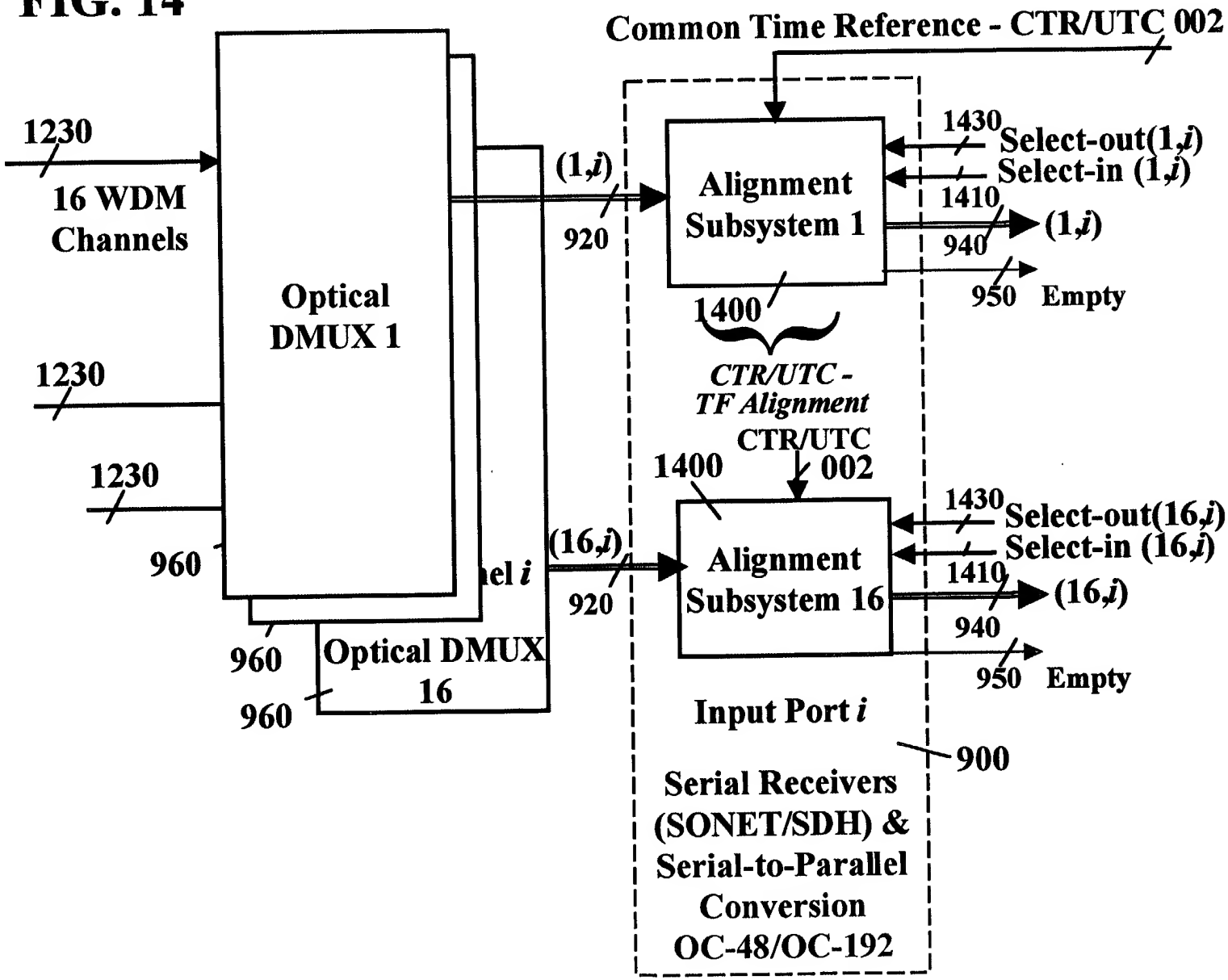


FIG. 15

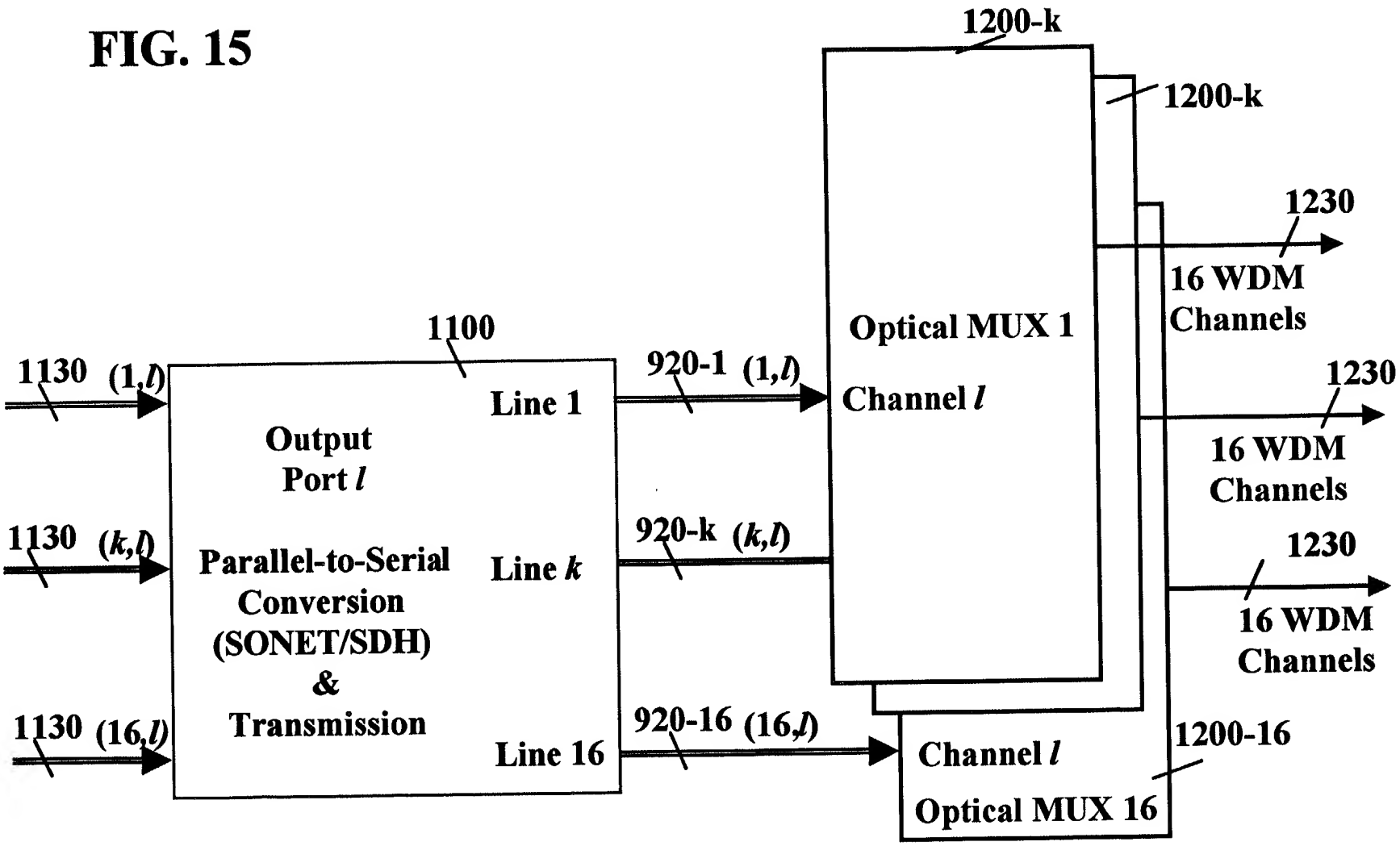


FIG. 16

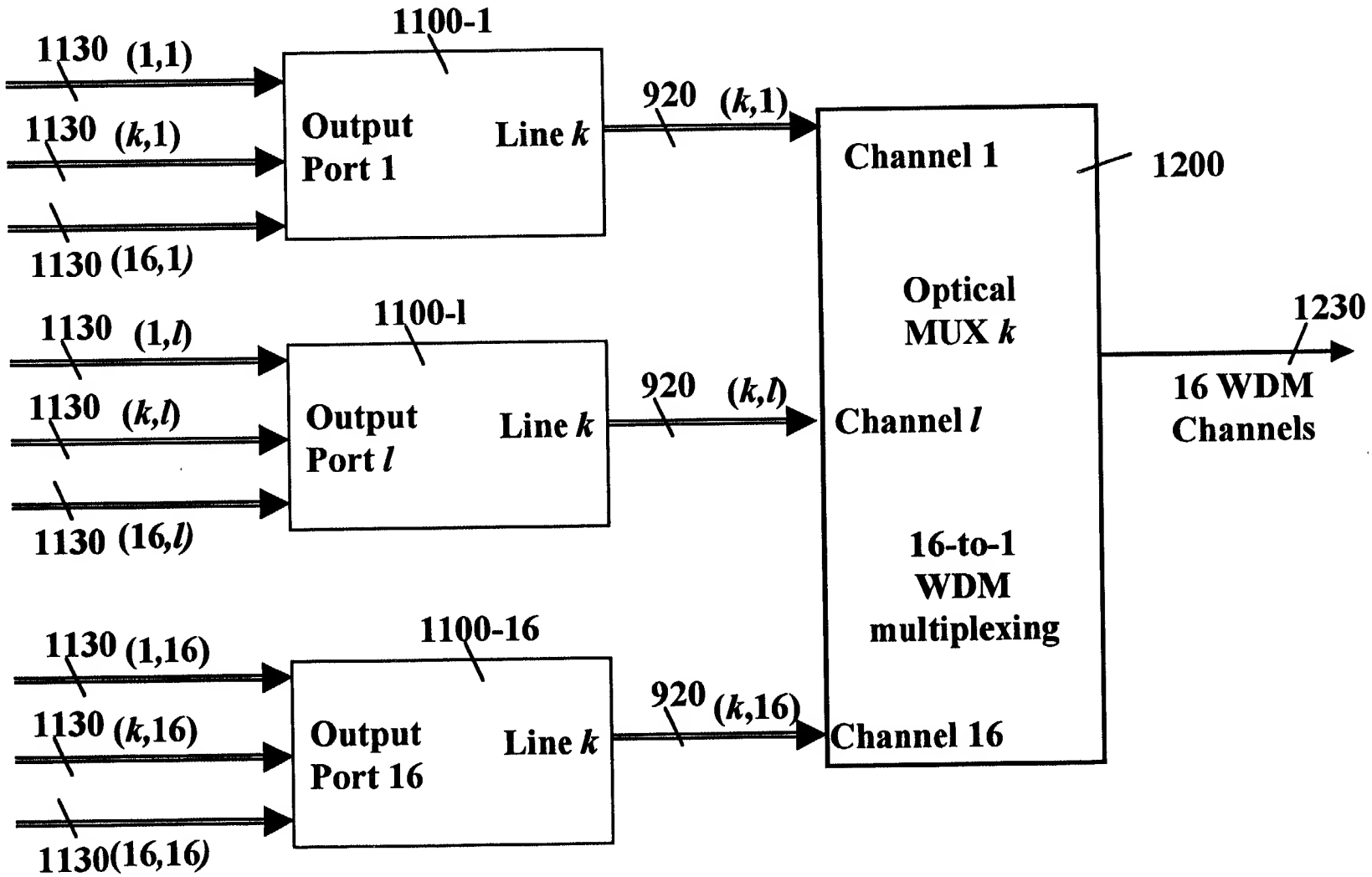

1100-1
1130 (1,1)
1130 (k,1)
1130 (16,1)
Output Port 1
Line k
920 (k,1)
Channel 1
1200
Optical MUX k
1230
16 WDM Channels
1100-l
1130 (1,l)
1130 (k,l)
1130 (16,l)
Output Port l
Line k
920 (k,l)
Channel l
16-to-1 WDM multiplexing
1100-16
1130 (1,16)
1130 (k,16)
1130(16,16)
Output Port 16
Line k
920 (k,16)
Channel 16

FIG. 17

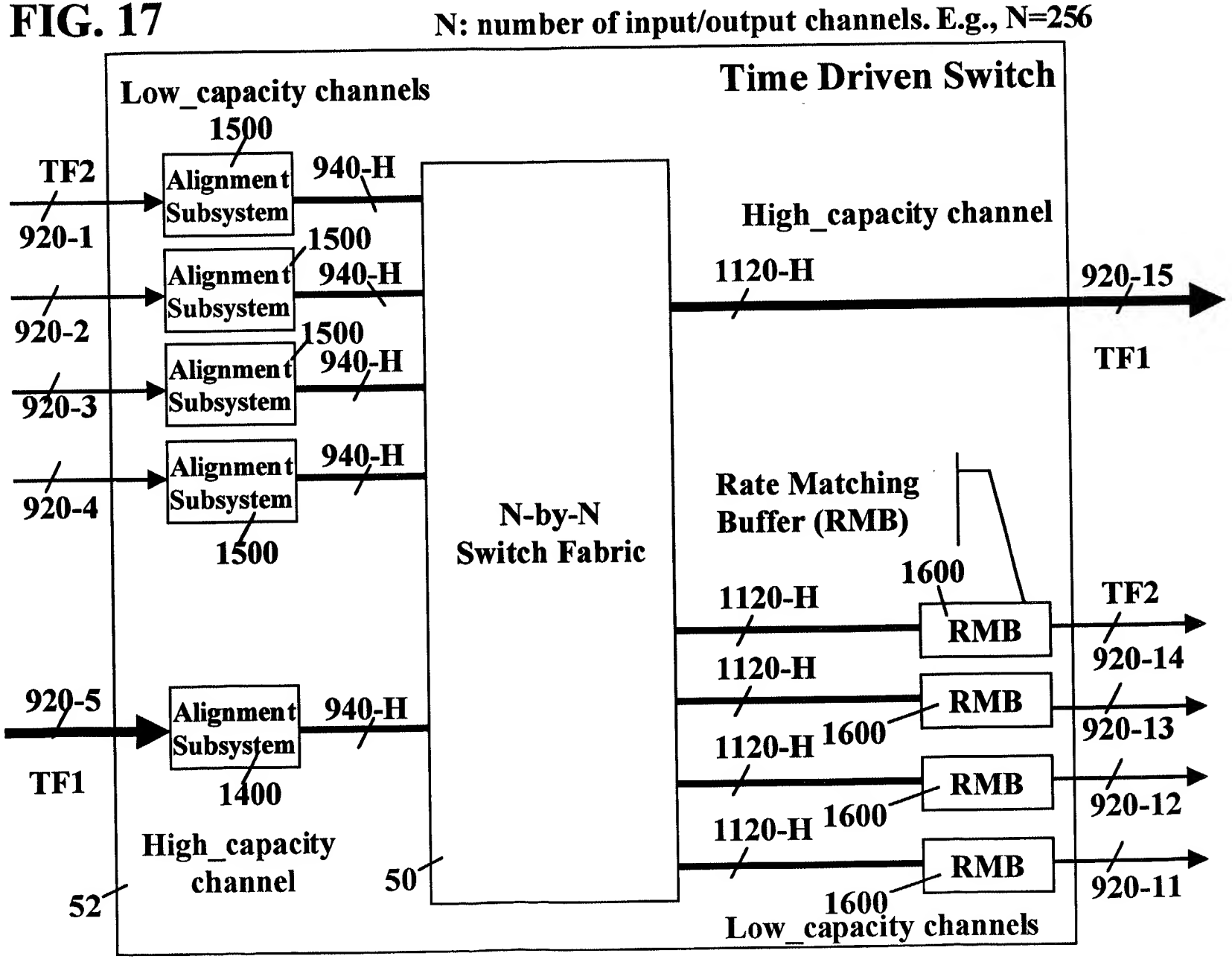


FIG. 18

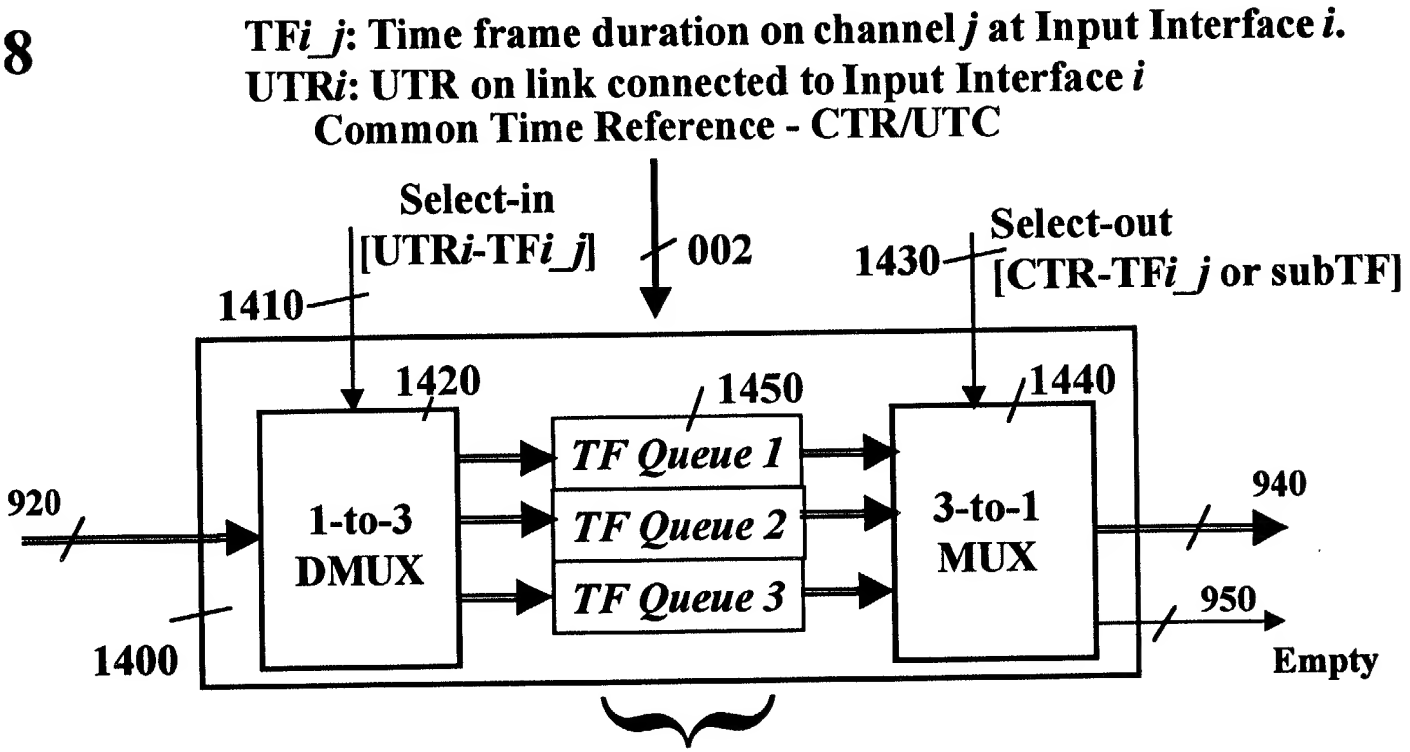
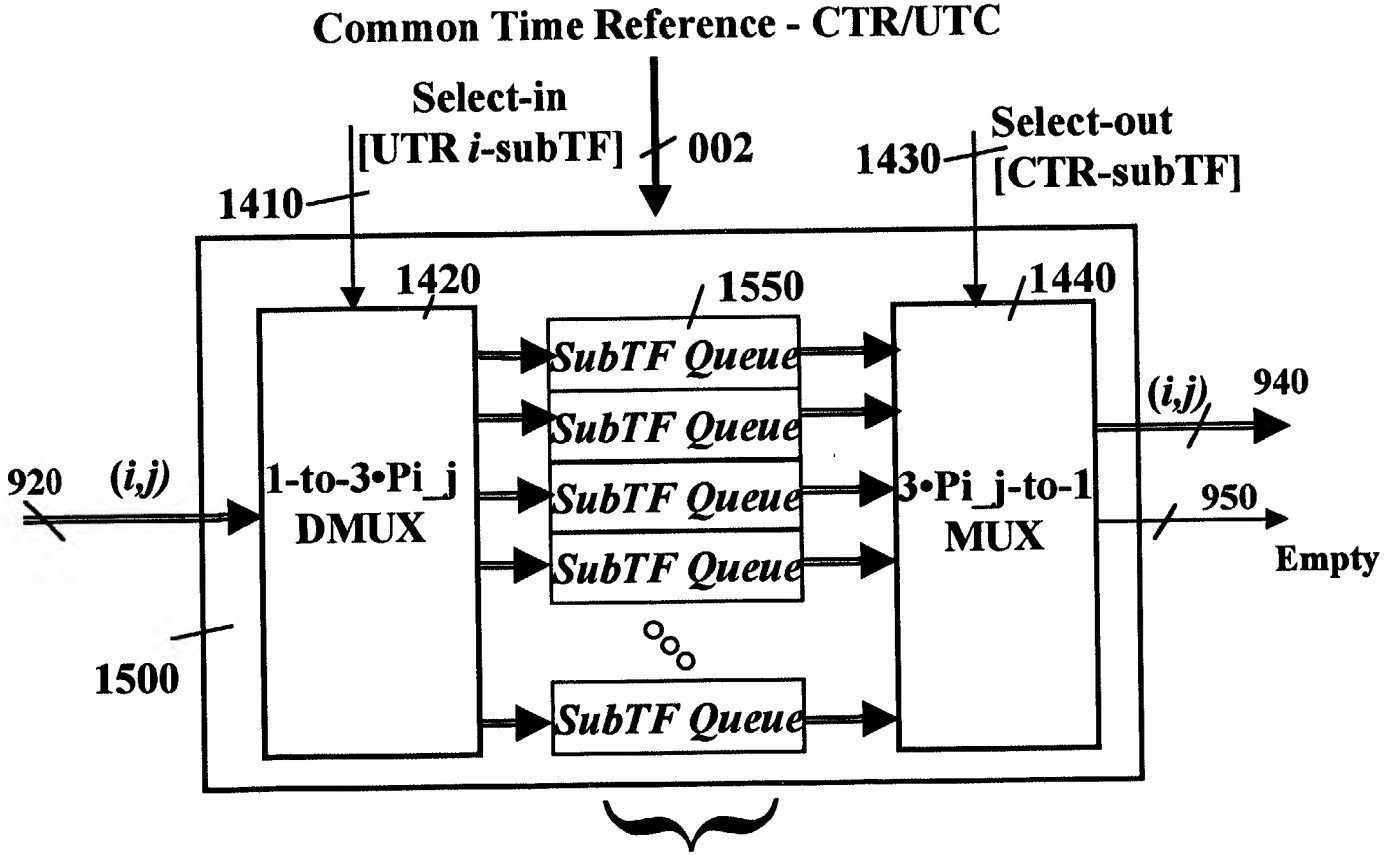

TFi_j: Time frame duration on channel j at Input Interface i.
UTRi: UTR on link connected to Input Interface i
Common Time Reference - CTR/UTC
Select-in
[UTRi-TFi_j]
002
Select-out
[CTR-TFi_j or subTF]
1410
1420
1450
1430
1440
920
1-to-3
DMUX
TF Queue 1
TF Queue 2
TF Queue 3
3-to-1
MUX
940
950
Empty
1400
Alignment Subsystem for Channel j at Input Interface i
with a Plurality of Time Frame Queues

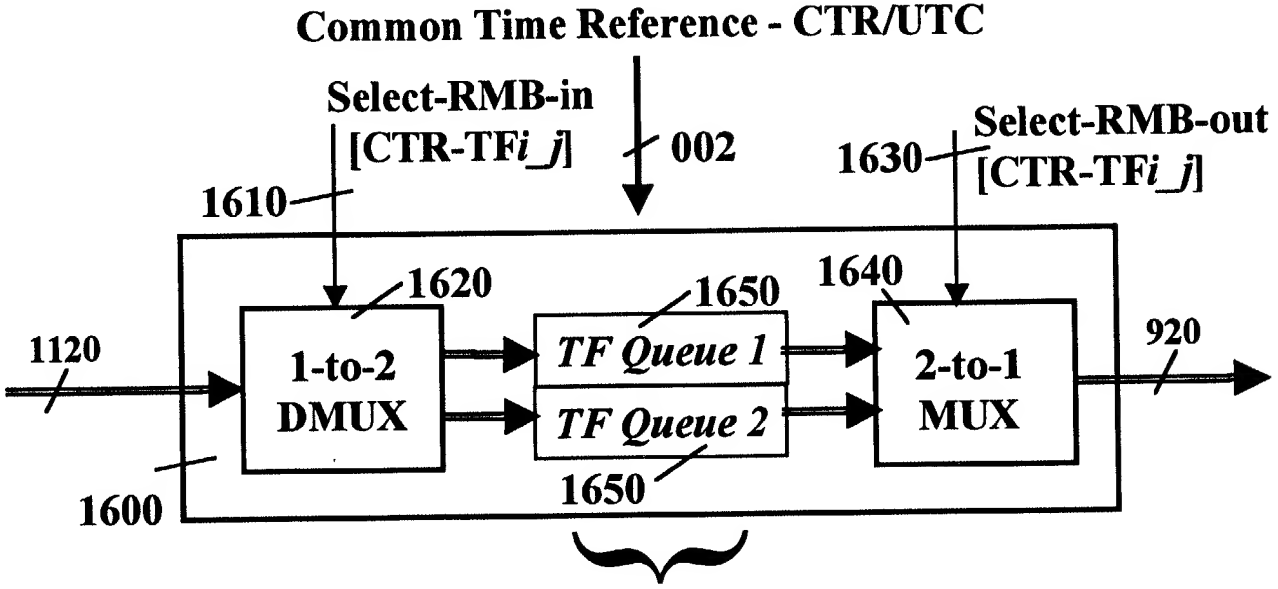
FIG. 19

TF*i_j*: Time frame duration on channel *j* at Input Interface *i*.
UTR *i*: UTR on link connected to Input Interface *i*
P*i_j* = TF*i_j*/subTF



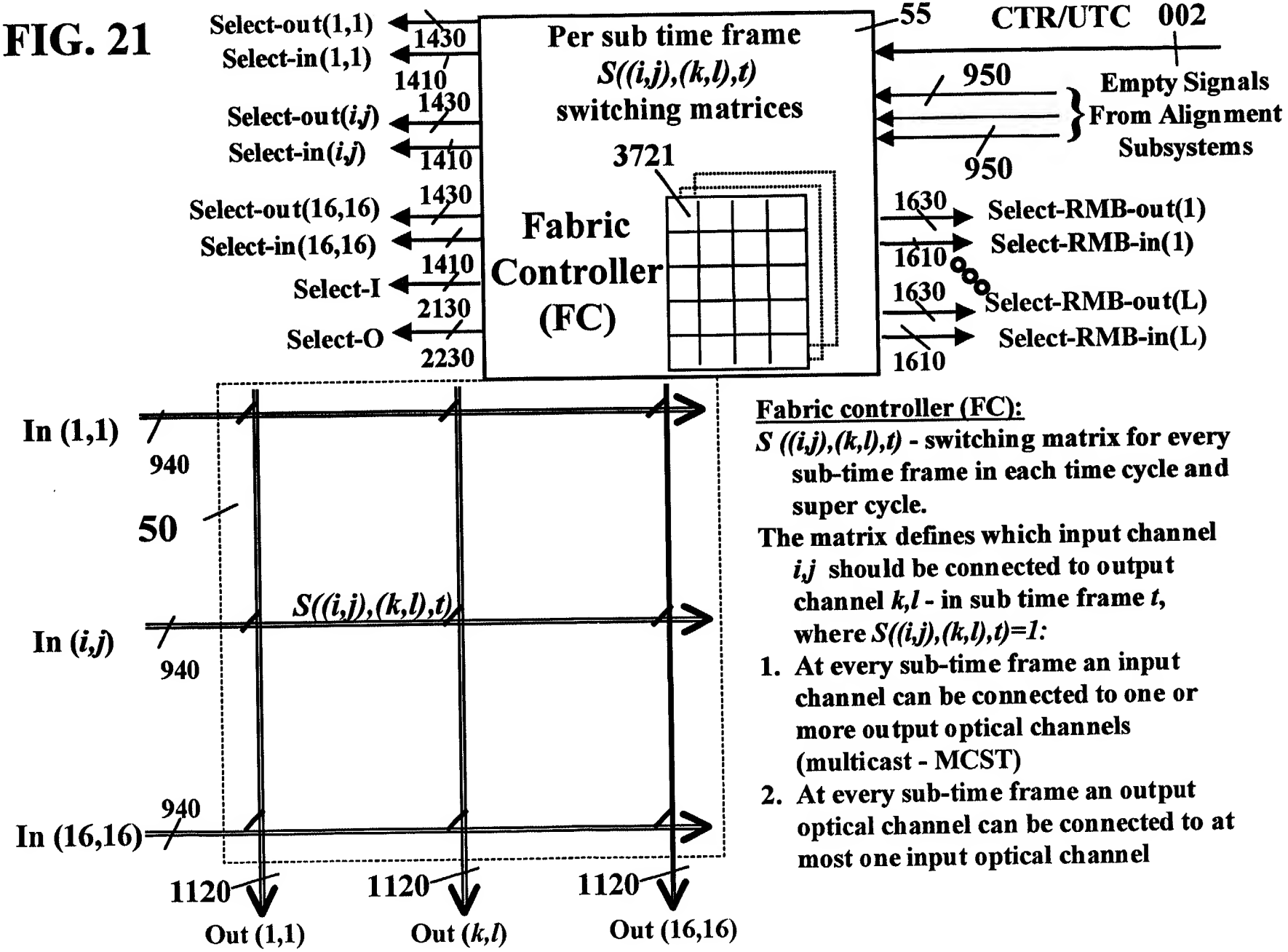
Alignment Subsystem for *high capacity* Channel *j* at Input Interface *i*
with a Plurality of Sub-Time Frame Queues

FIG. 18+2 TF*i_j*: Time frame duration on channel *j* at Input Interface *i*.
UTR*i*: UTR on link connected to Input Interface *i*



Rate Matching Buffer for Channel *j* at Output Interface *i*
with a Plurality of Time Frame Queues
(Also single buffer with dual access memory with single phase switching and forwarding)

FIG. 21



Fabric controller (FC):

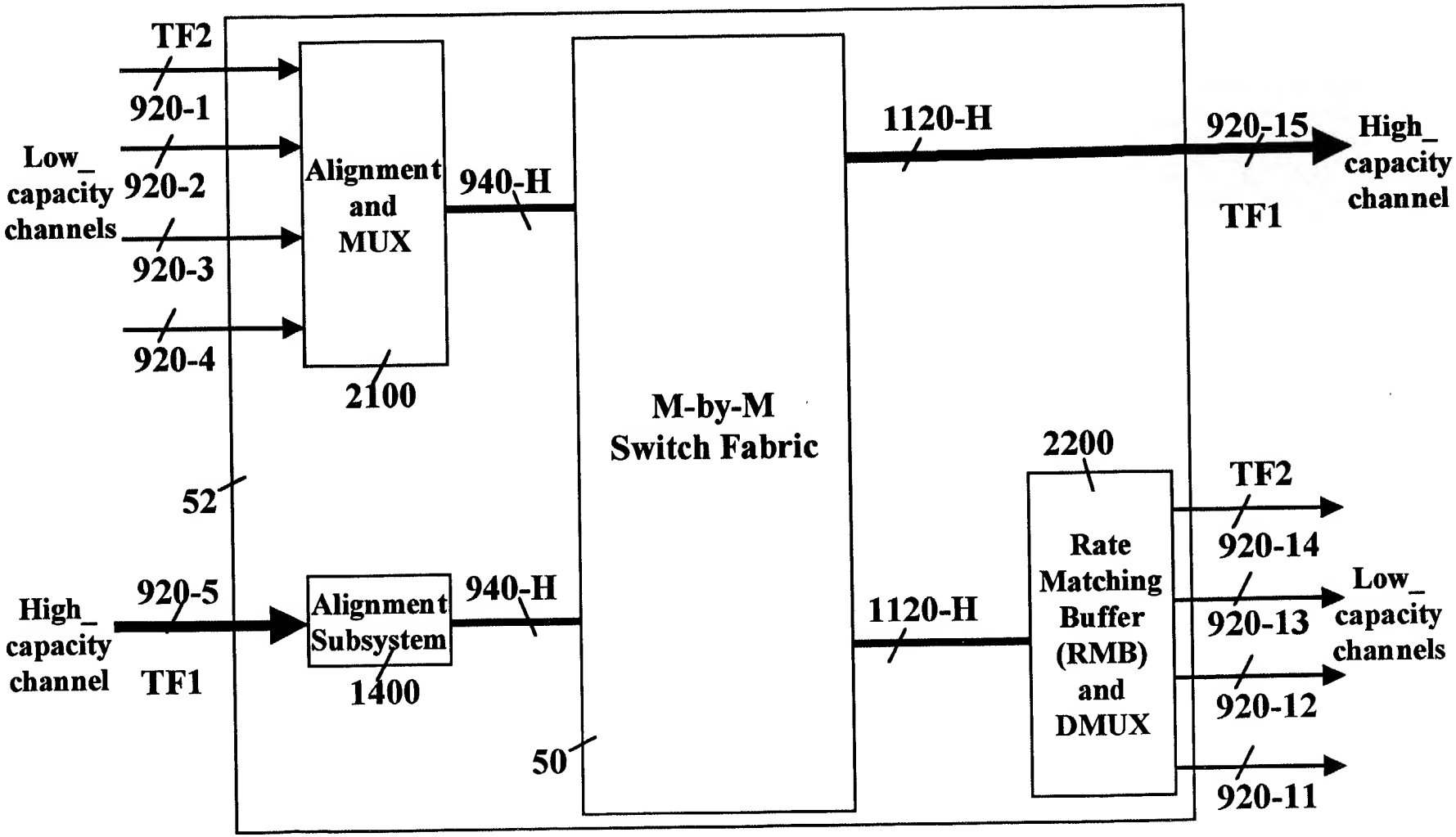
S ((i,j),(k,l),t) **- switching matrix for every sub-time frame in each time cycle and super cycle.**

The matrix defines which input channel *i,j* should be connected to output channel *k,l* - in sub time frame *t*, where *S((i,j),(k,l),t)=1:*

1. **At every sub-time frame an input channel can be connected to one or more output optical channels (multicast - MCST)**
2. **At every sub-time frame an output optical channel can be connected to at most one input optical channel**

FIG. 22

N: number of input/output channels. E.g., N=256
M • High_capacity = N_high • High_capacity + N_low • Low_capacity
M<N



Time Driven Switch

FIG. 23

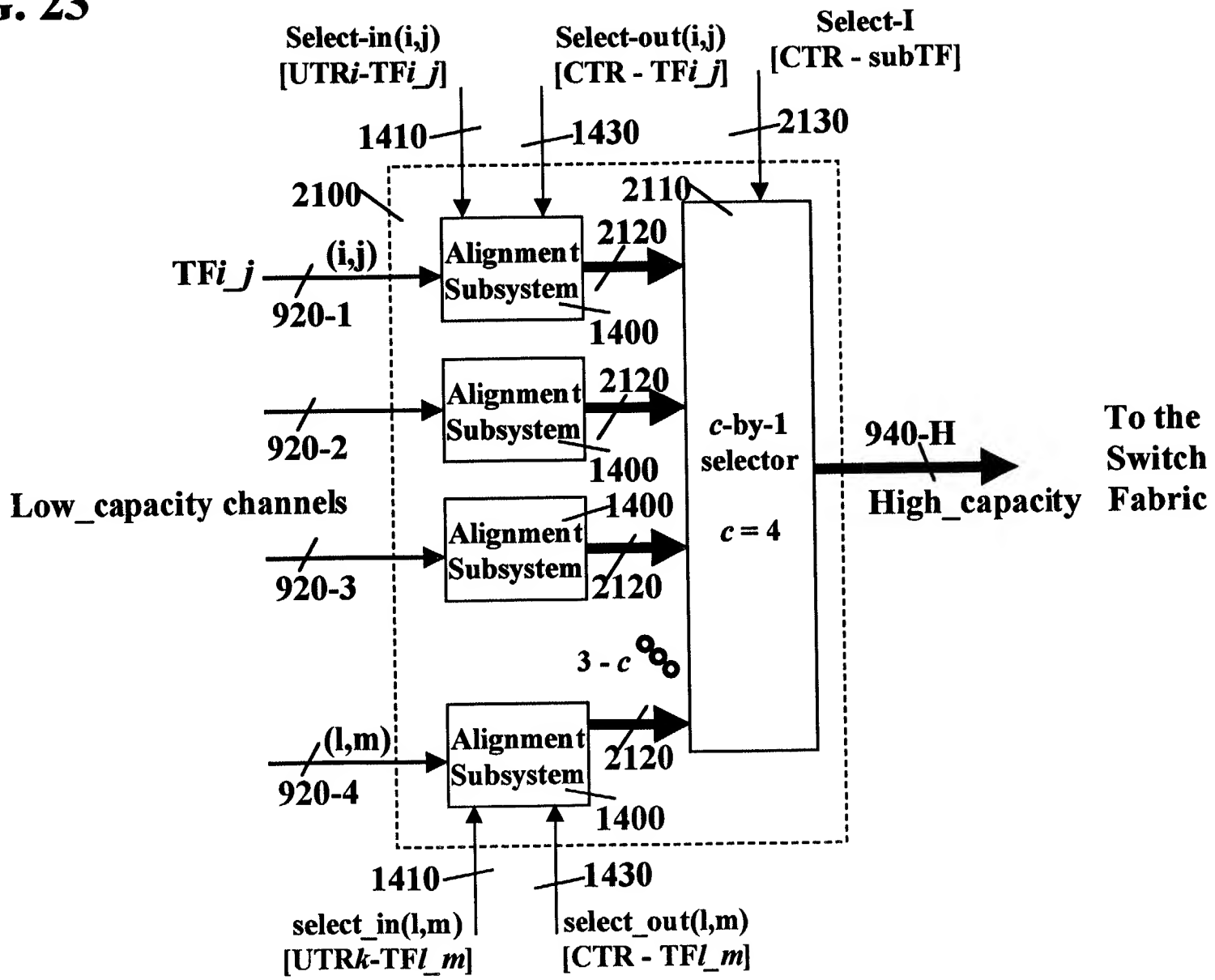


FIG. 24

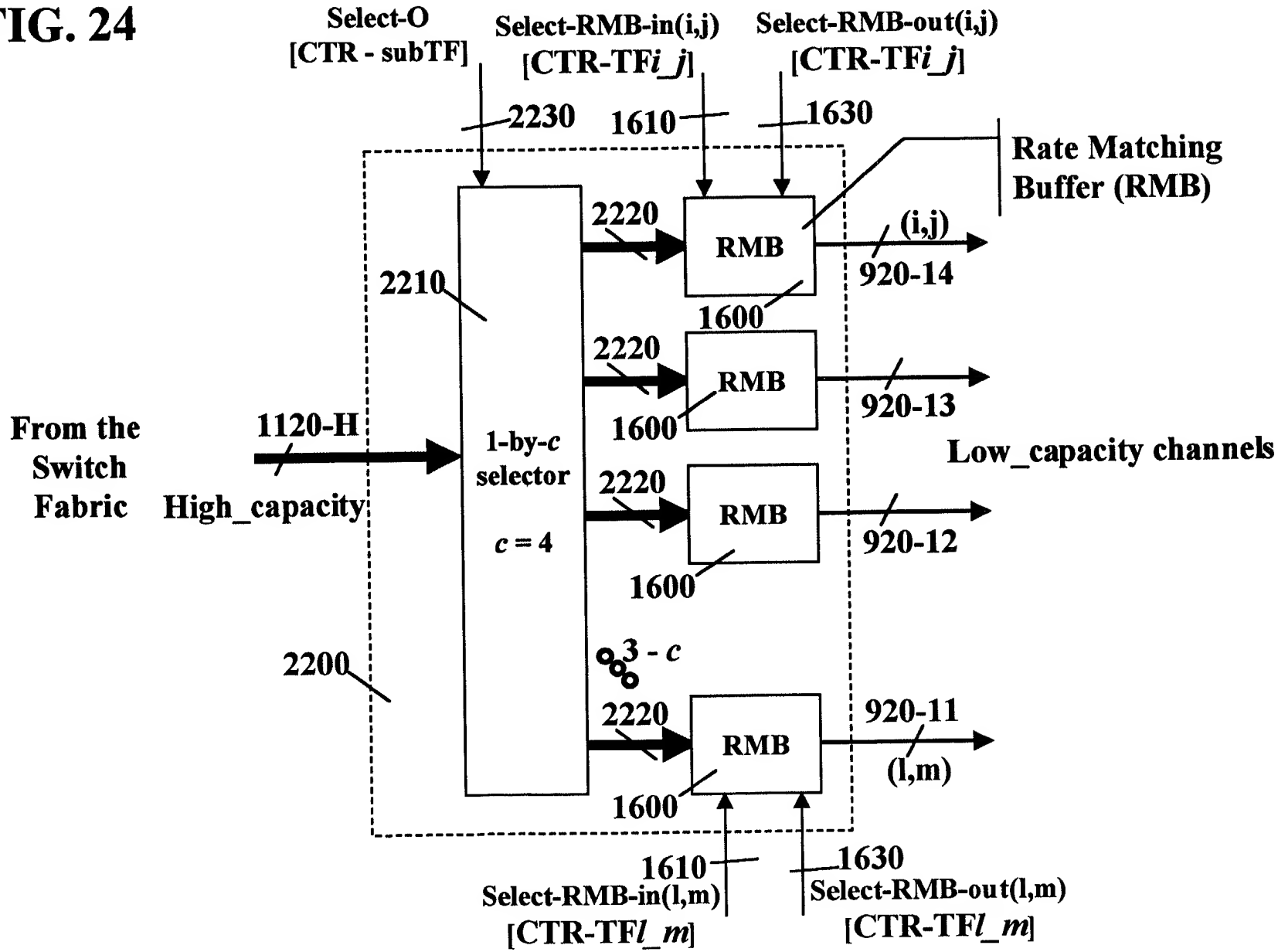

Select-O
[CTR - subTF]
Select-RMB-in(i,j)
[CTR-TFi_j]
Select-RMB-out(i,j)
[CTR-TFi_j]
2230
1610
1630
Rate Matching
Buffer (RMB)
2220
RMB
(i,j)
920-14
2210
1600
2220
RMB
920-13
From the
Switch
Fabric
1120-H
1-by-c
selector
c = 4
High_capacity
Low_capacity channels
2220
RMB
920-12
1600
2200
3 - c
2220
RMB
920-11
(l,m)
1600
1610
Select-RMB-in(l,m)
[CTR-TFl_m]
1630
Select-RMB-out(l,m)
[CTR-TFl_m]

FIG. 25

N: number of input/output channels. E.g., N=256
L • Low_capacity = N_high • High_capacity + N_low • Low_capacity
L > N

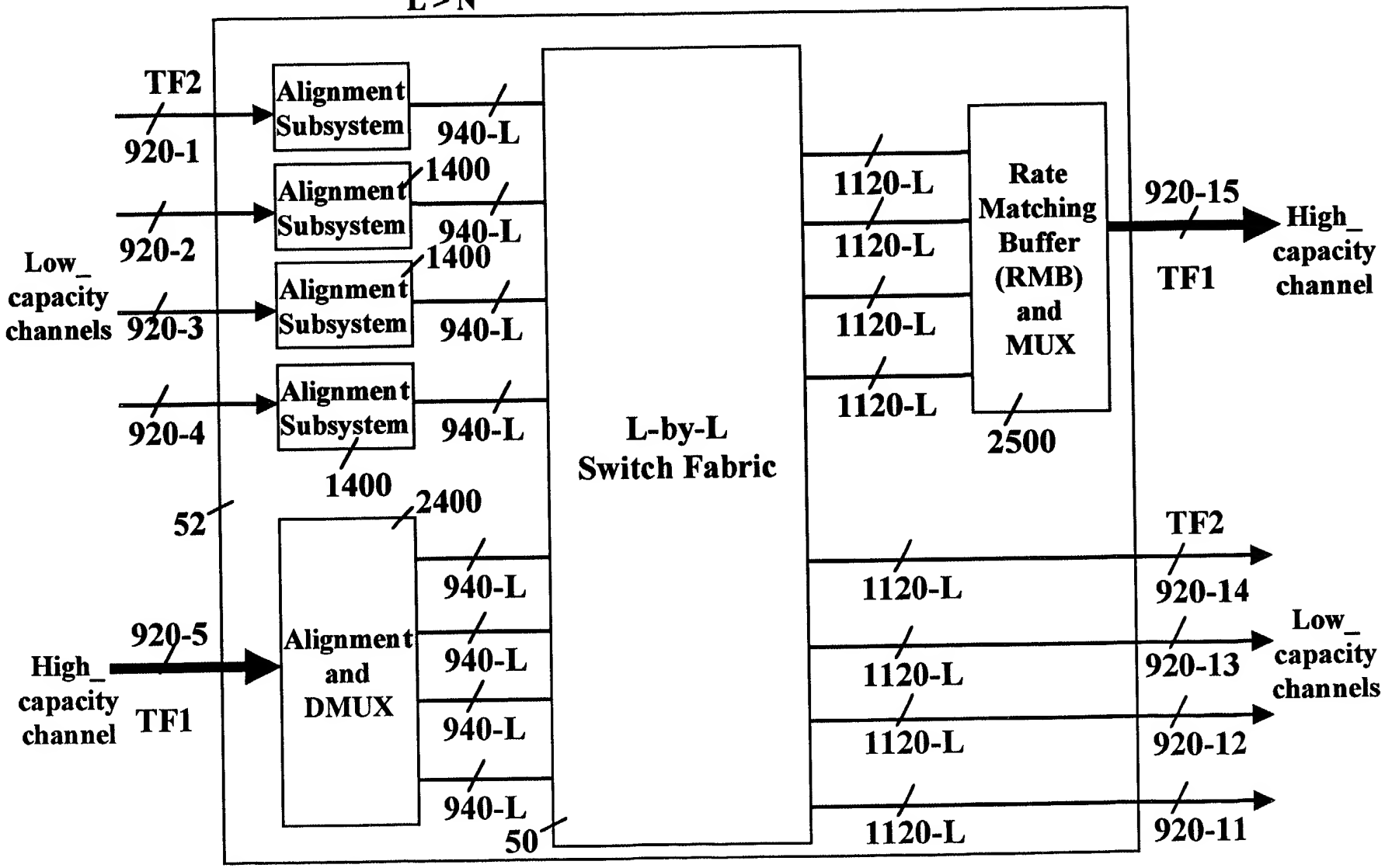


FIG. 26

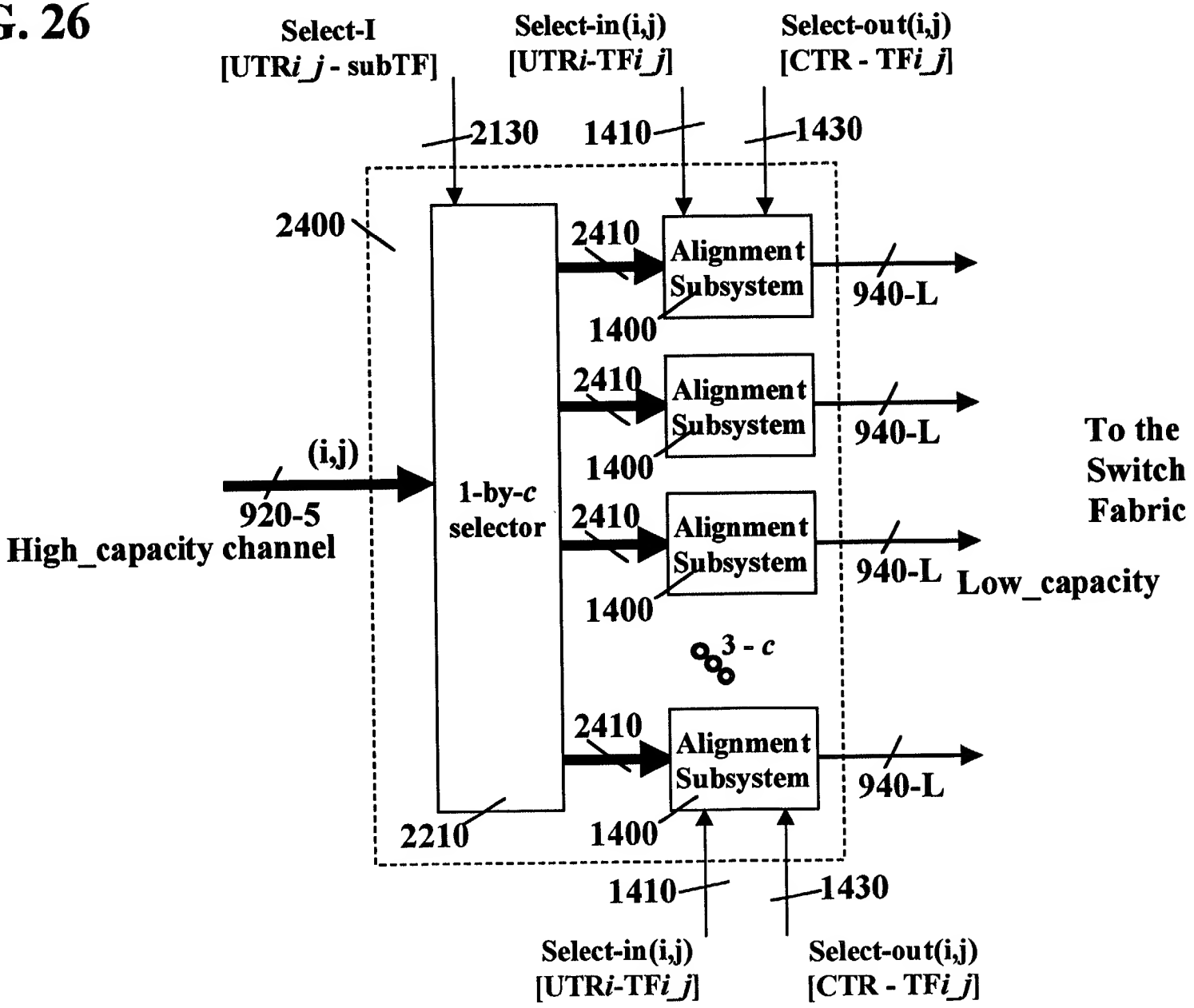


FIG. 27

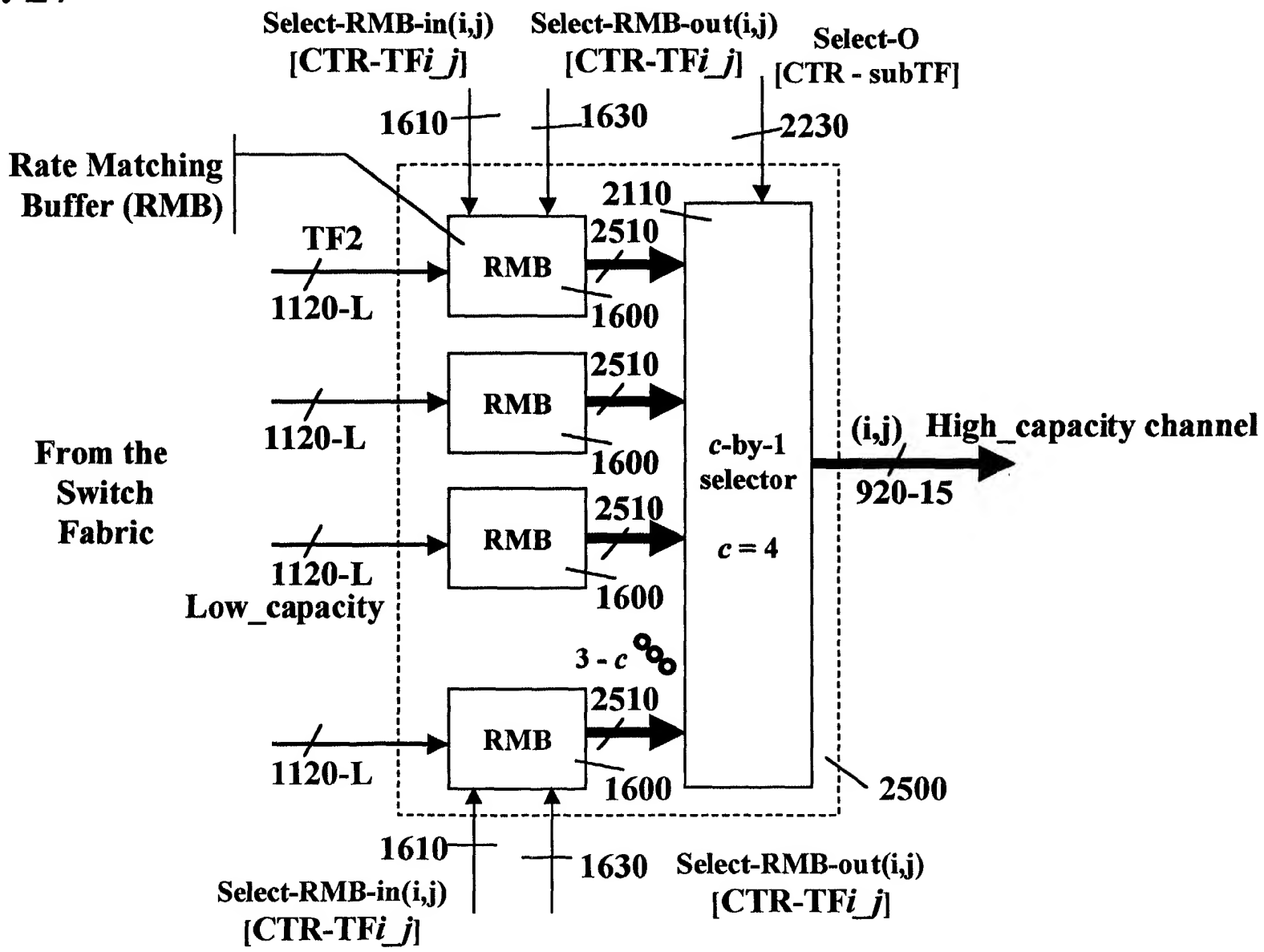
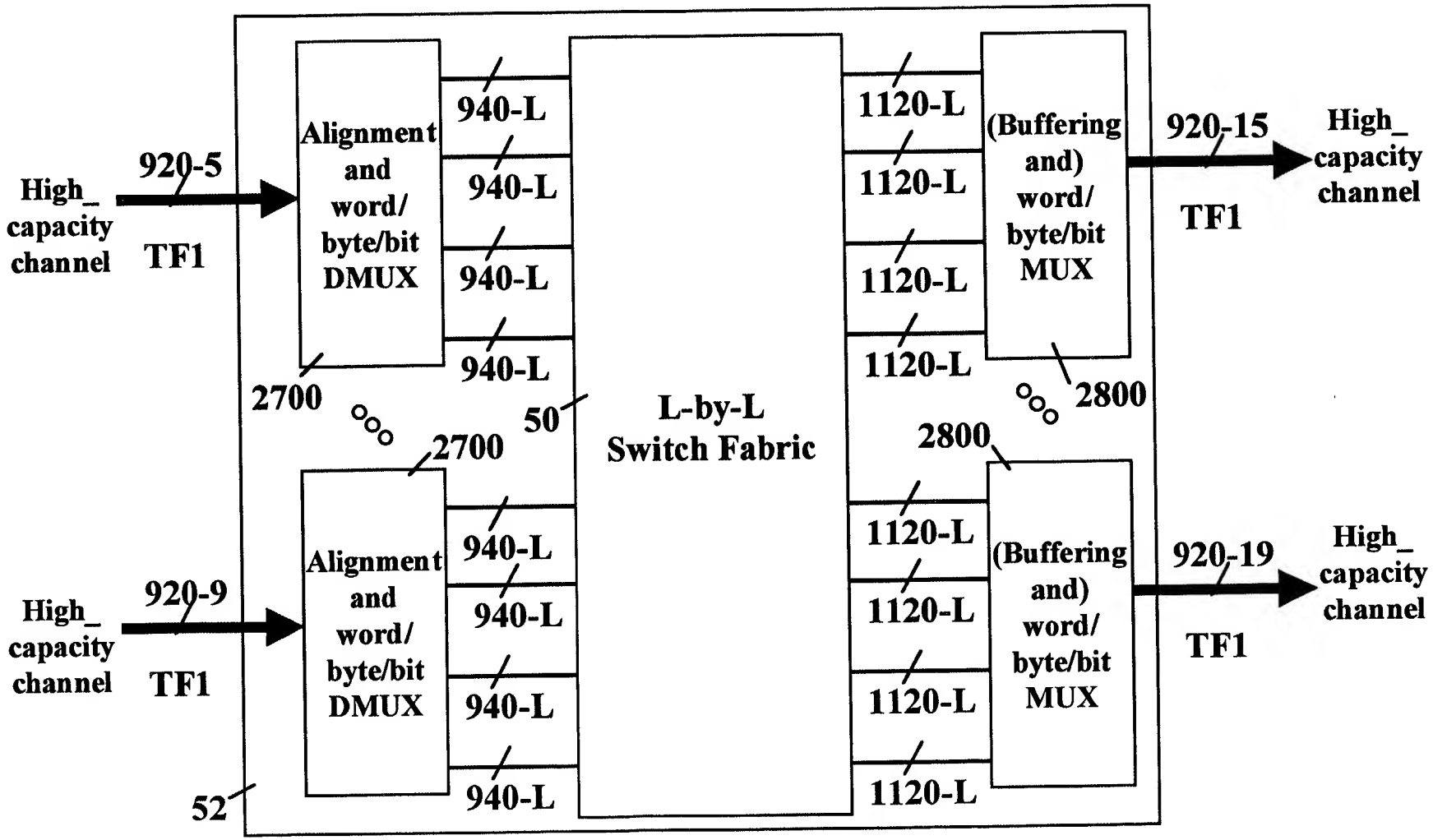


FIG. 28

N: number of input/output channels. E.g., N=256
L • Low_capacity = N • High_capacity
L = c • N > N



Time Driven Switch

FIG. 29

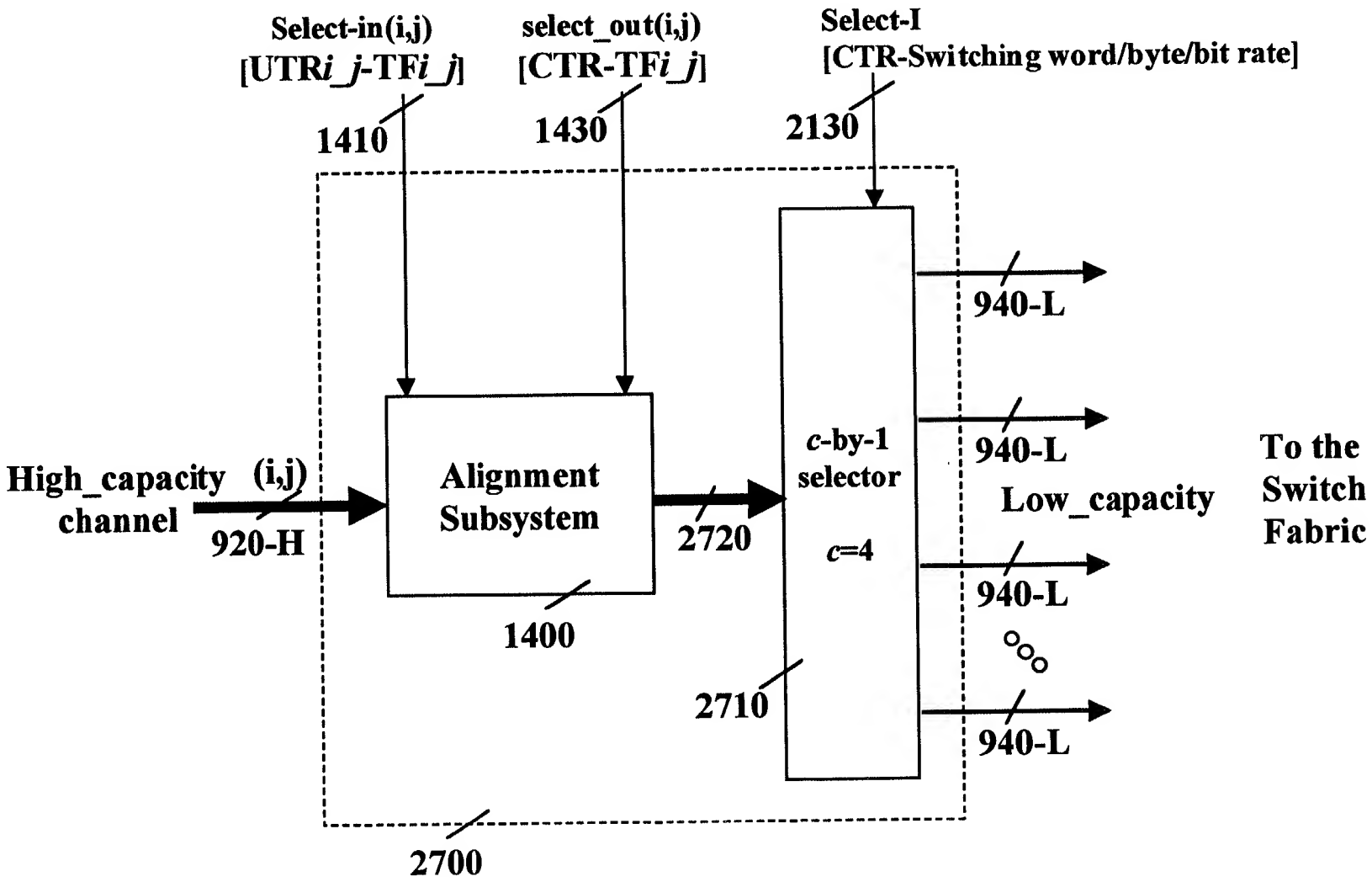


FIG. 30

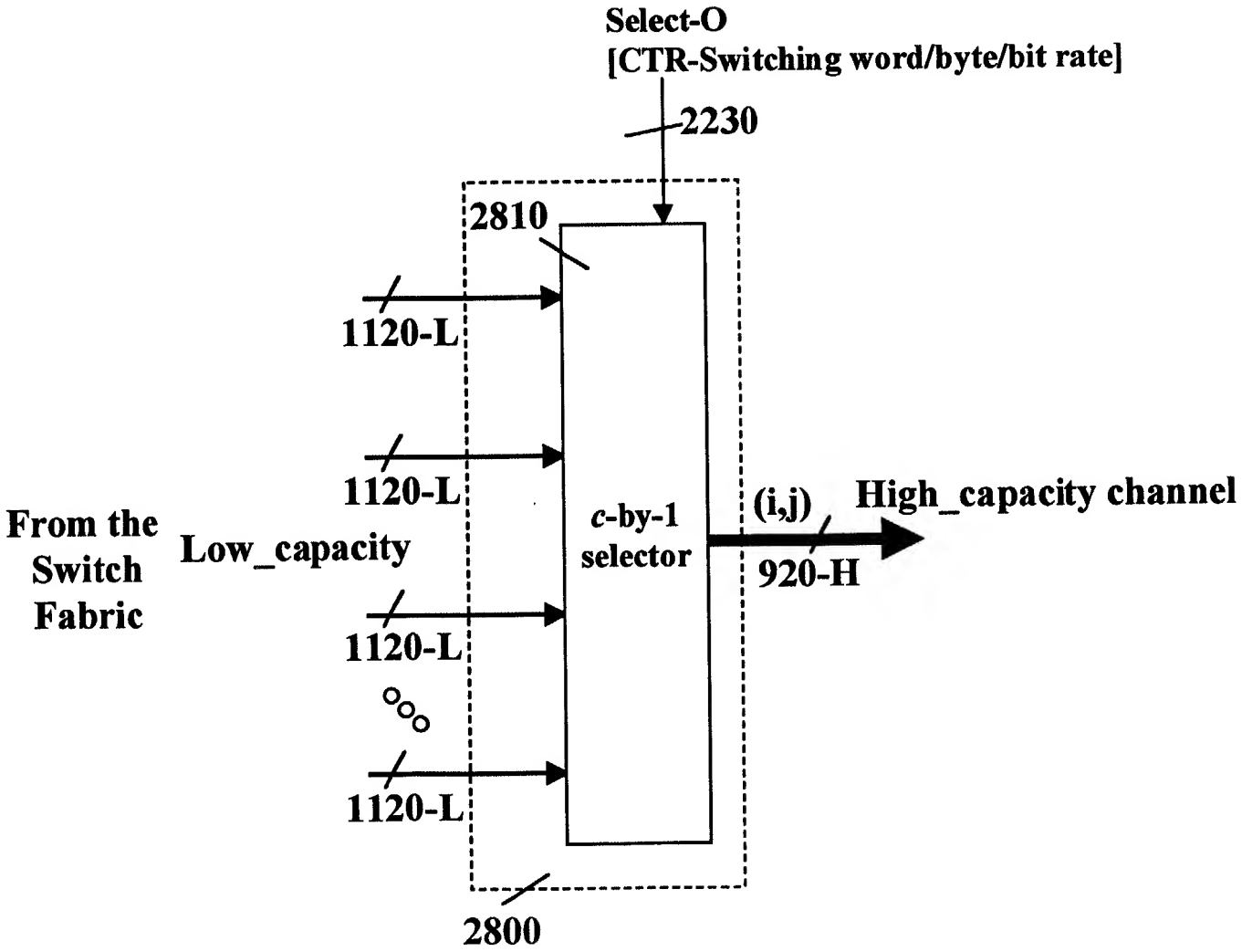


FIG. 31

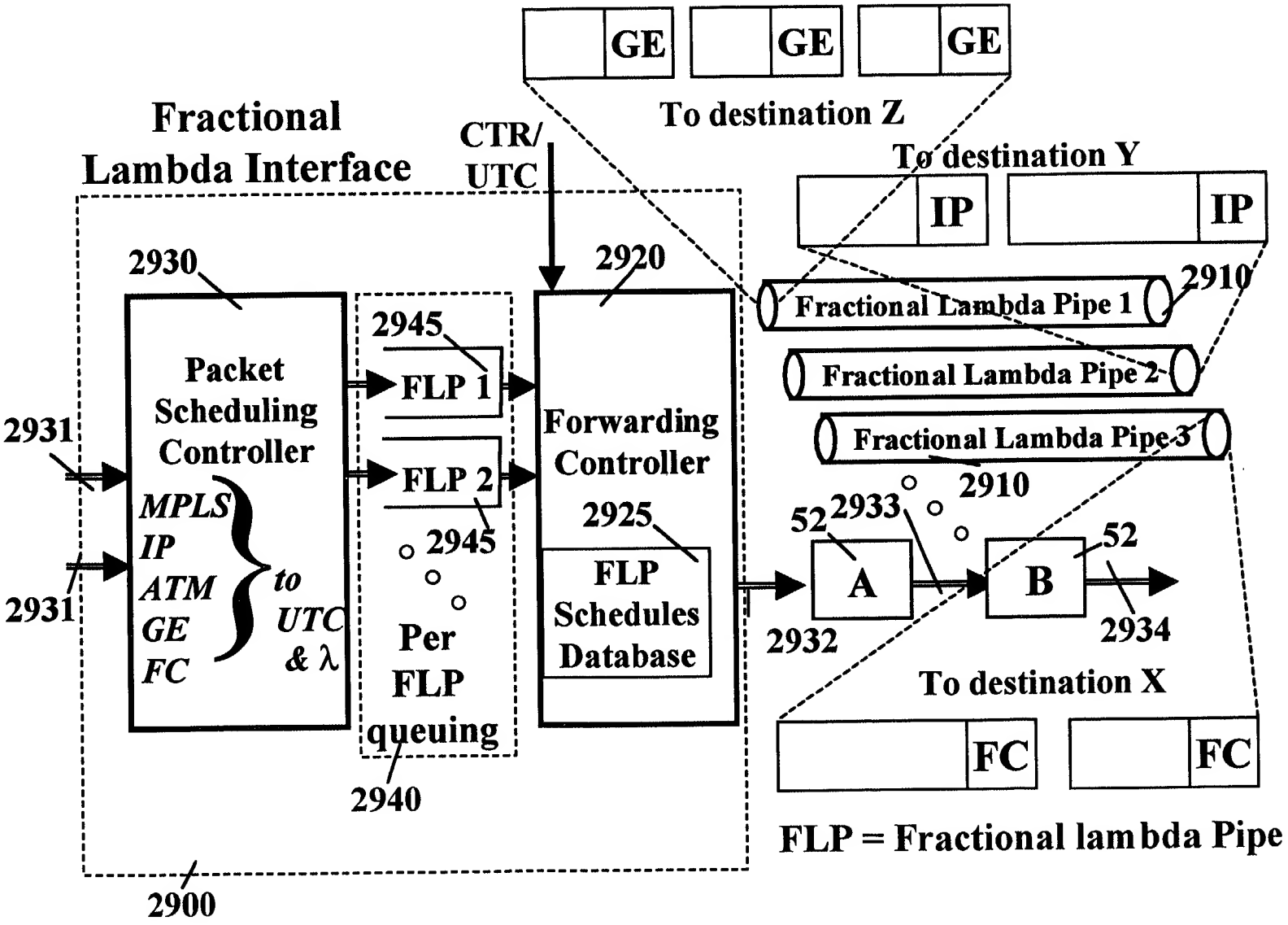

GE
GE
GE
To destination Z
To destination Y
IP
IP
Fractional Lambda Interface
CTR/
UTC
2930
2920
2910
Fractional Lambda Pipe 1
Fractional Lambda Pipe 2
Fractional Lambda Pipe 3
2945
FLP 1
FLP 2
2945
Packet
Scheduling
Controller
MPLS
IP
ATM
GE
FC
to
UTC
& λ
2931
2931
Forwarding
Controller
2925
FLP
Schedules
Database
Per
FLP
queuing
2940
2900
52
2933
2910
A
B
52
2932
2934
To destination X
FC
FC
FLP = Fractional lambda Pipe

FIG. 32

Channel Capacity		TF Duration	TF Size		STS-1s	TFs/s
51.84	STS- 1	250	1620	1512	2	4000
		500	3240	3024	4	2000
		1000	6480	6048	8	1000
155.52	STS- 3	125	2430	2268	3	8000
		250	4860	4536	6	4000
		500	9720	9072	12	2000
622.08	STS- 12	62.5	4860	4536	6	16000
		125	9720	9072	12	8000
		250	19440	18144	24	4000
2488.32	STS- 48	62.5	19440	18144	24	16000
		31.25	9720	9072	12	32000
		15.625	4860	4536	6	64000
9953.28	STS- 192	7.8125	9720	9072	12	128000
		15.625	19440	18144	24	64000
1000	GE	125	15625	15625	19.3	8000
		100	12500	12500	15.4	10000
		80	10000	10000	12.3	12500
10000	10GE	15.625	19531.25	19531.3	24.1	64000
		12.5	15625	15625	19.3	80000
		10	12500	12500	15.4	100000

FIG. 33

Ch Capacity		TF Dur.	TF Size	GE TFs	TFs/s
1000	GE	80	10000	1.0	12500
51.84	STS- 1	250	1512	0.15	4000
		500	3024	0.30	2000
		1000	6048	0.60	1000
155.5	STS- 3	125	2268	0.23	8000
		250	4536	0.45	4000
		500	9072	0.91	2000
622.1	STS- 12	62.5	4536	0.45	16000
		125	9072	0.91	8000
		250	18144	1.81	4000
2488	STS- 48	62.5	18144	1.81	16000
		31.25	9072	0.91	32000
		15.625	4536	0.45	64000
9953	STS- 192	7.8125	9072	0.91	128000
		15.625	18144	1.81	64000
10000	10GE	8	10000	1.00	125000
		16	20000	2.00	62500

FIG. 34

Ch Capacity		TF Dur.	TF Size	GE TFs	TFs/s
1000	GE	62.5	7812.5	1.0	16000
51.84	STS- 1	250	1512	0.19	4000
		500	3024	0.39	2000
		1000	6048	0.77	1000
155.52	STS- 3	125	2268	0.29	8000
		250	4536	0.58	4000
		500	9072	1.16	2000
622.08	STS- 12	62.5	4536	0.58	16000
		125	9072	1.16	8000
		250	18144	2.32	4000
2488.32	STS- 48	62.5	18144	2.32	16000
		31.25	9072	1.16	32000
		15.625	4536	0.58	64000
9953.28	STS- 192	7.8125	9072	1.16	128000
		15.625	18144	2.32	64000
10000	10GE	12.5	15625	2.00	80000
		25	31250	4.00	40000

FIG. 35

***TF Alignment of* UTR(j) *to* UTC -** **with three input queues - principle of operation:**

The same queue is not used simultaneously for:

1. **Receiving data packets from the serial link, and**
2. **Forwarding data packets to the switch**

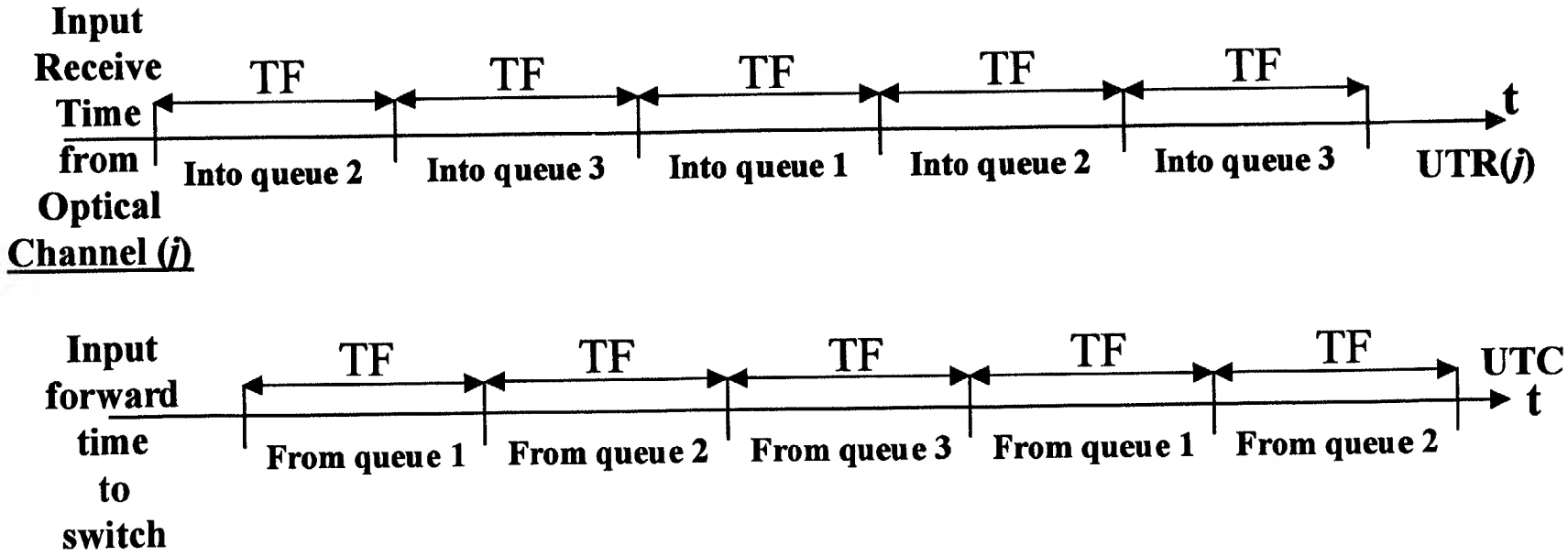


FIG. 36

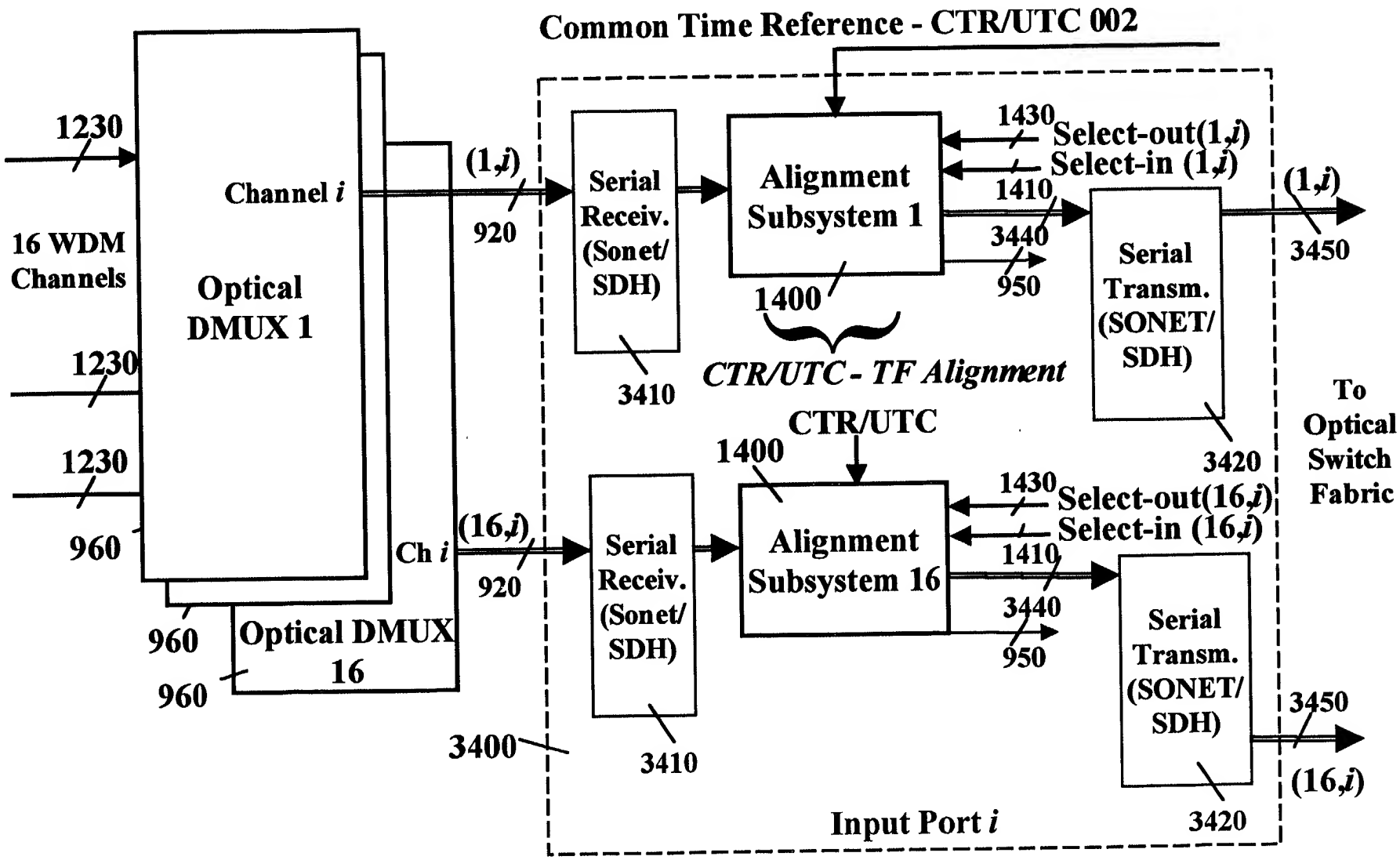

Common Time Reference - CTR/UTC 002
1230
16 WDM Channels
Optical DMUX 1
Channel i
(1,i)
920
Serial Receiv. (Sonet/ SDH)
3410
Alignment Subsystem 1
1400
CTR/UTC - TF Alignment
1430
Select-out(1,i)
Select-in (1,i)
1410
3440
950
Serial Transm. (SONET/ SDH)
3420
(1,i)
3450
To Optical Switch Fabric
1230
1230
960
960
960
Optical DMUX 16
Ch i
(16,i)
920
3400
Serial Receiv. (Sonet/ SDH)
3410
CTR/UTC
1400
Alignment Subsystem 16
1430
Select-out(16,i)
Select-in (16,i)
1410
3440
950
Serial Transm. (SONET/ SDH)
3420
3450
(16,i)
Input Port i

FIG. 37

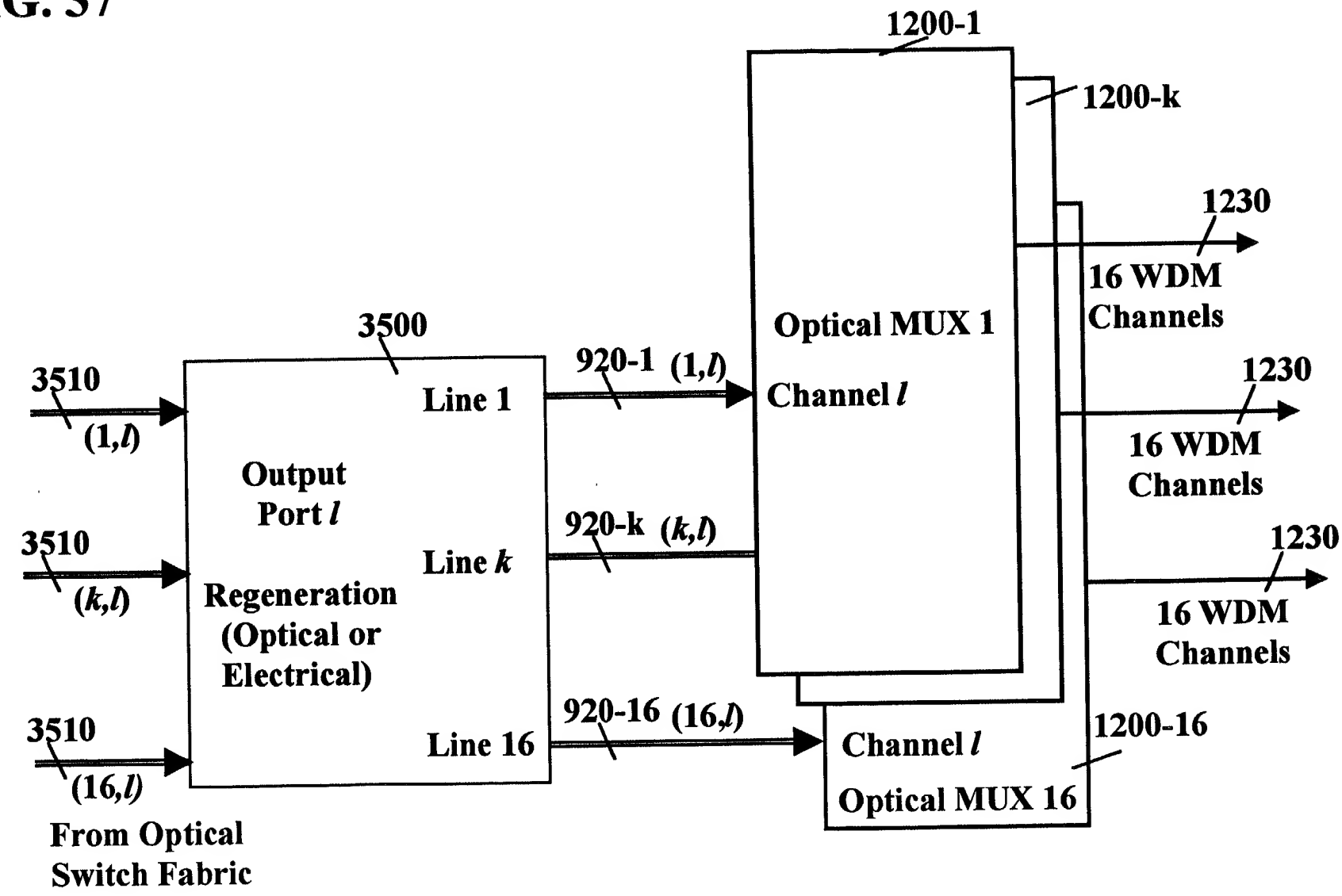

1200-1
1200-k
1230
16 WDM Channels
Optical MUX 1
Channel *l*
3500
3510
(1,*l*)
Line 1
920-1 (1,*l*)
1230
16 WDM Channels
Output Port *l*
3510
(*k*,*l*)
920-k (*k*,*l*)
Line *k*
1230
16 WDM Channels
Regeneration (Optical or Electrical)
3510
(16,*l*)
920-16 (16,*l*)
Line 16
Channel *l*
1200-16
Optical MUX 16
From Optical Switch Fabric

FIG. 38

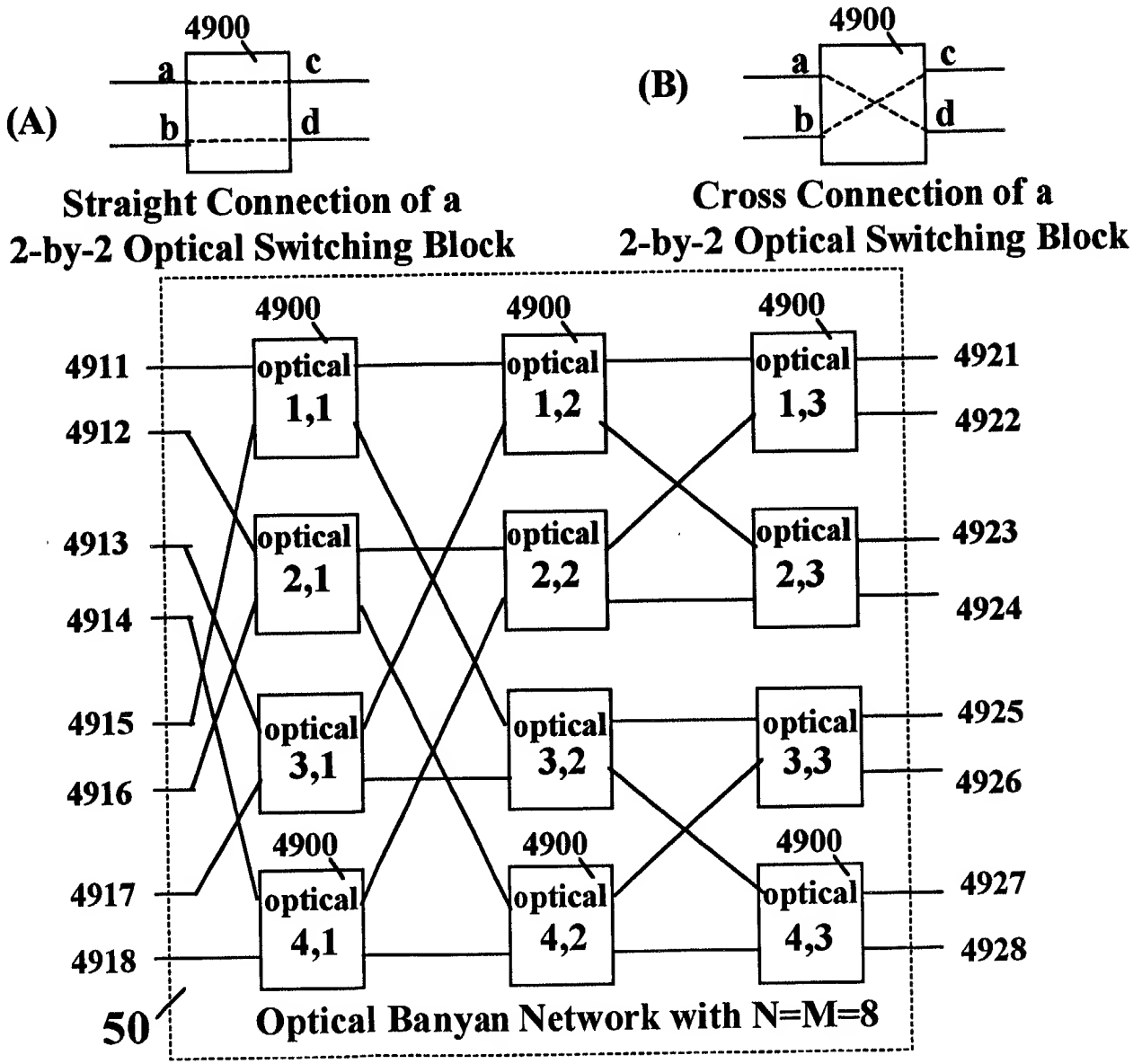


FIG. 39

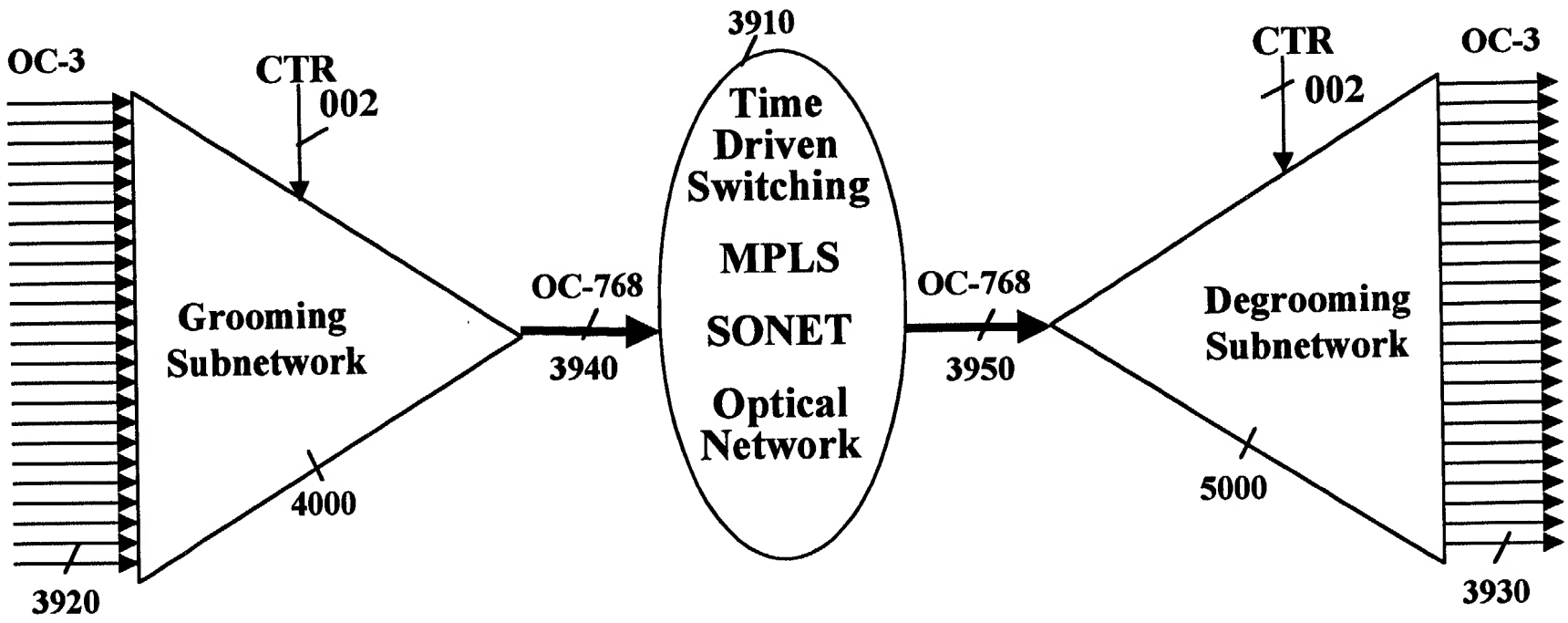


FIG. 40

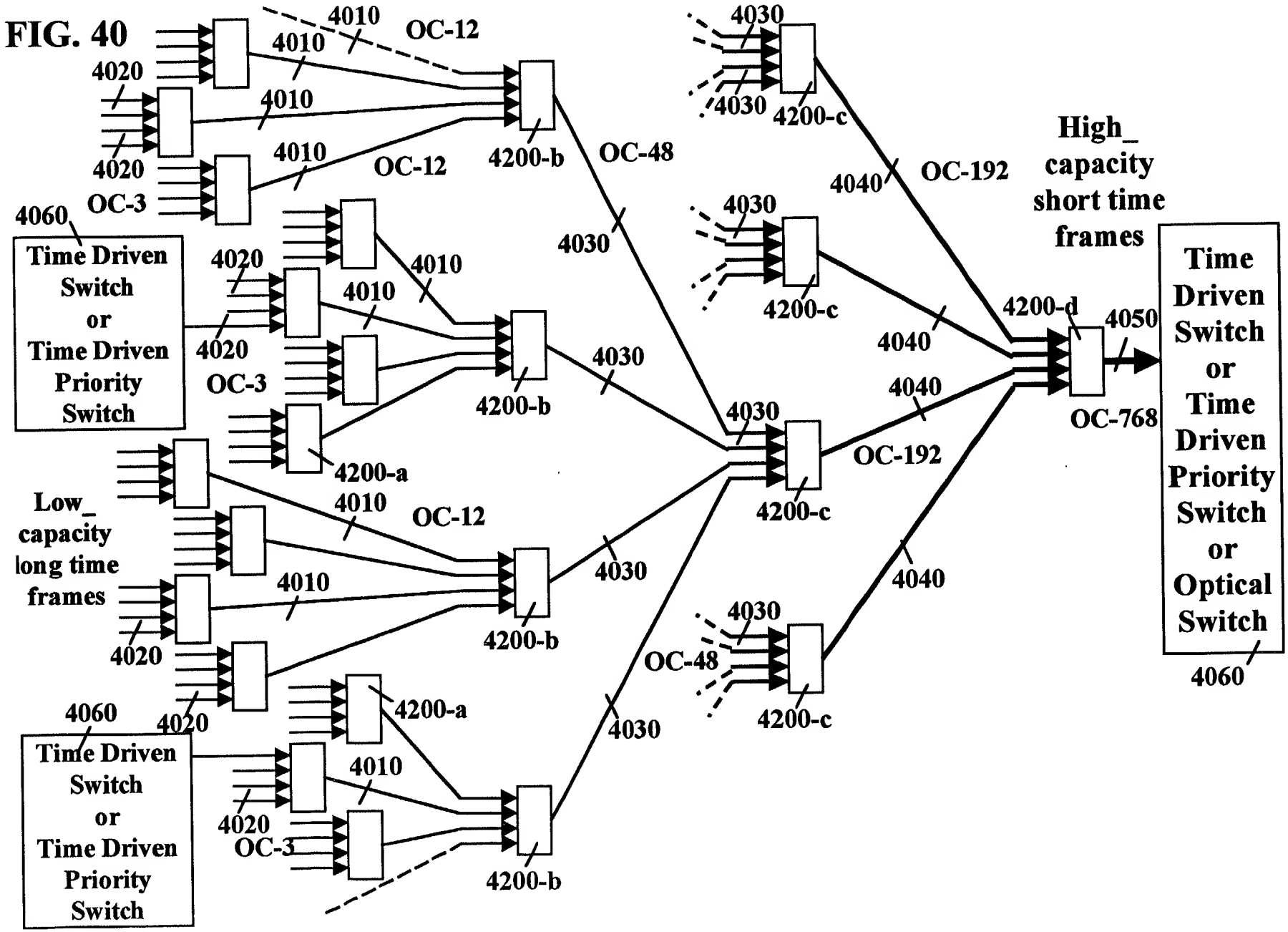

4010
OC-12
4020
4010
4010
4020
OC-3
4010
OC-12
4200-b
OC-48
4030
4060
Time Driven Switch or Time Driven Priority Switch
4020
4010
4010
4020
OC-3
4200-b
4030
4200-a
Low_ capacity long time frames
4010
OC-12
4200-b
4030
4010
4020
4020
4200-a
4060
Time Driven Switch or Time Driven Priority Switch
4010
4020
OC-3
4200-b
4030
4030
4200-c
4040
OC-192
4030
4200-c
4040
4030
4200-c
OC-48
4030
4200-c
High_ capacity short time frames
4200-d
4050
OC-768
4040
4040
4040
Time Driven Switch or Time Driven Priority Switch or Optical Switch
4060

FIG. 41

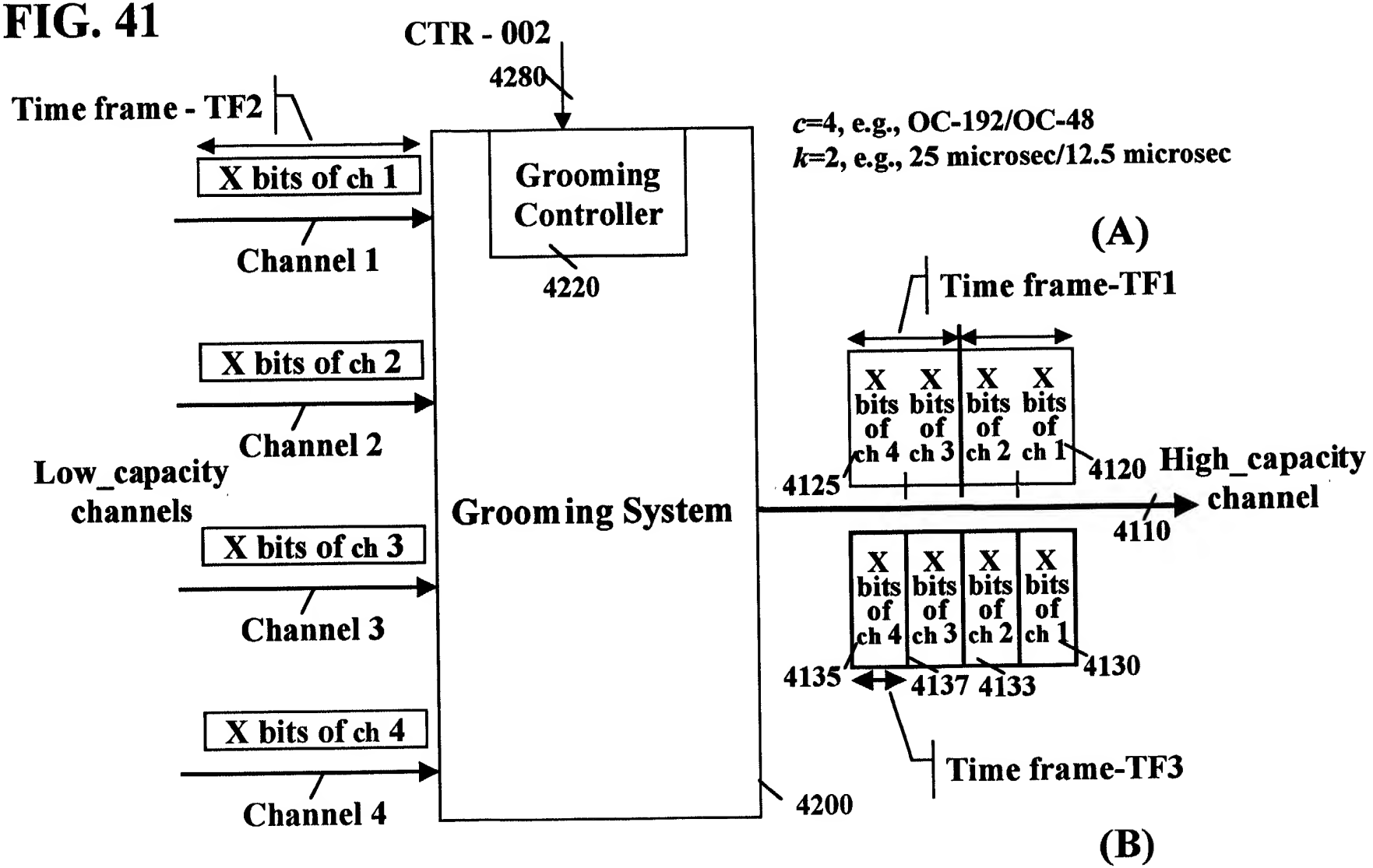

CTR - 002
4280
Time frame - TF2
X bits of ch 1
Channel 1
Grooming Controller
4220
X bits of ch 2
Channel 2
Low_capacity channels
Grooming System
X bits of ch 3
Channel 3
X bits of ch 4
Channel 4
4200
c=4, e.g., OC-192/OC-48
k=2, e.g., 25 microsec/12.5 microsec
(A)
Time frame-TF1
X bits of ch 4
X bits of ch 3
X bits of ch 2
X bits of ch 1
4125
4120
High_capacity channel
4110
4135
4137
4133
4130
Time frame-TF3
(B)

FIG. 42

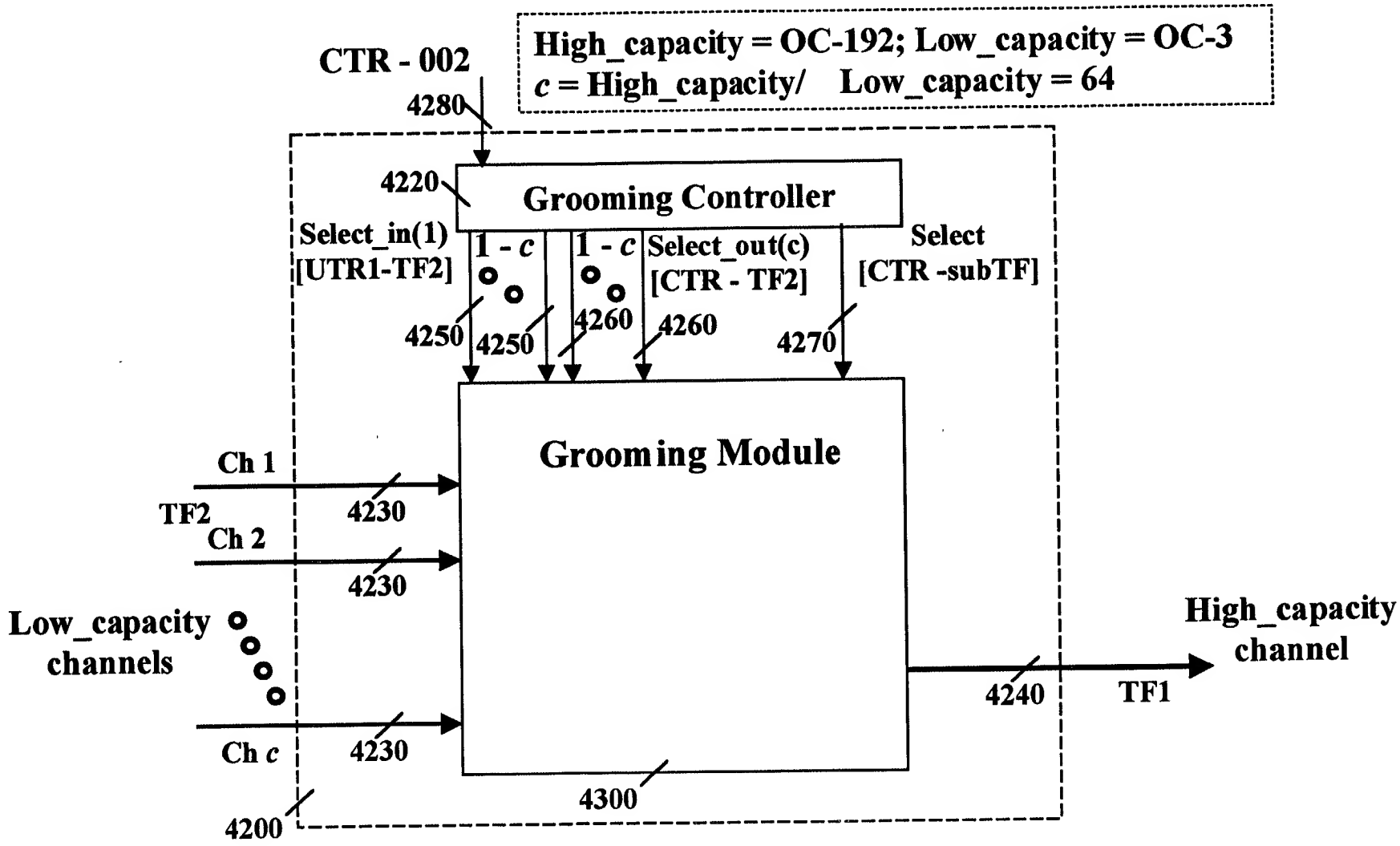

High_capacity = OC-192; Low_capacity = OC-3
c = High_capacity/ Low_capacity = 64
CTR - 002
4280
4220
Grooming Controller
Select_in(1)
[UTR1-TF2]
1 - c
1 - c
Select_out(c)
[CTR - TF2]
Select
[CTR -subTF]
4250
4250
4260
4260
4270
Grooming Module
Ch 1
TF2
4230
Ch 2
4230
Low_capacity
channels
Ch c
4230
High_capacity
channel
4240
TF1
4300
4200

FIG. 43

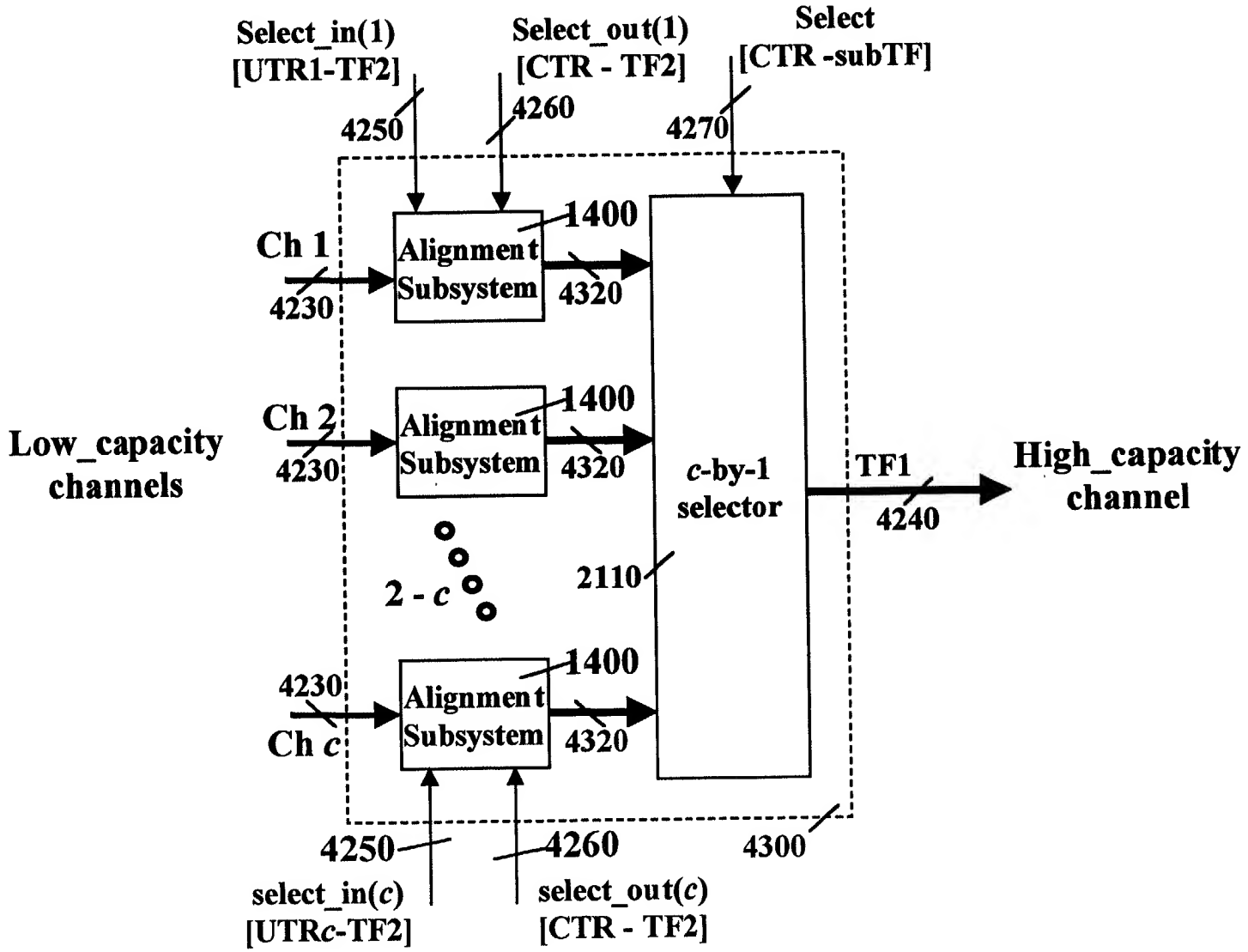

Select_in(1)
[UTR1-TF2]
Select_out(1)
[CTR - TF2]
Select
[CTR -subTF]
4250
4260
4270
Ch 1
Alignment Subsystem
1400
4230
4320
Low_capacity channels
Ch 2
Alignment Subsystem
1400
4230
4320
c-by-1 selector
TF1
4240
High_capacity channel
2 - c
2110
4230
Ch c
Alignment Subsystem
1400
4320
4250
4260
4300
select_in(c)
[UTRc-TF2]
select_out(c)
[CTR - TF2]

FIG. 44

- $CC1\_length \cdot TF1 = CC2\_length \cdot TF2 = CC3\_length \cdot TF2$
- $TF2 = (SC1\_length / SC2\_length) \cdot TF1 = k \cdot TF1$, where the common cycles of *TF1* and *TF2* are aligned with respect to UTC.

For $k = 2$ and $c = 4$ (e.g., High_capacity=OC-192, Low_capacity=OC-48):

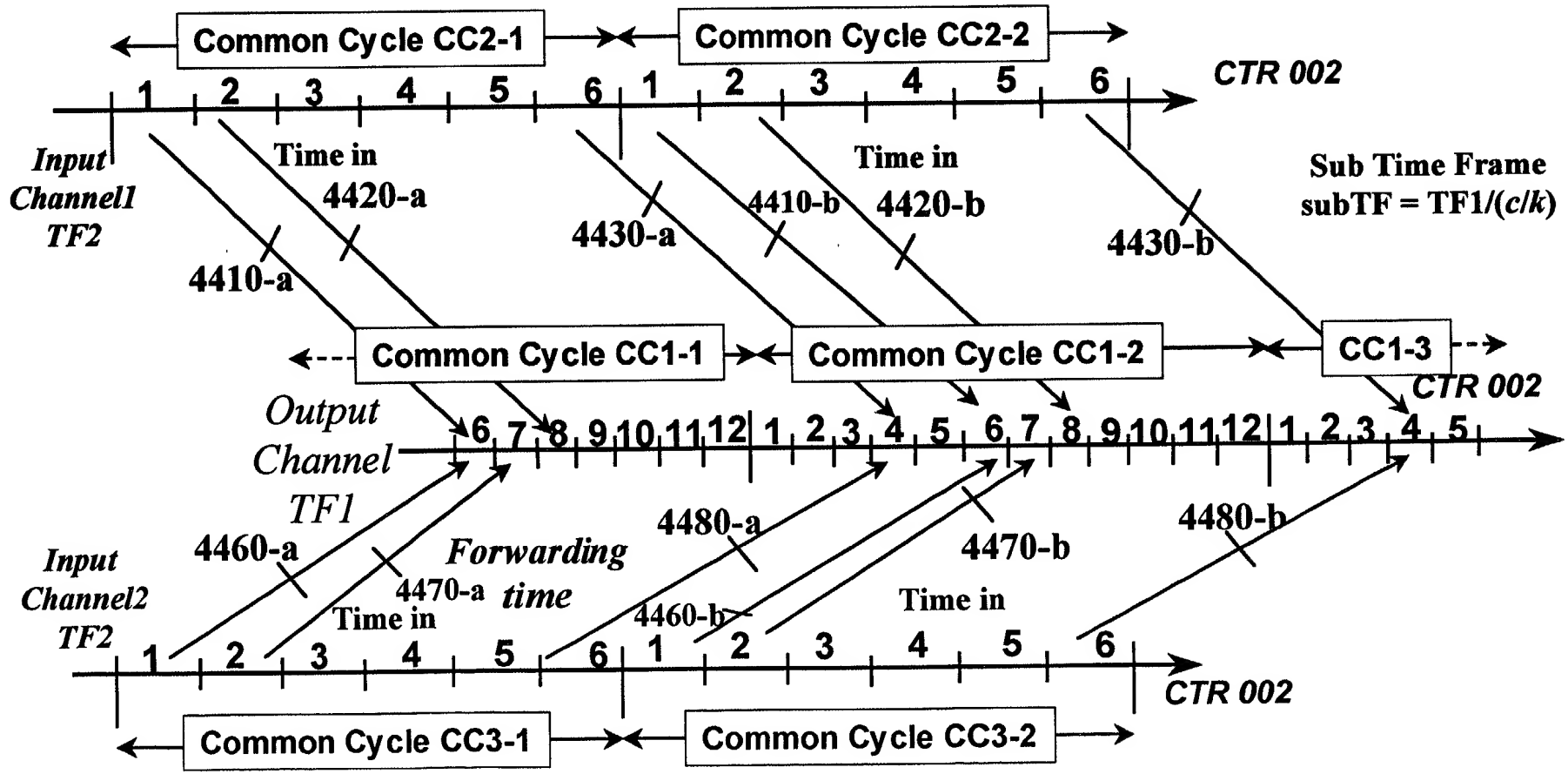


FIG. 45

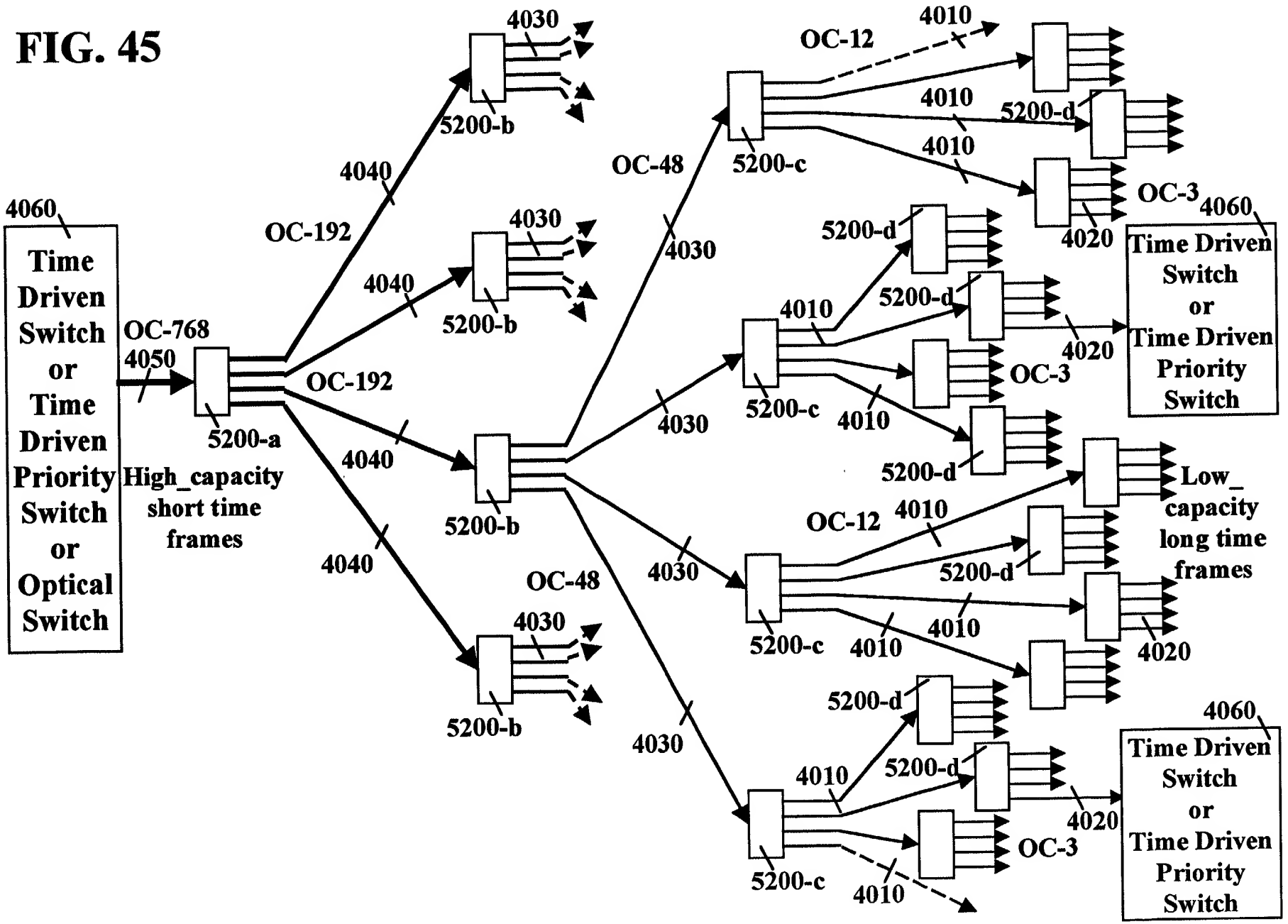
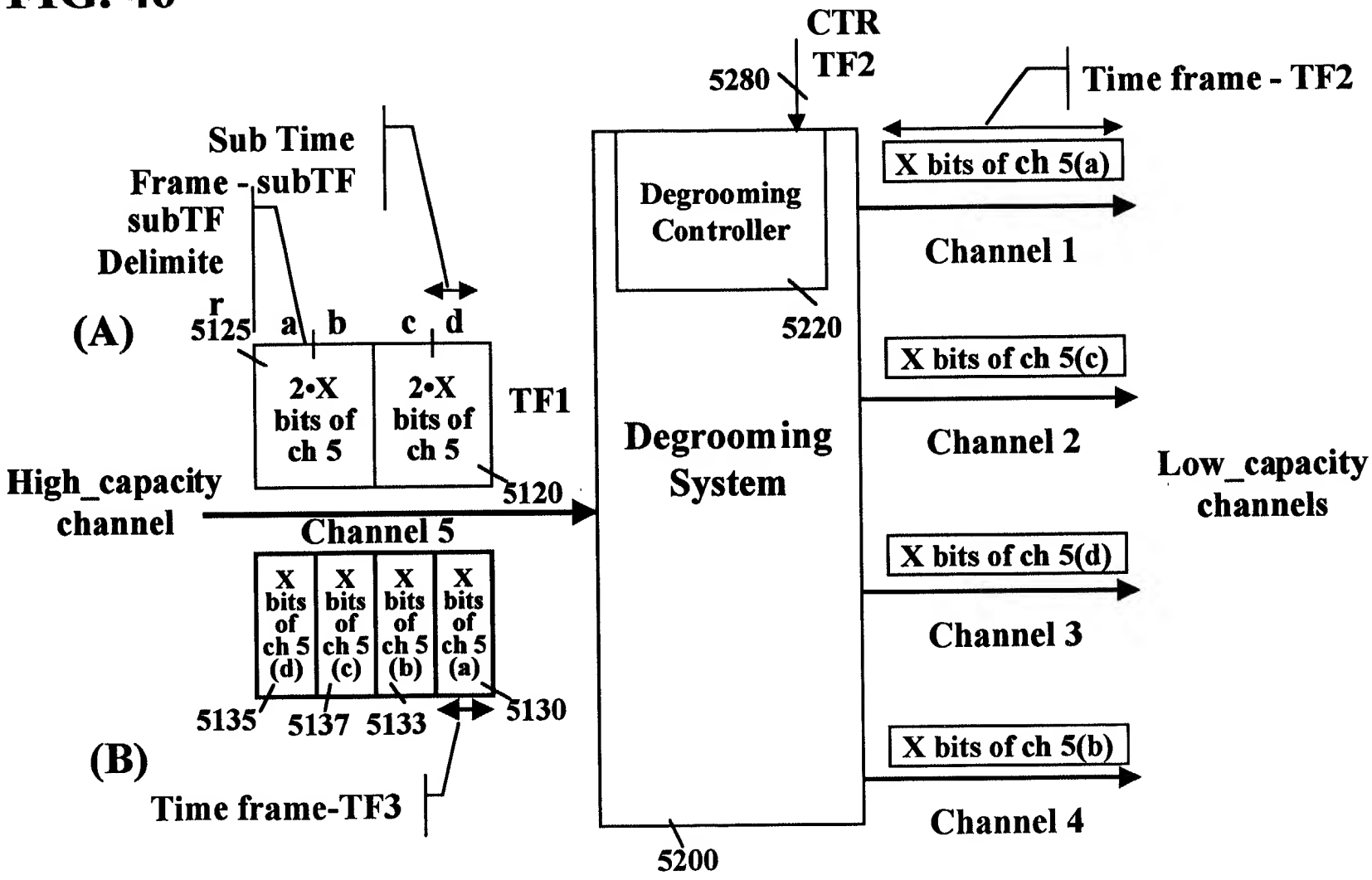

4060
Time Driven Switch or Time Driven Priority Switch or Optical Switch
OC-768
4050
5200-a
High_capacity short time frames
OC-192
4040
OC-192
4040
4040
5200-b
4030
5200-b
4030
5200-b
4030
OC-48
4030
OC-48
4030
4030
5200-c
OC-12
4010
4010
4010
5200-d
OC-3
4020
4060
Time Driven Switch or Time Driven Priority Switch
5200-c
4010
5200-d
5200-d
OC-3
4020
5200-c
4010
5200-d
OC-12
4010
Low_ capacity long time frames
5200-d
4020
5200-c
4010
4010
5200-d
4060
Time Driven Switch or Time Driven Priority Switch
5200-c
4010
5200-d
OC-3
4020

FIG. 46



c=4, e.g., OC-192/OC-48
k=2, e.g., 25 microsec/12.5 microsec

FIG. 47

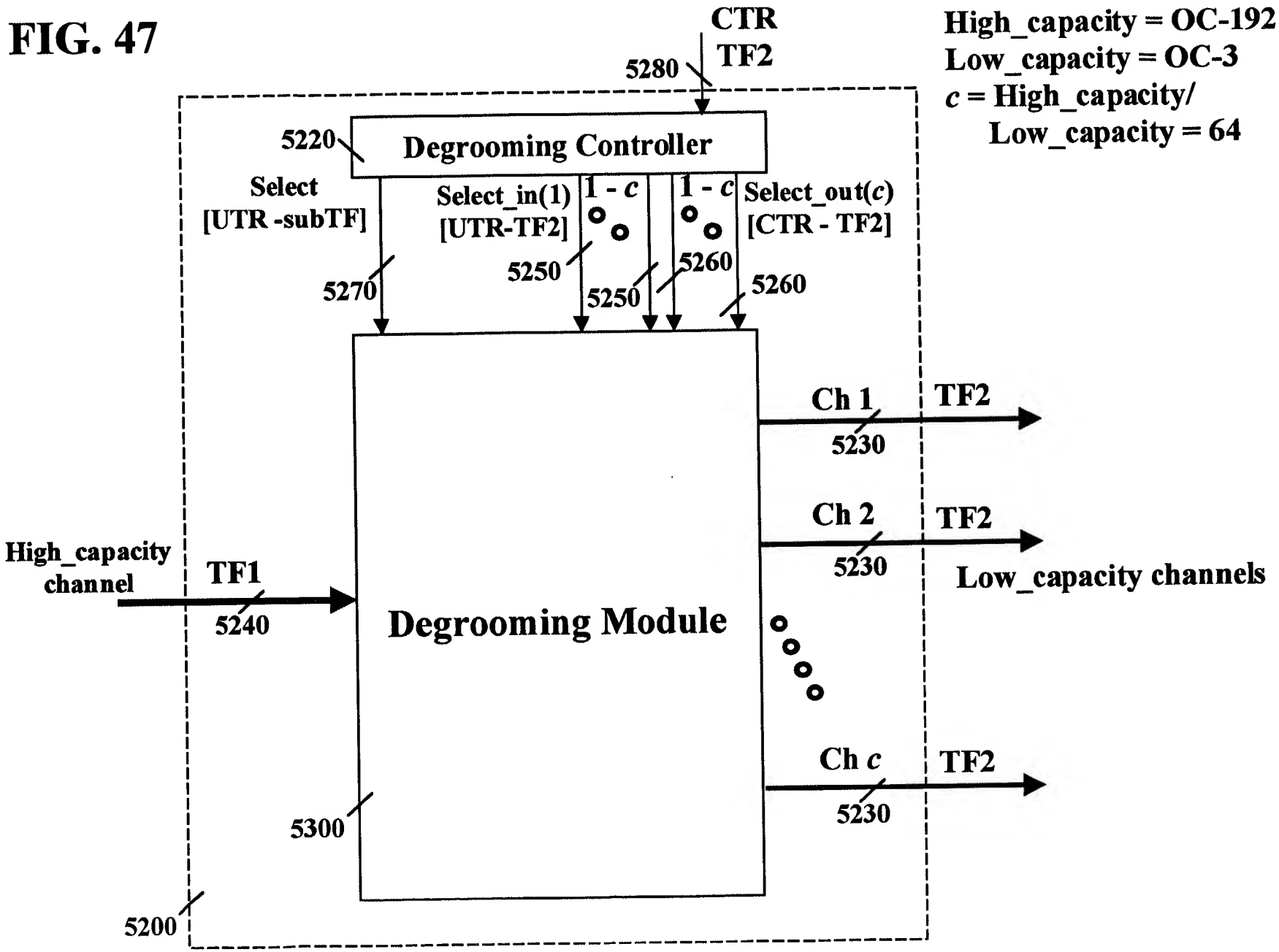

CTR
TF2
5280
High_capacity = OC-192
Low_capacity = OC-3
c = High_capacity/
Low_capacity = 64
5220
Degrooming Controller
Select
[UTR -subTF]
Select_in(1)
[UTR-TF2]
1 - c
1 - c
Select_out(c)
[CTR - TF2]
5270
5250
5250
5260
5260
Ch 1
TF2
5230
Ch 2
TF2
5230
High_capacity
channel
TF1
5240
Degrooming Module
Low_capacity channels
Ch c
TF2
5230
5300
5200

FIG. 48

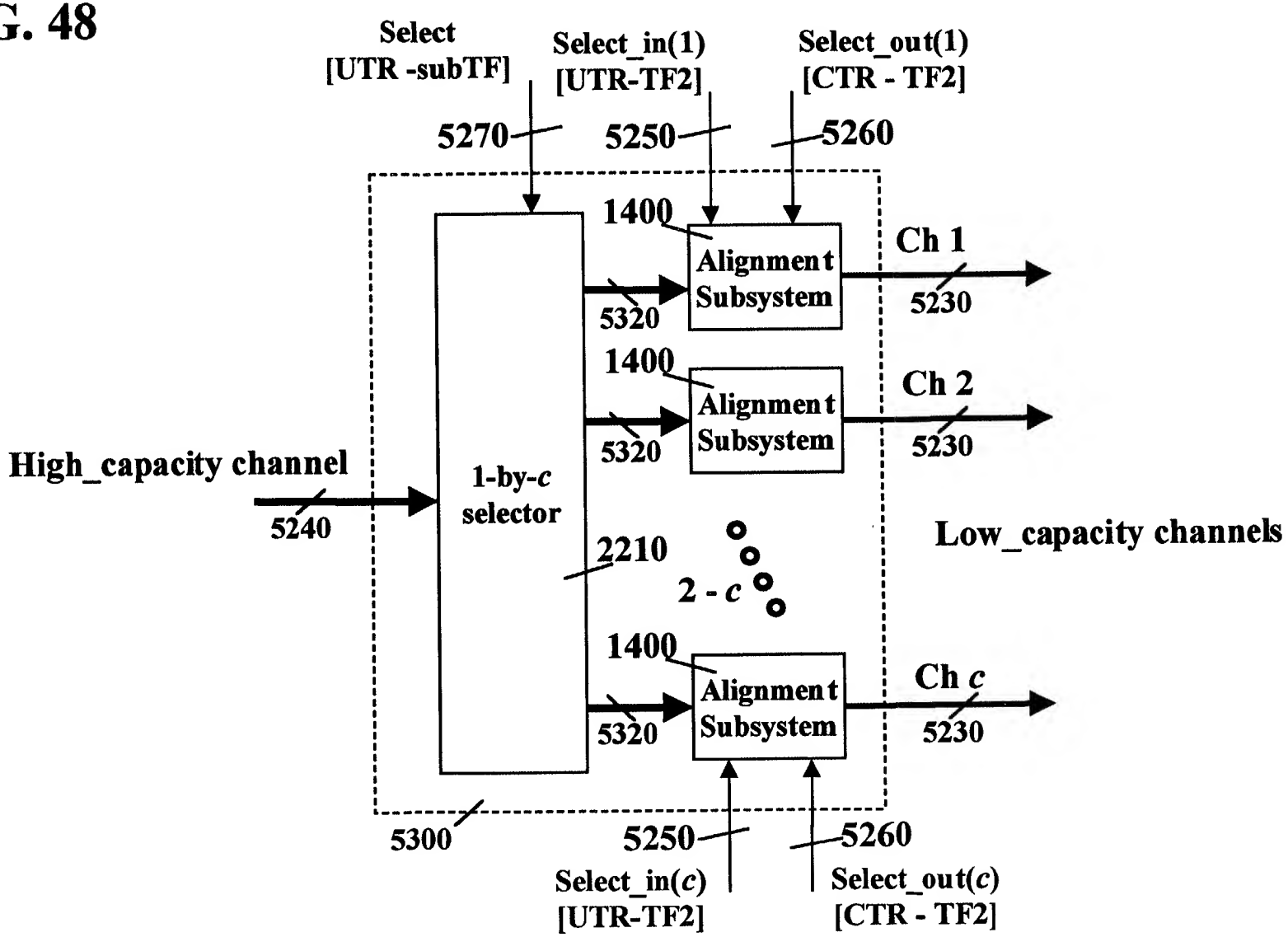

Select
[UTR -subTF]
Select_in(1)
[UTR-TF2]
Select_out(1)
[CTR - TF2]
5270
5250
5260
1400
Alignment Subsystem
Ch 1
5320
5230
1400
Alignment Subsystem
Ch 2
5320
5230
High_capacity channel
5240
1-by-c selector
2210
2 - c
Low_capacity channels
1400
Alignment Subsystem
Ch c
5320
5230
5300
5250
5260
Select_in(c)
[UTR-TF2]
Select_out(c)
[CTR - TF2]

FIG. 49

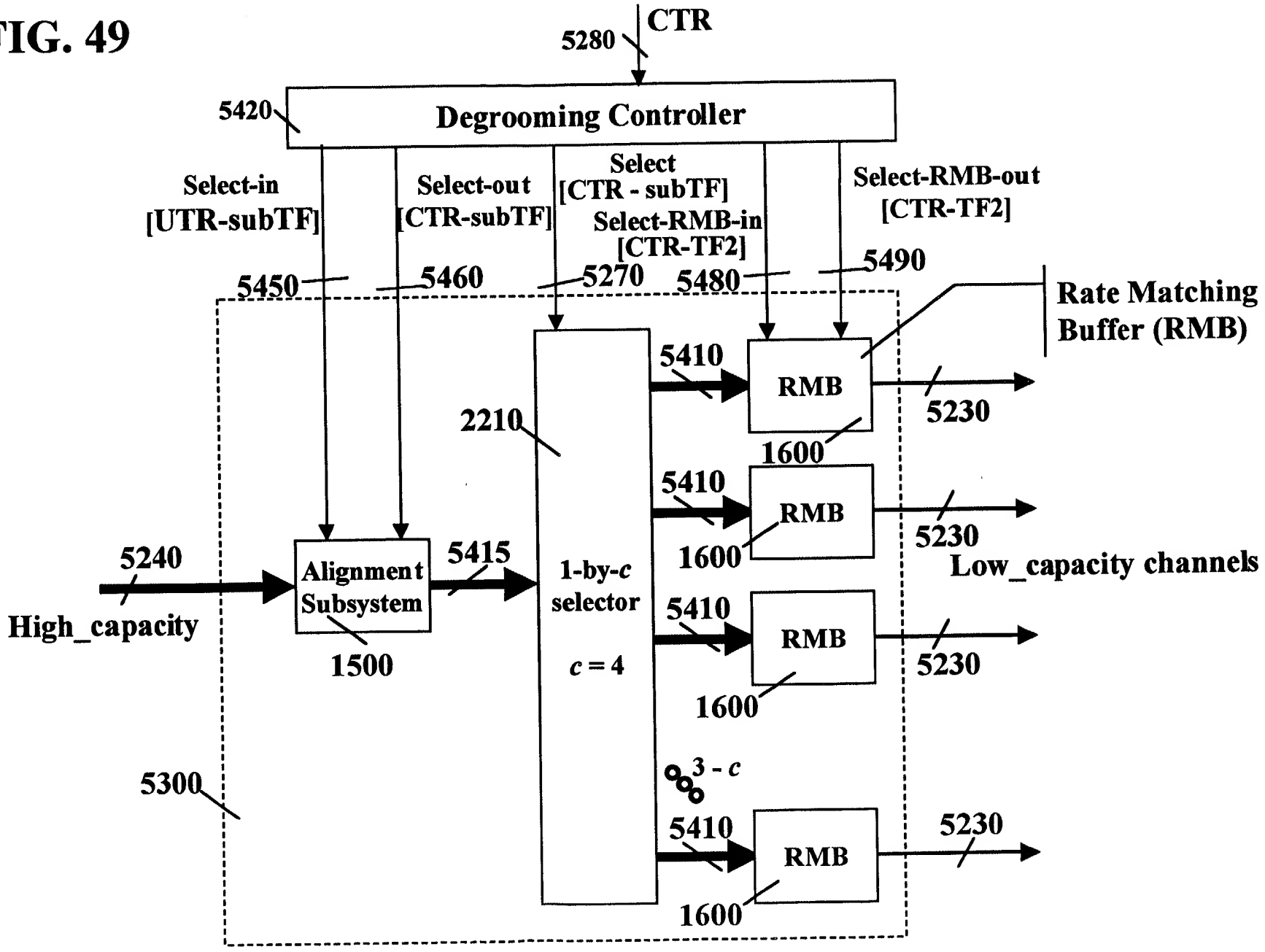

CTR
5280
Degrooming Controller
5420
Select-in
[UTR-subTF]
Select-out
[CTR-subTF]
Select
[CTR - subTF]
Select-RMB-in
[CTR-TF2]
Select-RMB-out
[CTR-TF2]
5450
5460
5270
5480
5490
Rate Matching
Buffer (RMB)
5410
RMB
5230
2210
1600
5240
High_capacity
Alignment
Subsystem
1500
5415
1-by-c
selector
c = 4
Low_capacity channels
3 - c
5300

FIG. 50

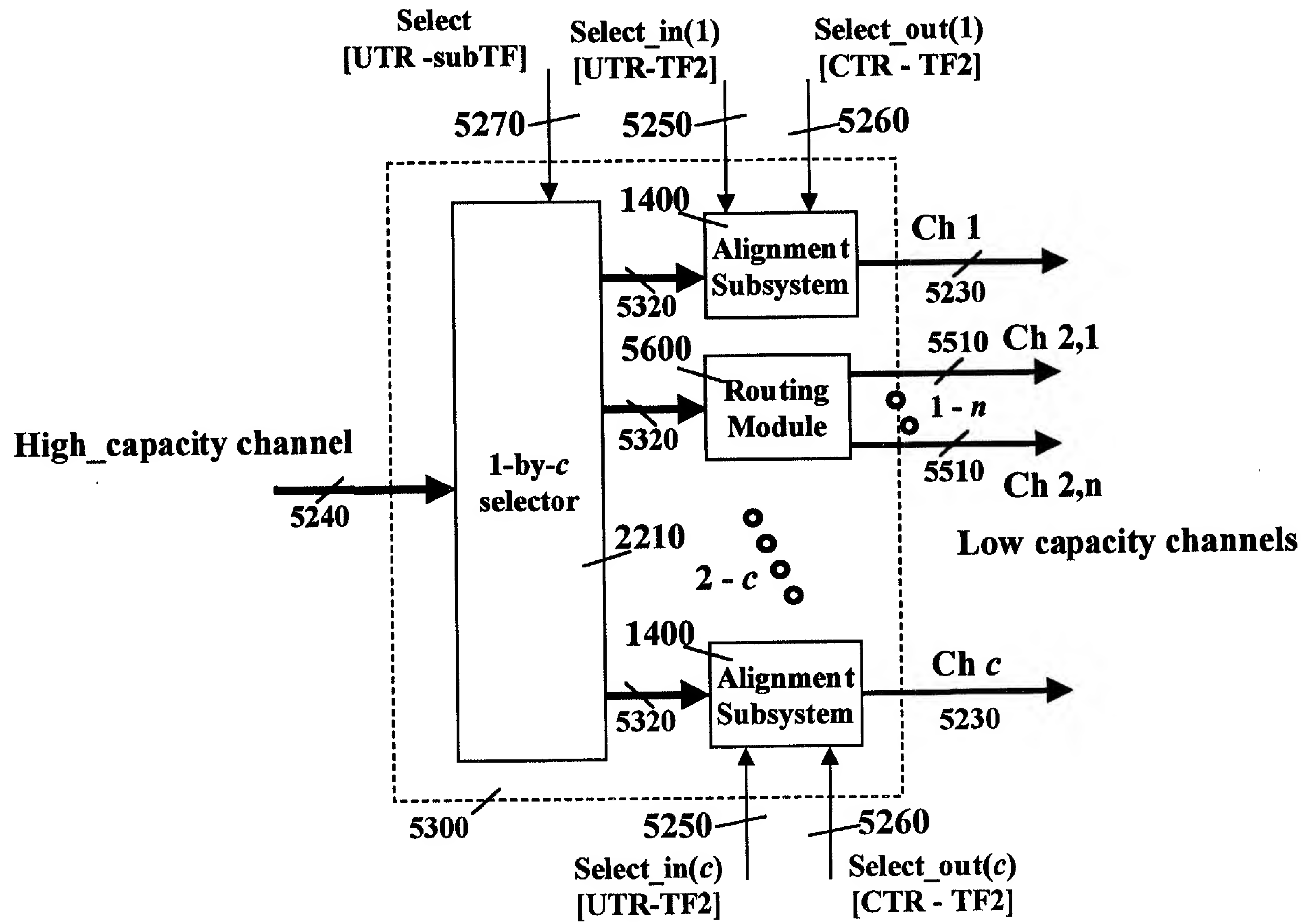


FIG. 51

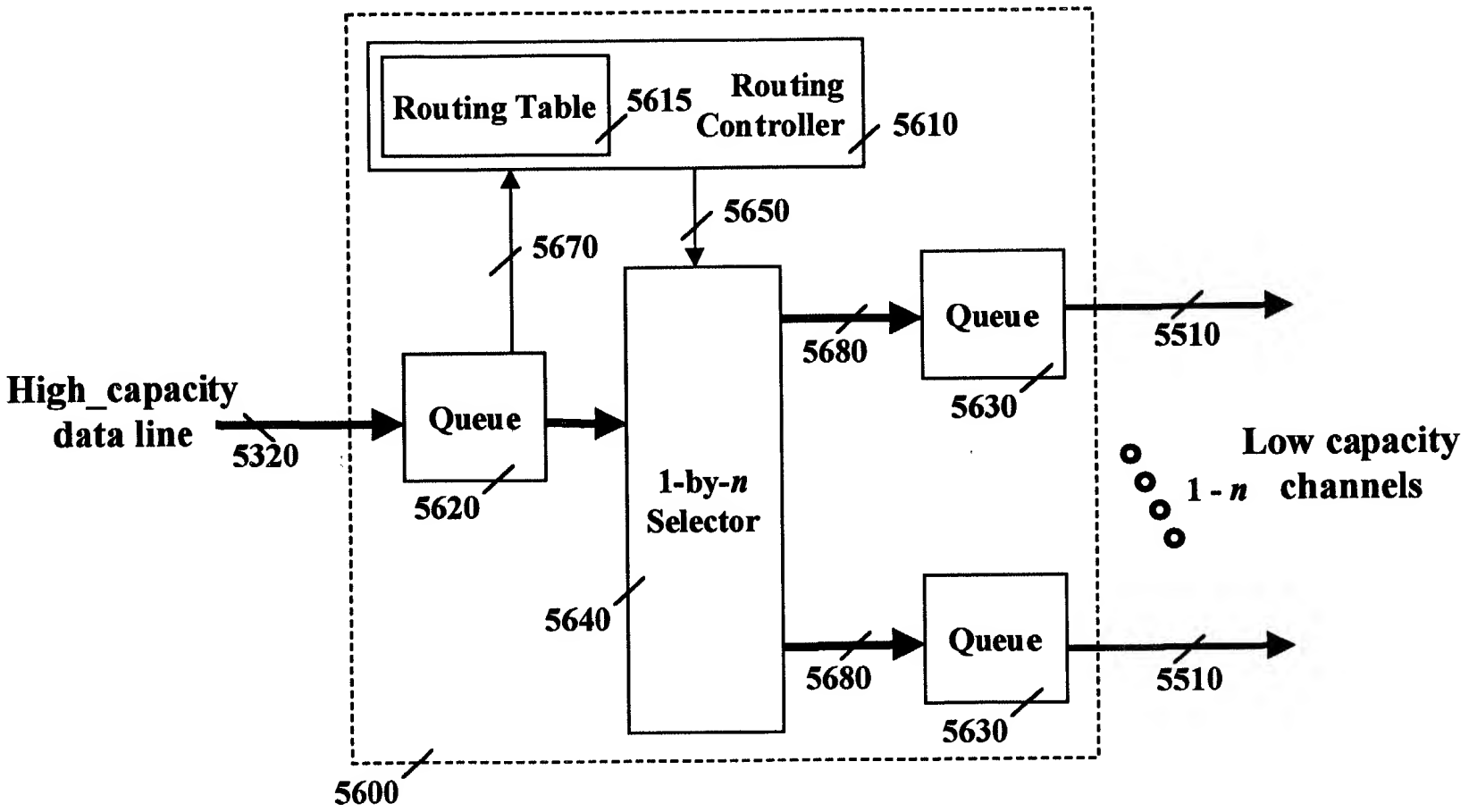

Routing Table
5615
Routing Controller
5610
5650
5670
High_capacity data line
5320
Queue
5620
1-by-n Selector
5640
Queue
5680
5630
5510
Low capacity
1 - n channels
Queue
5680
5630
5510
5600

FIG. 52

- $CC1\_length \cdot TF1 = CC2\_length \cdot TF2 = CC3\_length \cdot TF2$
- $TF2 = (SC1\_length / SC2\_length) \cdot TF1 = k \cdot TF1$, where the common cycles of *TF1* and *TF2* are aligned with respect to UTC.

For $k = 2$ and $c = 4$ (e.g., High_capacity=OC-192, Low_capacity=OC-48):

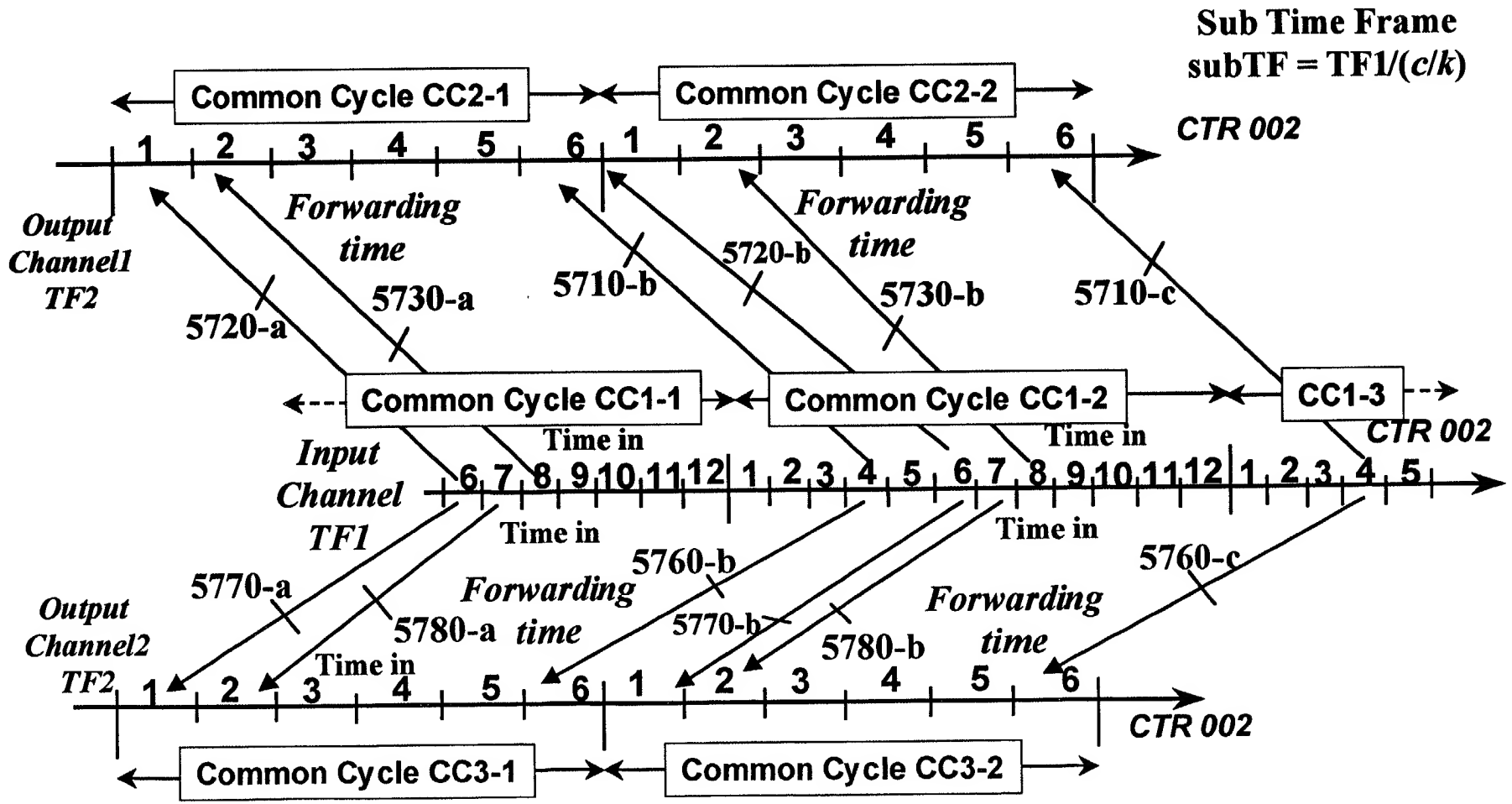


FIG. 53

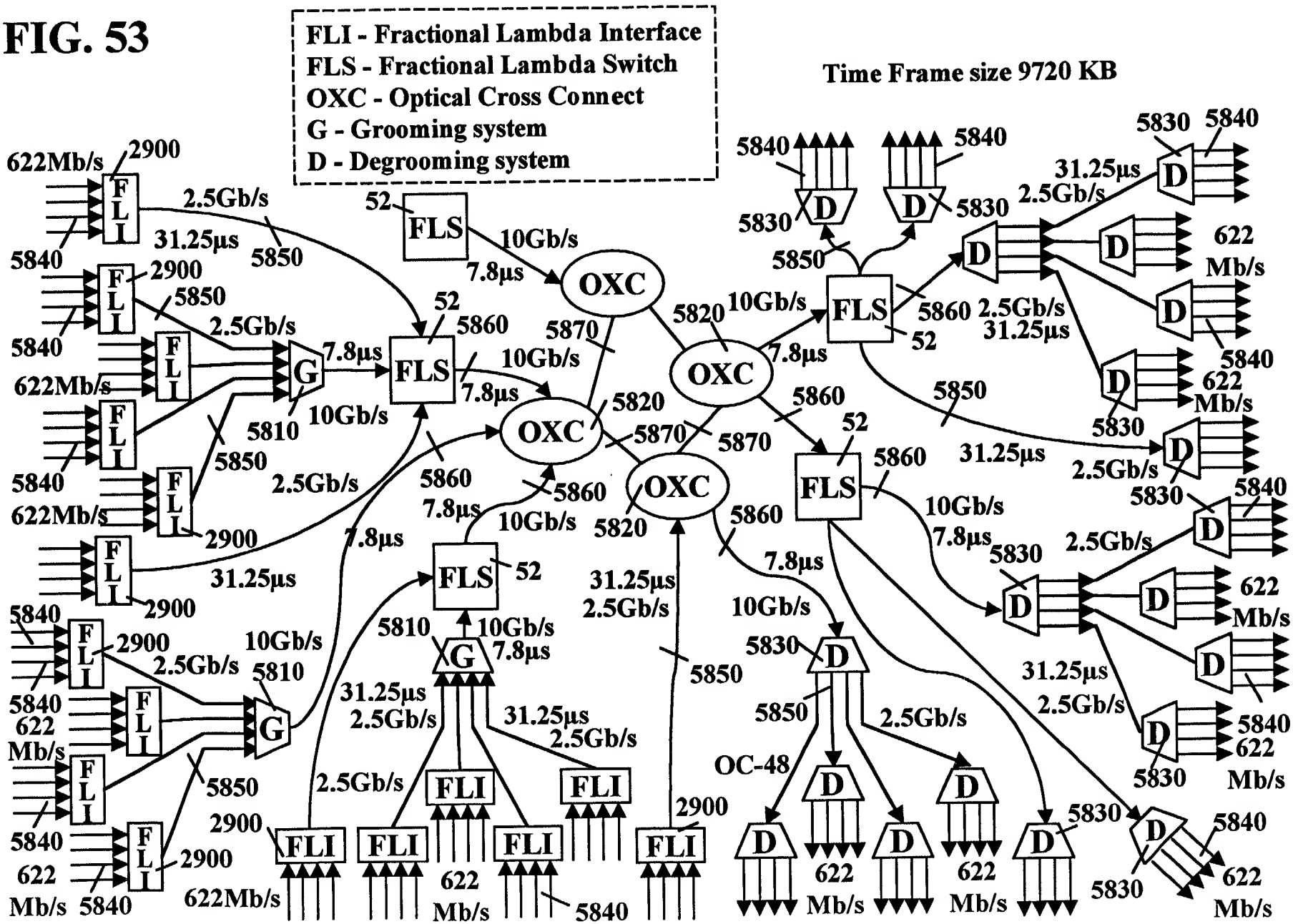

FLI - Fractional Lambda Interface
FLS - Fractional Lambda Switch
OXC - Optical Cross Connect
G - Grooming system
D - Degrooming system
Time Frame size 9720 KB
622Mb/s
2900
2.5Gb/s
31.25µs
5850
5840
5860
5870
5820
5810
5830
52
10Gb/s
7.8µs
OC-48
FLI
FLS
OXC
G
D
622 Mb/s

FIG. 54

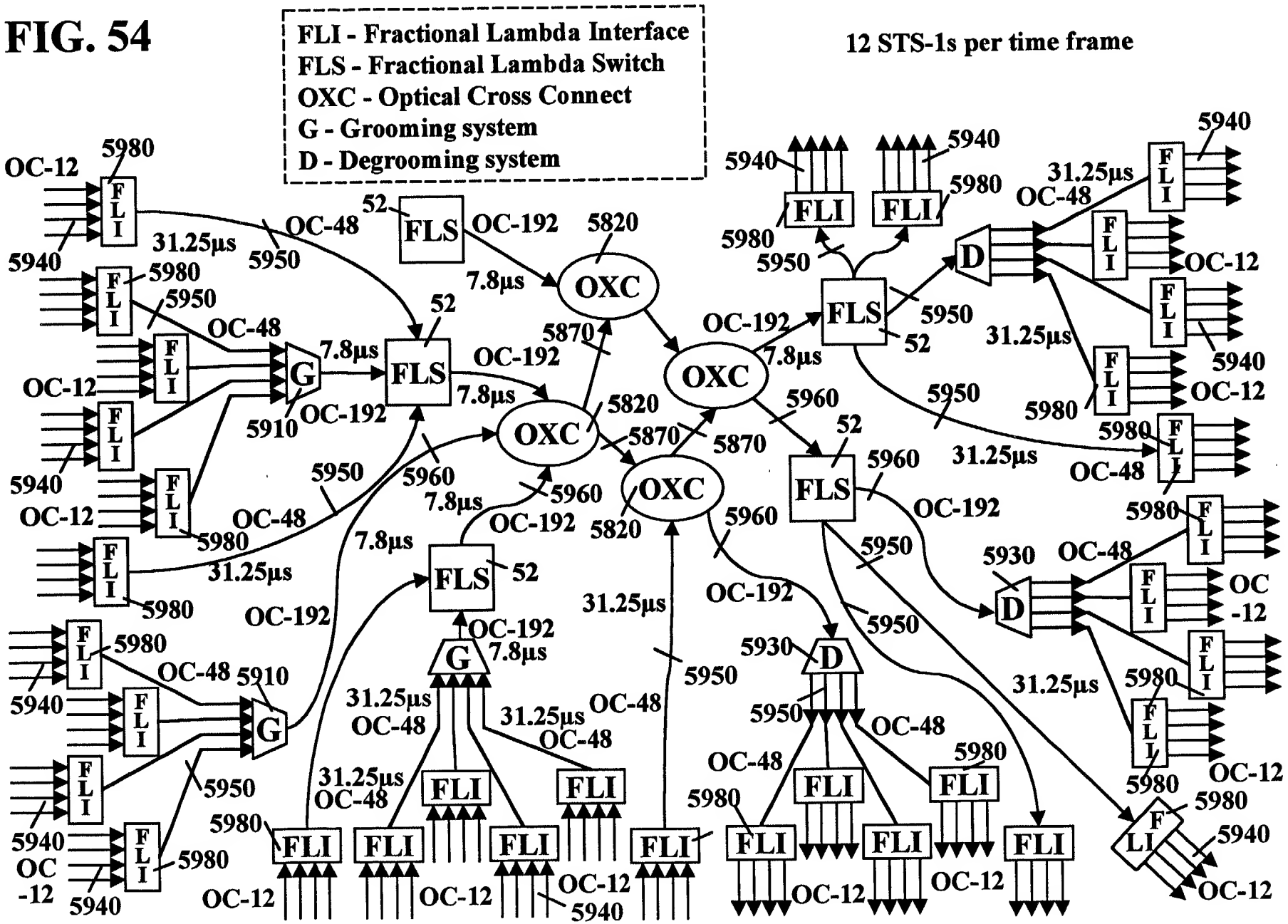

FLI - Fractional Lambda Interface
FLS - Fractional Lambda Switch
OXC - Optical Cross Connect
G - Grooming system
D - Degrooming system
12 STS-1s per time frame
OC-12
OC-48
OC-192
FLI
FLS
OXC
G
D
5980
5940
5950
5960
5910
5920
5930
5870
52
31.25µs
7.8µs

FIG. 55

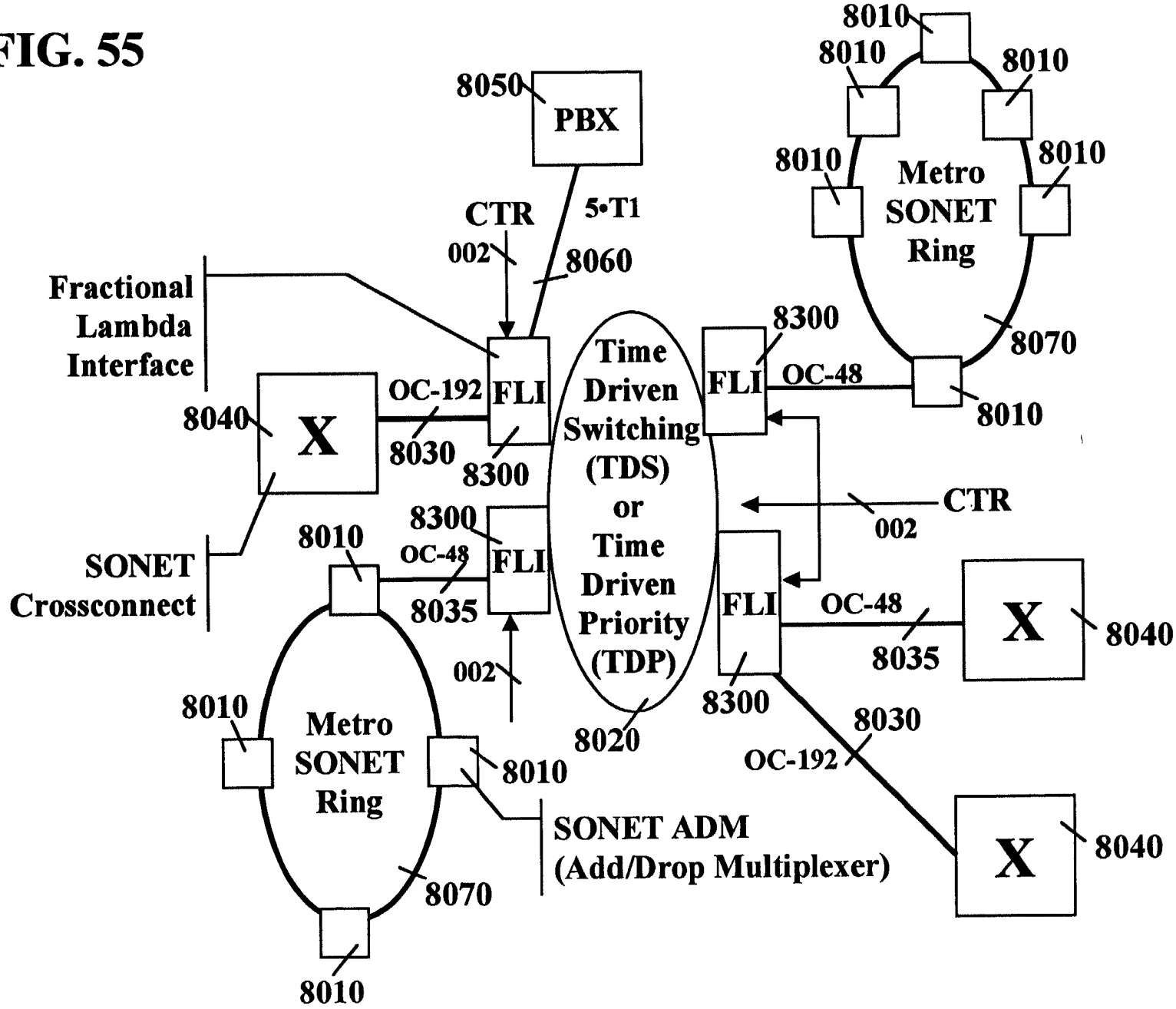

8050
PBX
5•T1
CTR
002
8060
Fractional
Lambda
Interface
8040
X
OC-192
FLI
8030
8300
Time
Driven
Switching
(TDS)
or
Time
Driven
Priority
(TDP)
8020
8010
8010
8010
8010
8010
Metro
SONET
Ring
8070
8300
FLI
OC-48
8010
CTR
002
SONET
Crossconnect
8300
FLI
OC-48
8035
8010
002
FLI
OC-48
8035
X
8040
8300
8030
OC-192
X
8040
8010
Metro
SONET
Ring
8010
8070
8010
SONET ADM
(Add/Drop Multiplexer)

FIG. 56

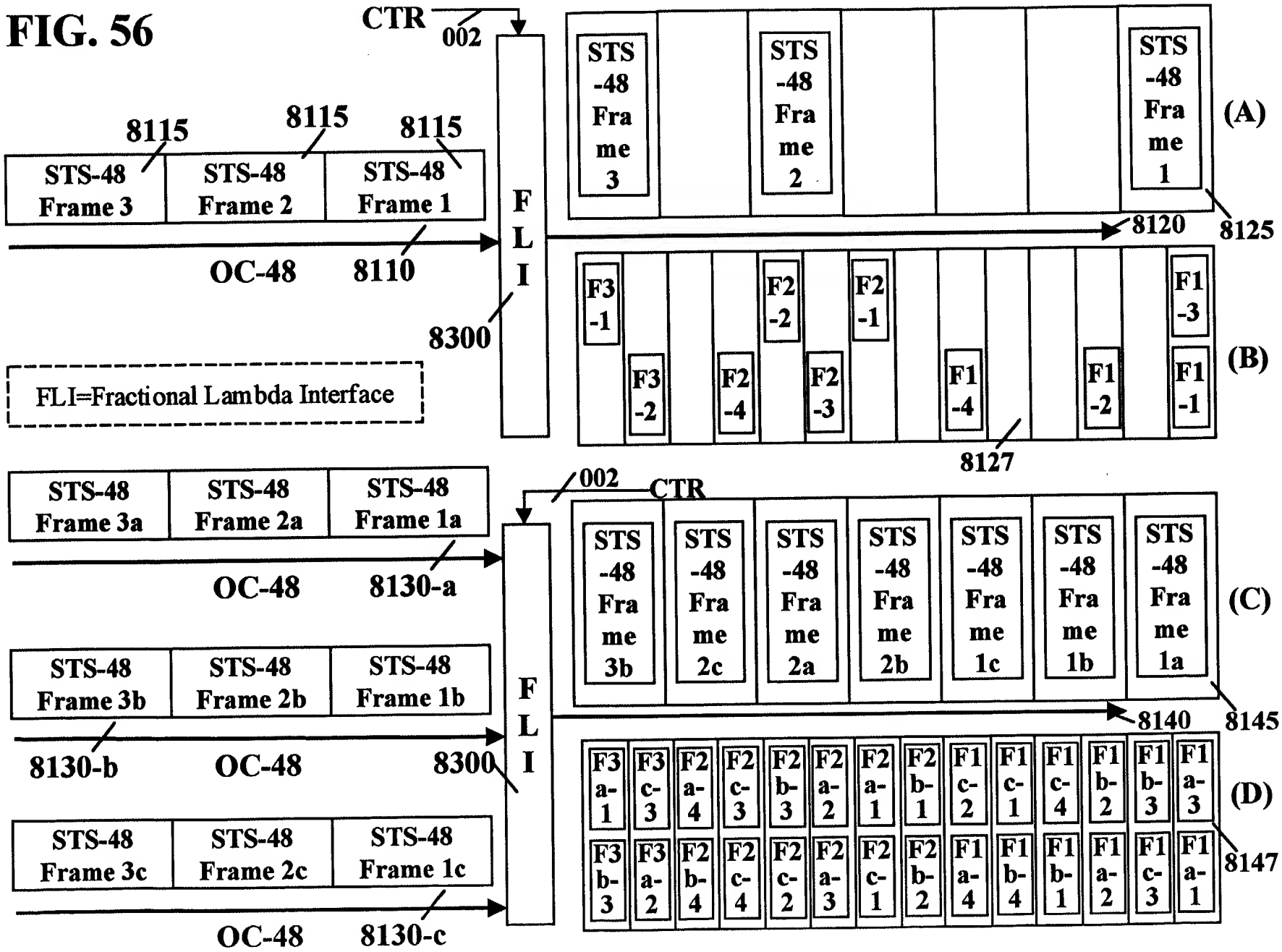


FIG. 57

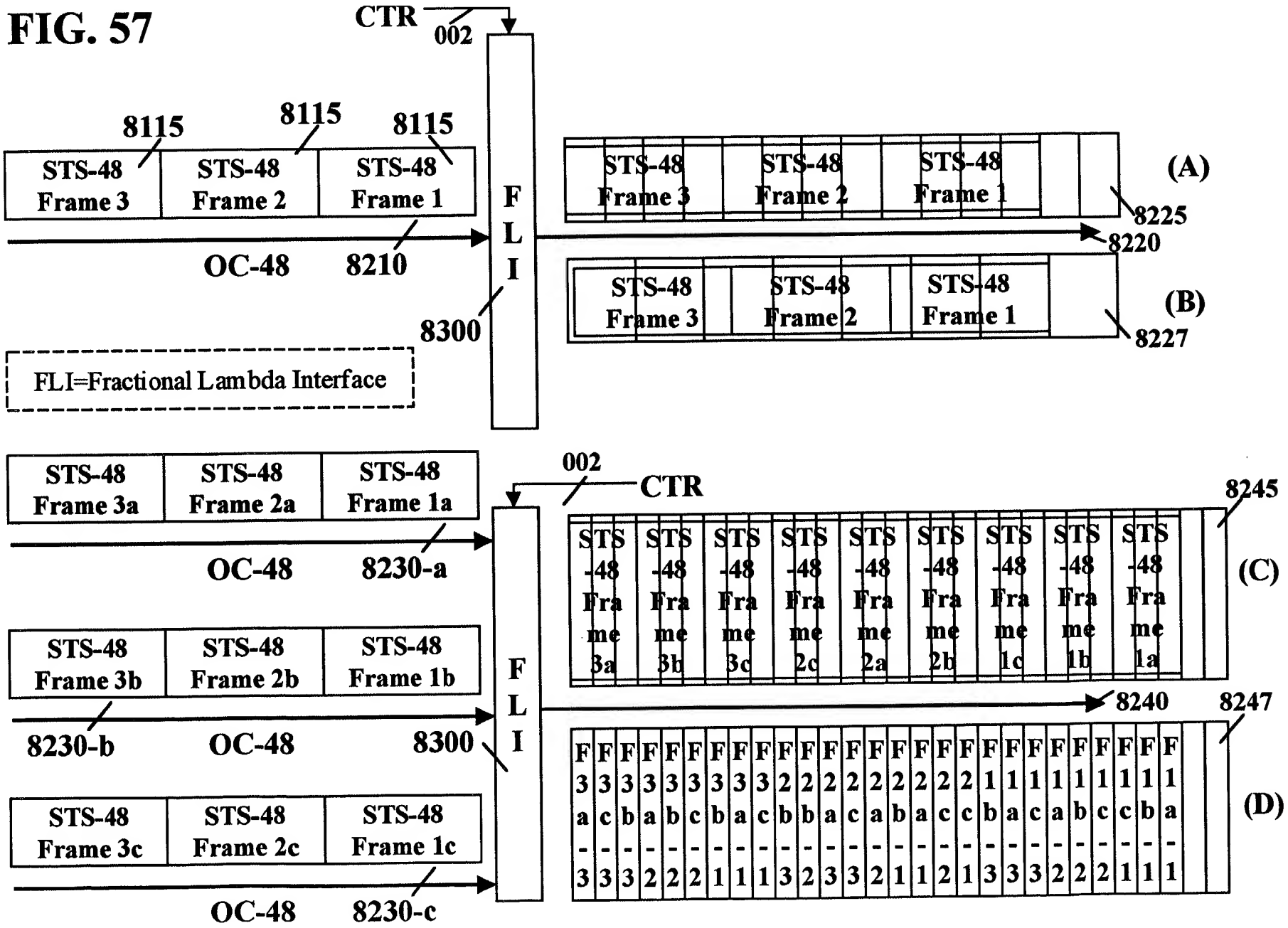

CTR
002
8115
8115
8115
STS-48 Frame 3
STS-48 Frame 2
STS-48 Frame 1
OC-48
8210
8300
FLI
FLI=Fractional Lambda Interface
STS-48 Frame 3
STS-48 Frame 2
STS-48 Frame 1
(A)
8225
8220
STS-48 Frame 3
STS-48 Frame 2
STS-48 Frame 1
(B)
8227
STS-48 Frame 3a
STS-48 Frame 2a
STS-48 Frame 1a
OC-48
8230-a
002
CTR
8245
STS-48 Frame 3a
STS-48 Frame 3b
STS-48 Frame 3c
STS-48 Frame 2c
STS-48 Frame 2a
STS-48 Frame 2b
STS-48 Frame 1c
STS-48 Frame 1b
STS-48 Frame 1a
(C)
8240
8247
STS-48 Frame 3b
STS-48 Frame 2b
STS-48 Frame 1b
8230-b
OC-48
8300
FLI
F3a-3
F3c-3
F3b-3
F3a-2
F3b-2
F3c-2
F3b-1
F3a-1
F2c-3
F2b-2
F2a-3
F2c-3
F2a-2
F2b-1
F2a-1
F2c-2
F2c-1
F1b-3
F1a-3
F1c-3
F1a-2
F1b-2
F1c-2
F1c-1
F1b-1
F1a-1
(D)
STS-48 Frame 3c
STS-48 Frame 2c
STS-48 Frame 1c
OC-48
8230-c

FIG. 58

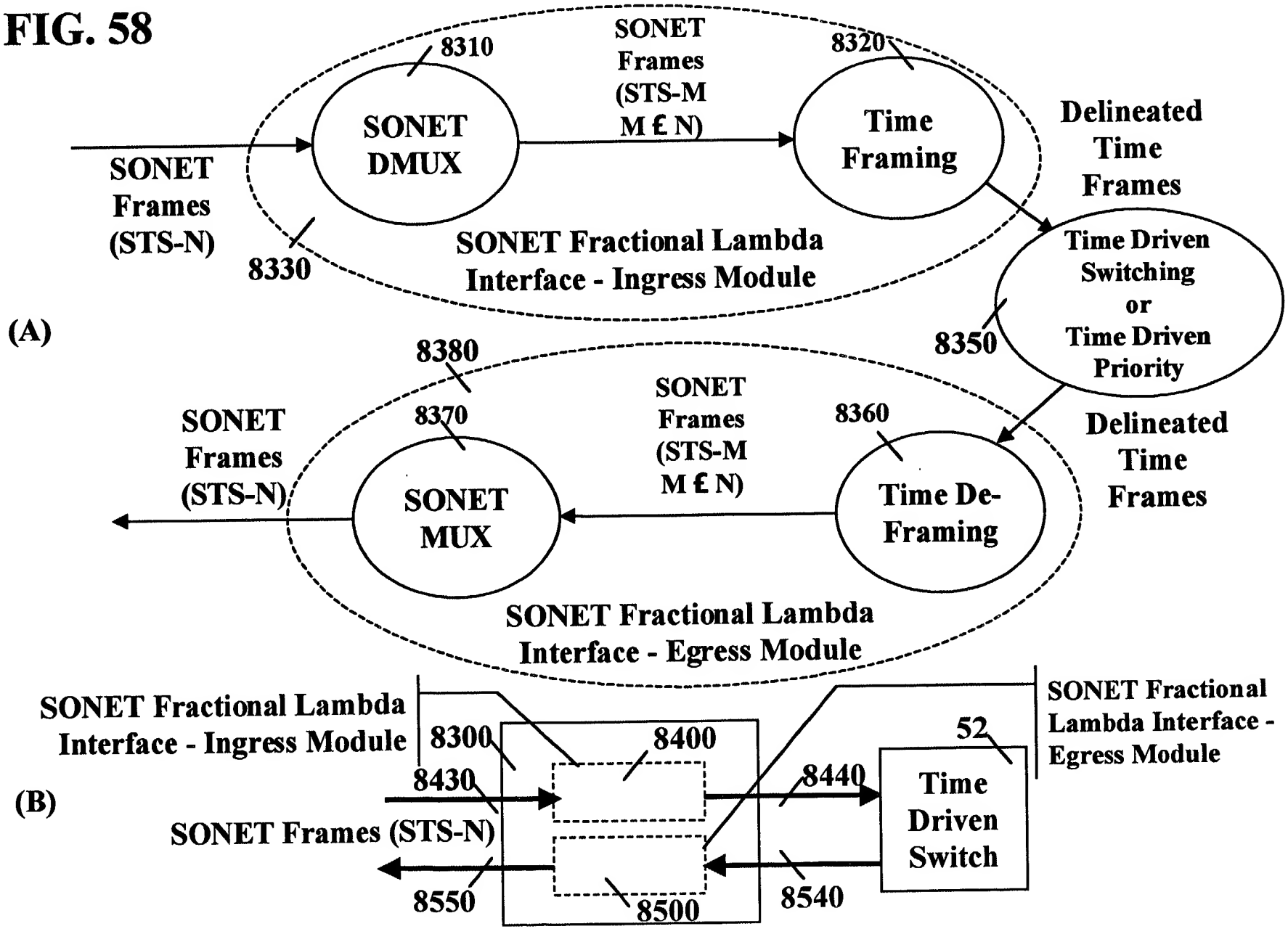

SONET Frames (STS-N)
8310
SONET DMUX
SONET Frames (STS-M M ≤ N)
8320
Time Framing
Delineated Time Frames
8330
SONET Fractional Lambda Interface - Ingress Module
(A)
8350
Time Driven Switching or Time Driven Priority
8380
SONET Frames (STS-N)
8370
SONET MUX
SONET Frames (STS-M M ≤ N)
8360
Time De-Framing
Delineated Time Frames
SONET Fractional Lambda Interface - Egress Module
SONET Fractional Lambda Interface - Ingress Module
8300
8400
52
SONET Fractional Lambda Interface - Egress Module
(B)
8430
8440
Time Driven Switch
SONET Frames (STS-N)
8550
8500
8540

FIG. 59

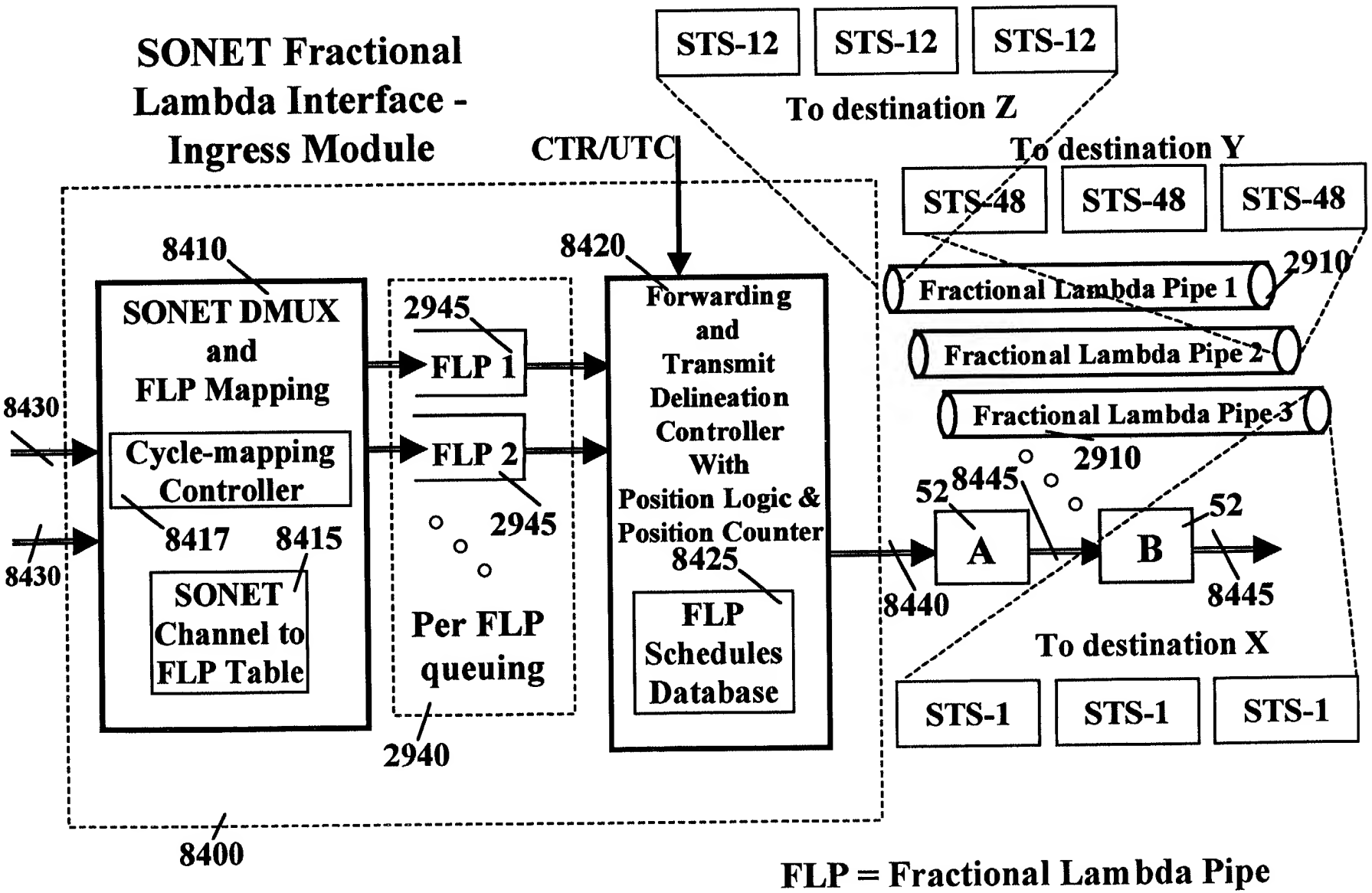
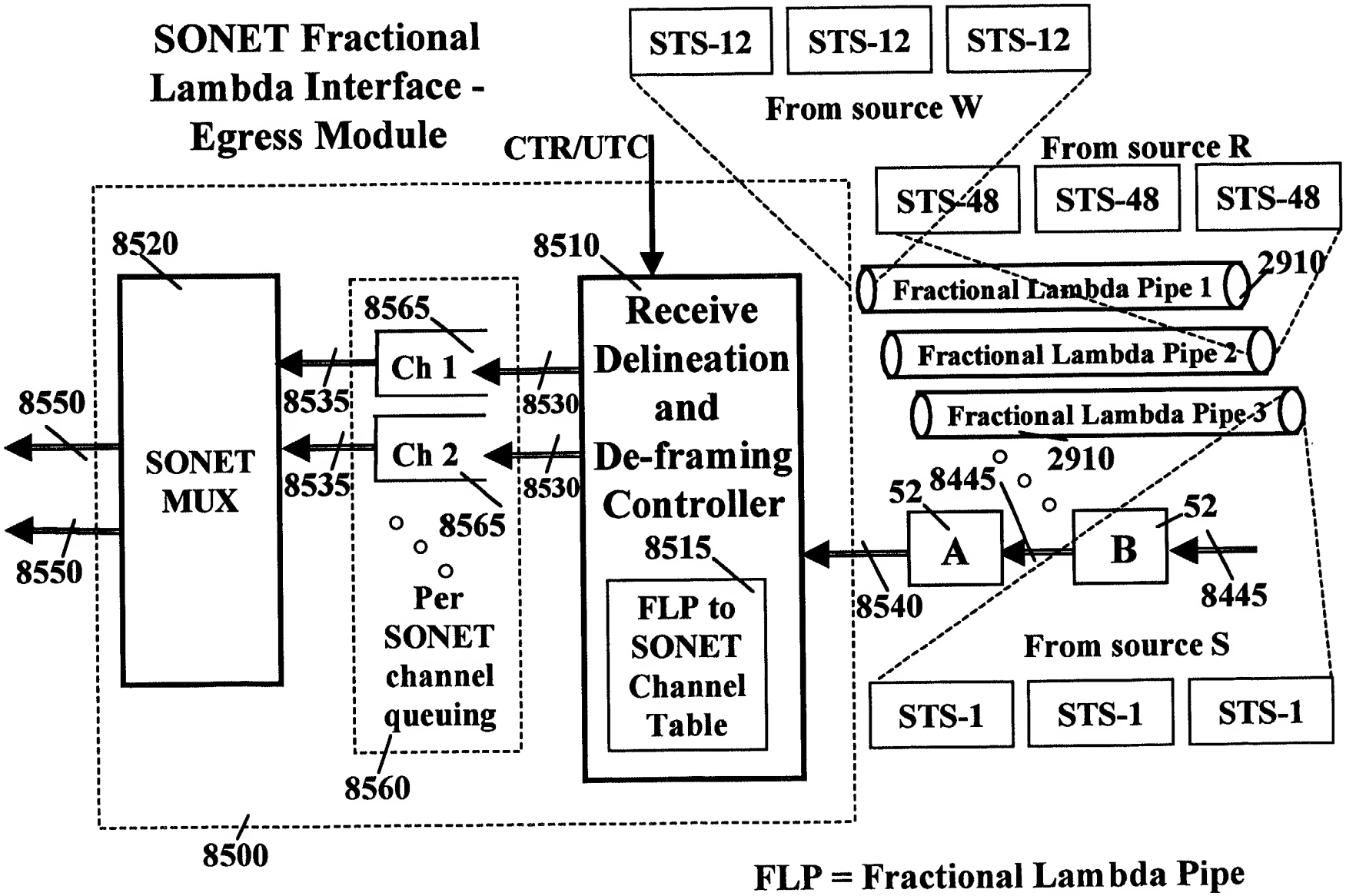


FIG. 60



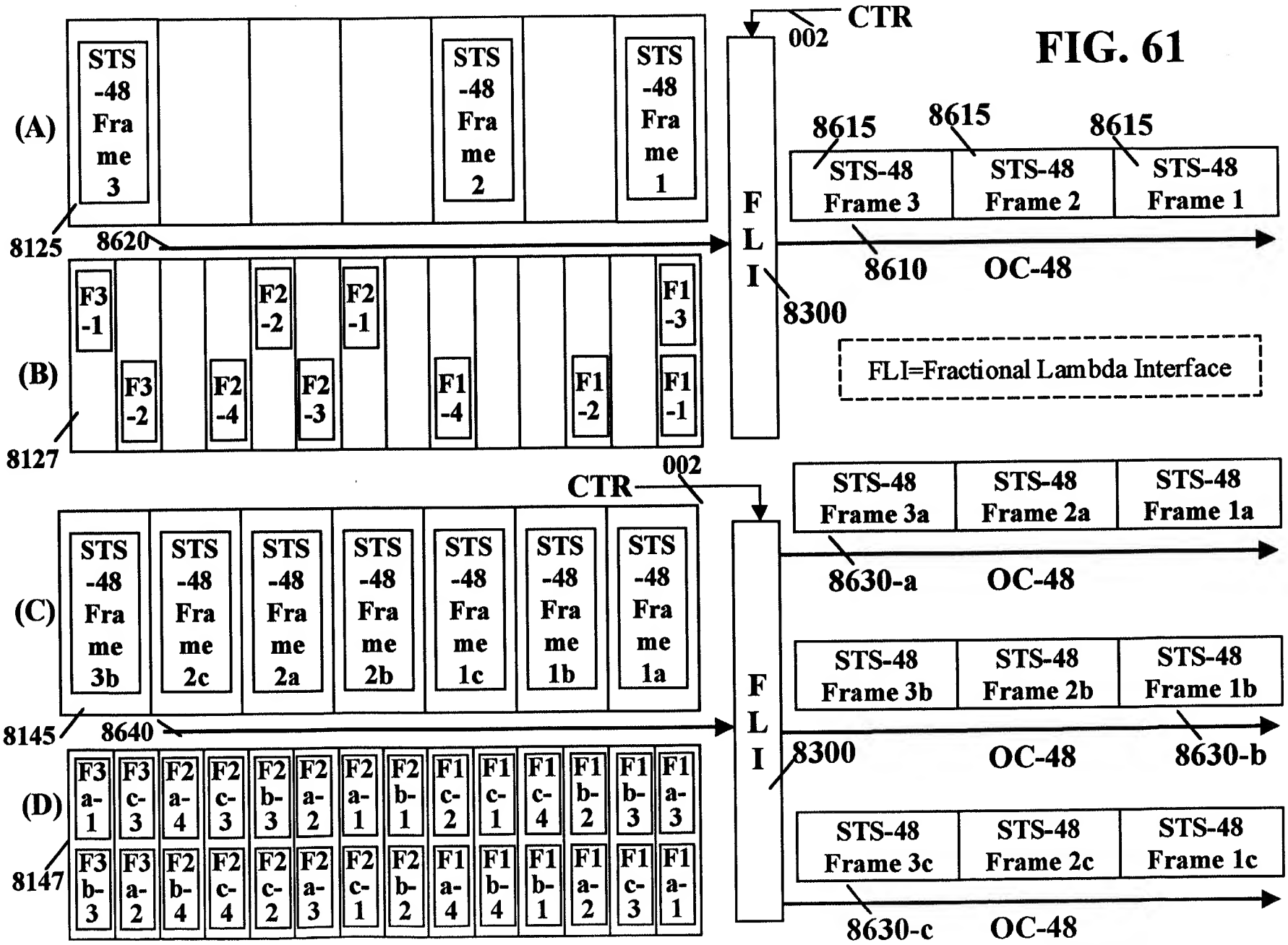

(A)
STS -48 Fra me 3
STS -48 Fra me 2
STS -48 Fra me 1
8125
8620
(B)
F3 -1
F3 -2
F2 -4
F2 -2
F2 -3
F2 -1
F1 -4
F1 -2
F1 -3
F1 -1
8127
CTR
002
F L I
8300
8615
8615
8615
STS-48 Frame 3
STS-48 Frame 2
STS-48 Frame 1
8610
OC-48
FLI=Fractional Lambda Interface
(C)
STS -48 Fra me 3b
STS -48 Fra me 2c
STS -48 Fra me 2a
STS -48 Fra me 2b
STS -48 Fra me 1c
STS -48 Fra me 1b
STS -48 Fra me 1a
8145
8640
CTR
002
STS-48 Frame 3a
STS-48 Frame 2a
STS-48 Frame 1a
8630-a
OC-48
STS-48 Frame 3b
STS-48 Frame 2b
STS-48 Frame 1b
8630-b
(D)
F3 a-1
F3 c-3
F2 a-4
F2 c-3
F2 b-3
F2 a-2
F2 a-1
F2 b-1
F1 c-2
F1 c-1
F1 c-4
F1 b-2
F1 b-3
F1 a-3
F3 b-3
F3 a-2
F2 b-4
F2 c-4
F2 c-2
F2 a-3
F2 c-1
F2 b-2
F1 a-4
F1 b-4
F1 b-1
F1 a-2
F1 c-3
F1 a-1
8147
STS-48 Frame 3c
STS-48 Frame 2c
STS-48 Frame 1c
8630-c
OC-48

FIG. 61

FIG. 62

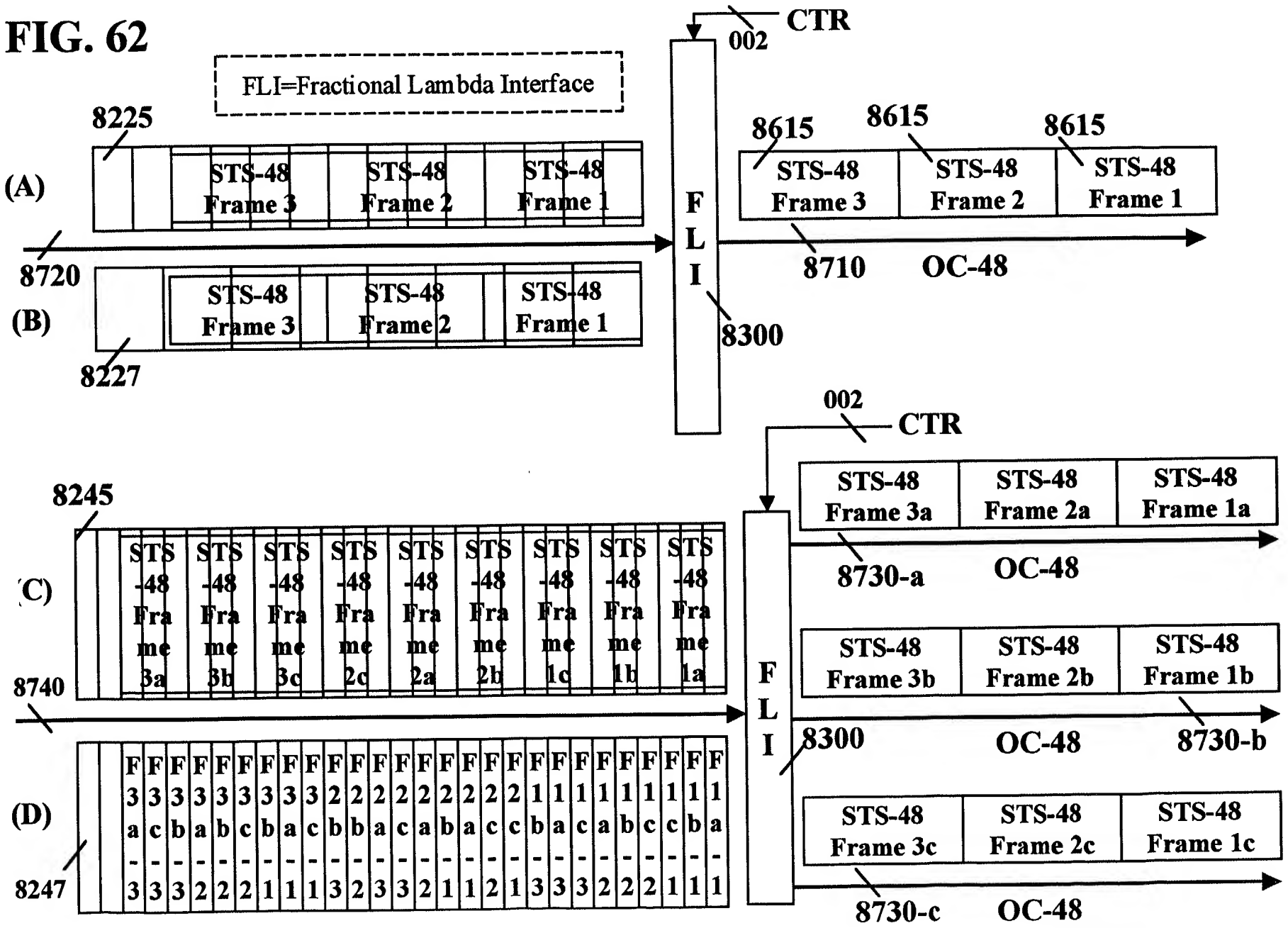

FLI=Fractional Lambda Interface
CTR
002
8225
(A)
STS-48 Frame 3
STS-48 Frame 2
STS-48 Frame 1
8720
(B)
8227
F L I
8300
8615
8615
8615
8710
OC-48
002
CTR
8245
(C)
8740
STS -48 Fra me 3a
STS -48 Fra me 3b
STS -48 Fra me 3c
STS -48 Fra me 2c
STS -48 Fra me 2a
STS -48 Fra me 2b
STS -48 Fra me 1c
STS -48 Fra me 1b
STS -48 Fra me 1a
(D)
8247
STS-48 Frame 3a
STS-48 Frame 2a
STS-48 Frame 1a
8730-a
OC-48
STS-48 Frame 3b
STS-48 Frame 2b
STS-48 Frame 1b
8730-b
STS-48 Frame 3c
STS-48 Frame 2c
STS-48 Frame 1c
8730-c

FIG. 63

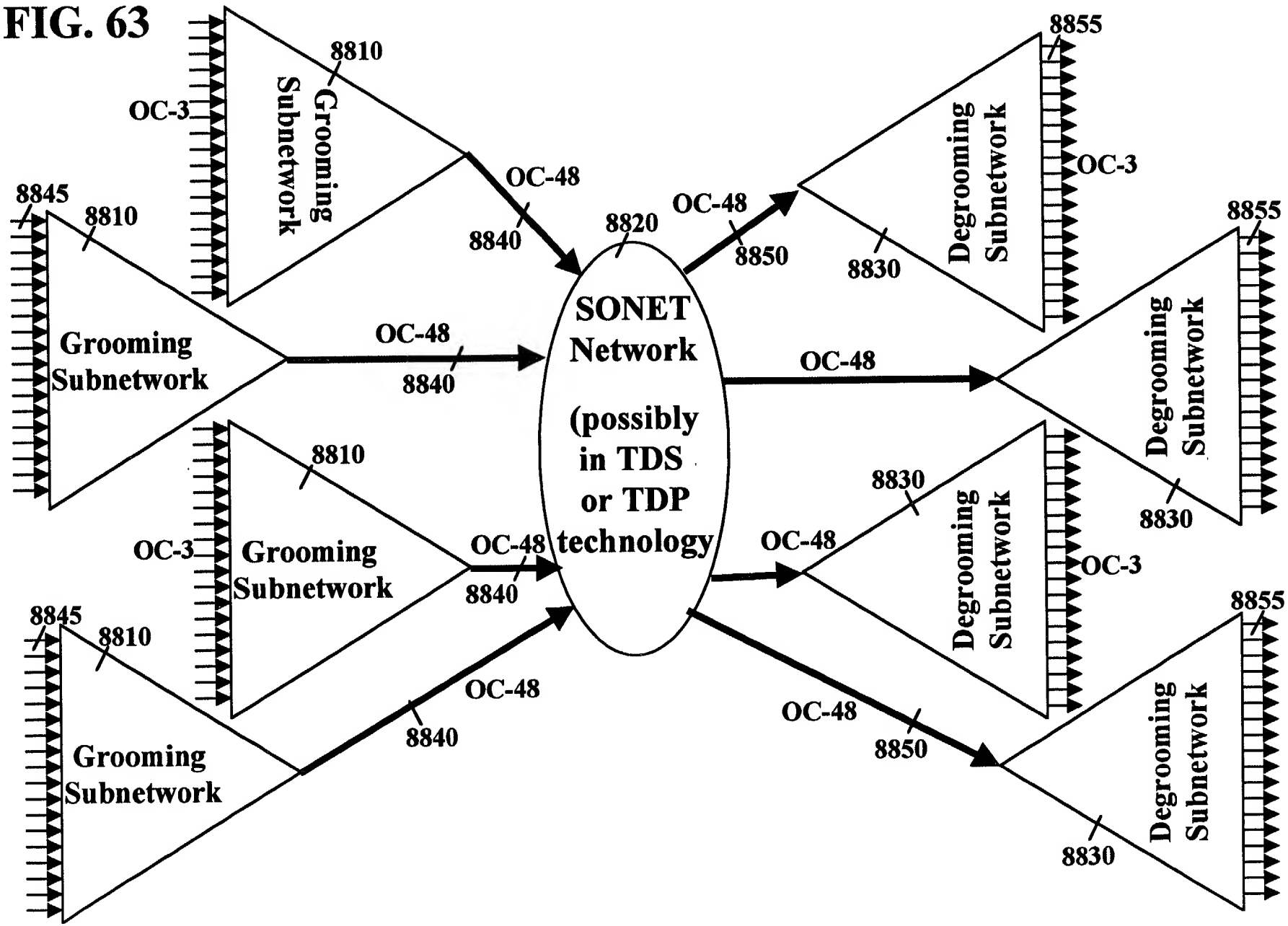

8810
OC-3
Grooming Subnetwork
OC-48
8840
8845
8810
Grooming Subnetwork
OC-48
8840
8820
SONET Network
(possibly in TDS or TDP technology
OC-48
8850
8830
Degrooming Subnetwork
8855
OC-3
8855
Degrooming Subnetwork
OC-48
8830
8810
OC-3
Grooming Subnetwork
OC-48
8840
8830
Degrooming Subnetwork
OC-48
OC-3
8845
8810
Grooming Subnetwork
OC-48
8840
OC-48
8850
8855
Degrooming Subnetwork
8830

FIG. 64

- SONET - synchronous optical network
- Multiplexing method: byte interleaving
- Signal hierarchy: OC-N (STS-N)
  - STS-N rate: N*51.84 Mb/s
  - Frame format: 9 rows by 90*N columns
    - capacity: N*810 bytes in 125 microsecond.
    - overhead: N*27 bytes
    - payload: N*783 bytes

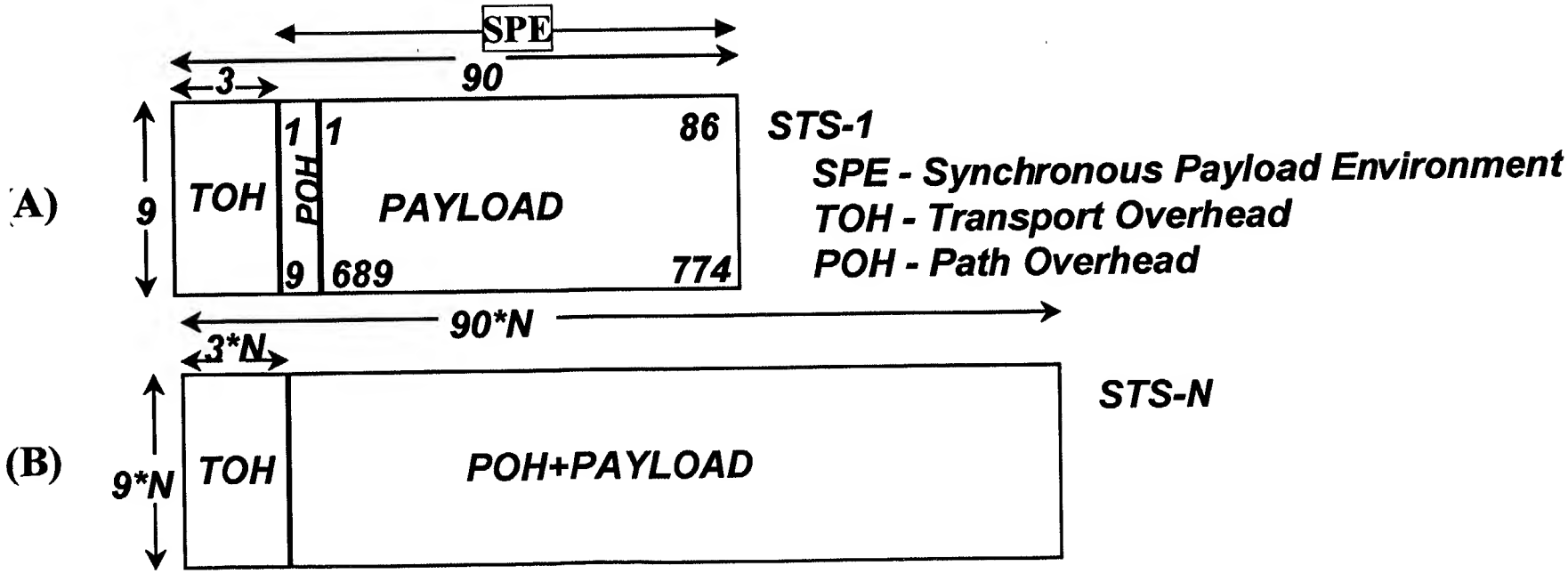
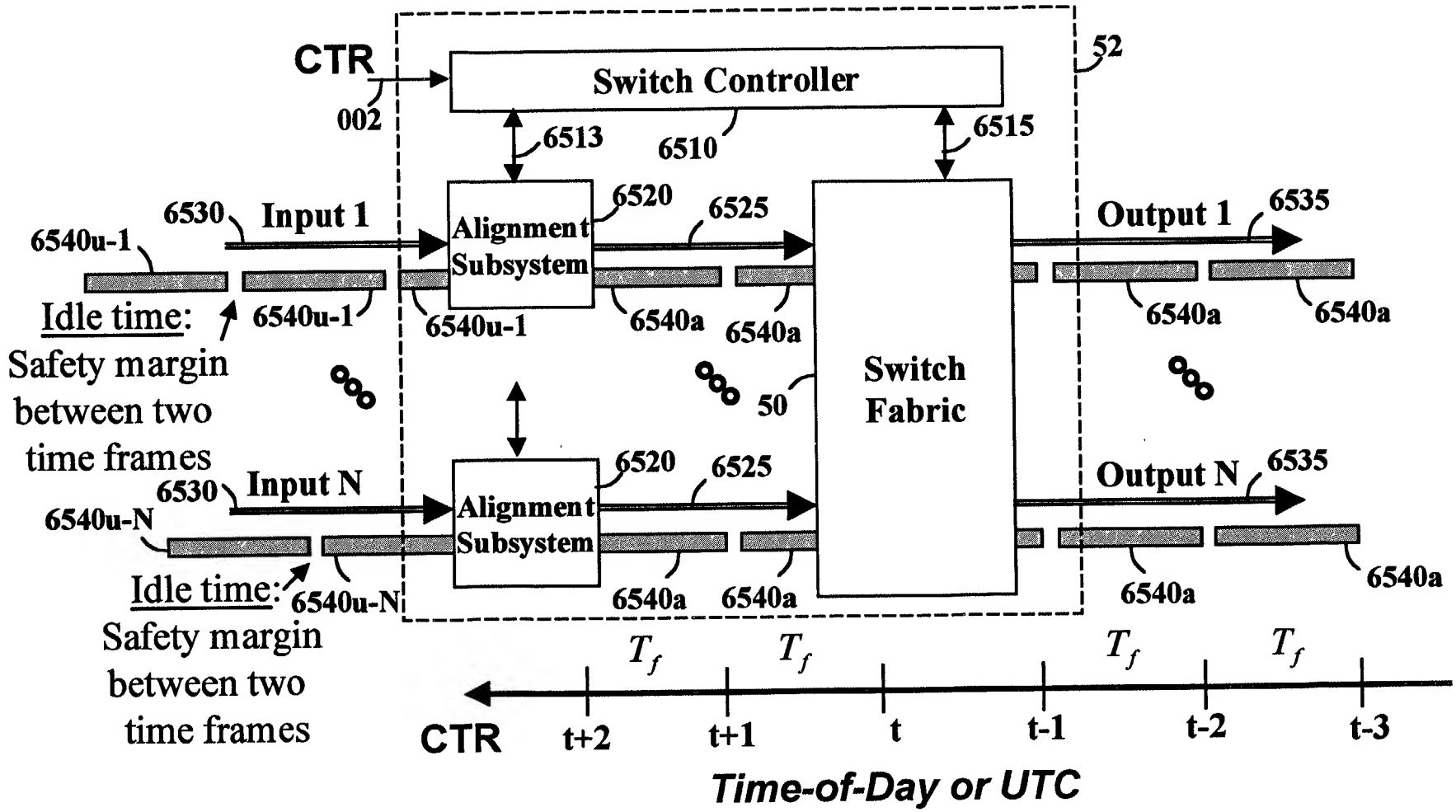


FIG. 65


CTR
002
Switch Controller
52
6513
6510
6515
6530
Input 1
6520
6525
Alignment Subsystem
Output 1
6535
6540u-1
Idle time:
Safety margin between two time frames
6540u-1
6540u-1
6540a
6540a
6540a
6540a
Switch Fabric
50
6530
Input N
6520
6525
Alignment Subsystem
Output N
6535
6540u-N
Idle time:
Safety margin between two time frames
6540u-N
6540a
6540a
6540a
6540a
T_f
T_f
T_f
T_f
CTR
t+2
t+1
t
t-1
t-2
t-3
Time-of-Day or UTC
6540
T_f : Time frame
: Time frame payload – with a predefined number of data units